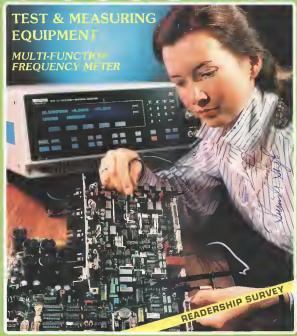


152

electronics



Publisher: C.R. Chandarana
Editor: Surendra Iyer
Technical Editor : Ashok Dongre
Circulation, J Dhas
Advertising: B.M. Mehte

Production: C.N. Mithagari

Address:

ELEKTOR ELECTRONICS PVT. LTD. 52, C Proctor Road, Bombay-400 007 INDIA Telax. (011) 76661 ELEK IN

Ovarseas editions: Elektor Electronics

real West Road, Stensford TWS SEW U.K. Editor Lan Beymann Pulitron Publicacosa Tecnicas Lida Avignange 1100 9° ender CEP01040 Seo Panio — Brezil Editor Jrillene Sersell

Elektor eerl Ronte Notionale La Sean, 8 P 53 592270 88 Meni - France

Editore OR & Mayer, Elektor Verlag GmbH erfeld-StraBe 25 100 Auchen - Wes

Editor E J A Kimmynisauti Kere-skakr 14 46673 Voule -- Athens -- Greece Editor E Xauthoules Elektuur B.V.

Poter Trackpoelstroot 2 4 6191 VK Beek — the Nerheslands Editor P E L Kerminiskere Patretta & Bento kda R D Estelanie 32 4º 1000 Lisbos - Portnosi & Editor Jurge Gancolves

Incolet S 4 Plaza Republica Ecnador 2 28016 Madrid-Spain Editor: A M Ferrer

lu Pert Kedhern Heldings PTY Lid Cor Fex Valley Road & Kiegle Street Withounge NSW 2076 - Anstrolie Editor Roger Herrison

Electronic Pisse A5 182 11 Denderyd - Sweeden Editor Bill Cedium

The Circrets are for domestic use only. The subsension of demans of ermeles of Elektor India implies of damgins of enteres of claims industributes of the publisher in eller and hunslave the two and designed and to rise the contents in other Etcklor publications and ectivities. The publishers cannot

gnmanter to secure only material animated to them. All distwings photographs printed excust boards and esticles published in Elektor India are copyright and may not be storodrond or imitated in whate or next well-out prior wrater permettion of the problems Parent protection may asset in repaid of circums

devices, components as described in this measure The publishess do not eccept resonability for fashing to identify such patent or other presection



Printed at : Trupti Offset: Bombay - 400 013

Ph 4923261, 4921354

Copyright @ 1987 Elektuur B V

CONTENTS

Electronics Technology

flathematical Principles of Natural Philosophy by Isaac Newton	1.32
nformation theory and Encryption	1.38
lectronic & Magnetic Quantities	1.36
lumbers and the machine	1.48
Whet is Wett?	1.56

Projects

LCD VU Meter	1.22
Stereo pan pot	1.34
Multi-function Frequency Meter	1.41
Multiple voltage supply	1.48
Top-of-the-range preemplifier (3)	1.49

Test & Measuring Equipment

Dual-trace Oscilloscopes (A) Part 1	1.2
Elektor india Test Equipment	1.5

Readership Survery

le ask all our readers to take a	
w minutes to answer the 20 questions	
our Readership Survey	1.39

Information

Electronic News	1.17
Telecommunication News	1.20
New Products	1.62
Panders Caminas	1 72

Guide lines

Switch board	1.6
Index of adverticers	17

Selex-30

Cherging/Discharging Current Meter	1.58
D A	1 60

TELECOMMUNICATION NEWS • TELECO

INDO-ARAB CABLE

The India-United Arab Emirates submarine cable communication in the India of India of

seconds. The India-UAE cable system is a joint international communication project with costs being shared by both the countries. The system uses a coaxial cable connecting Bombay and Fujariah. It provides 1380, two-way grade circuits. The total length of the cable is 1984 km.

The international telecommunication traffic stream between India and the UAE is one of the largest next only to the UK and the USA. About 250,000 Indians live in the UAE. The Videsh Sanchar Nigam Limited (VSNL) formerly known as the Overseas Communication Service of India and tha Emiratas Telecommunicationa Corporation Ltd. hava iointly plannad and

project.
While satellite
communication was
already providing
reliable wide band
communication
capacities, there were
still certain drawbacks
like the propagation
delay of the order of 250
milliseconds on each
satellite hop and the
susceptibility of
microwave satellite

implemented tha

submarine cabla

transmission to external interference. It is an international practice to provide alternate transmission medium in case of failure of any one systam. This view was sharad by both the governments and the submarine cable project work took birth in 1981.

By this time, optical fibres emerged as a contender for the conventional coaxial cables, But, optical communication technology was proven only on short routes. Further, costs involved were too high for the required circuit capacity, Hence, conventional technology was chosen for the India-UAE link. A memorandum of understanding was signed by both the countries in 1984. The total cost of the project is Rs. 80 crores. When this project was put for global tender Japan and USA did not respond as manufacturers in those countries were no longer producing coppar trunk submarine cables. They switched over to fibre optic cables, Standard Telephone and Cabla company of the UK was given the contract for supply, installation and commissioning of the system and in 13 months the project was completed. The light weight unarmoured cable is used where the depth of the sea exceeds 800 metres. The off-shore and fishing activities are more likely in coastal area and the cable laid in such regions face hazards. Outside sheath of the cable is armoured with

high tensile steel wires for protection. Double armoured cables are used in shallow waters. In some portions, the cable is laid as below as 3550 metres in the Arabian Sea Initially, investigation of sea and bed profile. sub-bottom stratum. cable fault histories. water temperature end its quality offshore and fishing activities etc. had been carried out to determine the basic route. During the survey prior to the laying of the cable, sea bottom profile, topography of sea bed, quality and temperature of sea water, seismic activity. under current, reefs and wrecks, marine plantation and navigational data were collected

As a result of advances in micro-electronics and computers which are being integrated with communication systems, the capital cost of submarine cable circuit has come down drastically. The capital cost per channel kilometre in 1956 was 580 dollars. It came down to 70 dollars in 1965 and in 1997, it cost just 22 dollars.

The first fibre optic submarine cable will be commissioned across the Atlantic in 1988, it will have a capacity of 40,000 circuits. Instead of a repeater for every 25 km in a copper cable, optical fibre requires repeater once every 150 to 200 km.

The first cable laid in 1956 was retired last year after 30 years of service because there was no need to maintain it for the meagre 36 circuits provided by it. Satellites require

seven years and next generation satellite may last for 14 years. Satellites, dependable during peace, can bacome a security risk during global wars. In the 130 years of submarine cable history, no cable was damaged due to enemy action. Thay could not be tappad unlika satellite communication. Howaver, satellites have no rival for providing communication to inaccessible and

replacement every

inaccessible and inhospitable places. India had its first submarine cable for telecommunications in 1869-70 when the Eastern Telegraph Company of UK laid a cable connecting Suez-Aden-Bombay.

The first submarine

cable of India was commissioned in 1981, linking Madras and Kuala Lumpur, covering a distance of 2509 km. The India-UAE link is the second submarine cable of the country.

The signals in the submarine cables have to be amplified at ragular intervals and this is achieved by providing submerged repeaters at suitable intervals. The nominal repaatar spacing for India-UAE cable is 13.5 km. The cable contains 147 repeaters each giving a gain of 48 dBs. Each repeater boosts the received power by 60,000 times or in other words the journey of a speech from Bombay to

UAE is enhanced by 8.82 million times. A big ship, specially built for cable laying, called "Venture" was used for deep waters and a small cable

TELECOMMUNICATION NEWS • TELECO

ship"Galaxie" was used near the shore. When the cable was brought to the shore near Bombay with the help of e rope tied to a boet. it was kept floeting by maans of inflated balloons. The submarine cable was joined to the lend cabla in Bombay Back Bay . After the joint was accomplished the balloons were defiated and the cable slowly sunk to the sea bed.

Though the first under water cables for telephony were laid in 1891, the real long distance under water telephony began with the commissioning of the transatiantic telephone cable in 1956. The invention of insulating polyethylene and polypropylene made the cables more resistant to moisture. This ensured a life expectancy of over 25 vears for the cables.

There are now 135 telephone cables on the ocean beds around the world. All of them use copper conductors in coaxial formation. The number of telephone circuits which were 36 in 1956 have now gone upto 2000 . In some shortar cablas, even 4.000 circuits ara provided.

The real competition to submarine cables come from satallites. The present generation of satellites provida 25,000 to 40,000 circuits while tha naxt generation, Intelset VI, expected in 1989, will have 120,000 circuits. The cable technology still has e future as its capacity too is increasing rapidly.

UK START FOR NEW ATLANTIC LINK Work in Britain on

laying the world's first transatlantic optical fibre cable-code named TAT8-has started At Wide-mouth Bay in Cornwall, the UK shore end of this £220 million undersea system is being installed by staff from British Telecom International.

The shore end of the cable will be floated ashore from a cableship secured, and sunk into position by divers. The cableship will then move off to lay the remainder of the first 12 km of the UK section of the cable Next spring, the main 520 km UK section of TAT8 will be laid by BTI's cableship CS Alert. She will carry out the laving with the aid of BTI's remotelycontrolled plough. which will bury the cable beneath the seabed to protect it from damage by ships' anchors and trawling. The UK section of the cable will be connected by a further 20 km link to a special junction device on the ocean floor, 540 km south of Widemouth Bay, This will join the UK cable to a similar section from France, connecting both to the main 5,000 km span of the cable to the LISA

When TAT8 comes into service next summer, it will have the potential capacity to carry the equivalent of 40,000 simultaneous telephone calls, or their equivalent in data, text, facsimile, graphics, or TV pictures. TAT8 is the eighth telephone cable to span the Atlantic between Europe and the United States, its capacity is three times greater than

that of all the others

together.

The new cable will form an important part of a new global communications network, which will offer customers faster connections, and improved quality links at lower cost. A whole range of additional services will be made possible with the new digital links.

A second transatlantic optical fibre cable is being planned to come into service in 1991. Called TAT9, the \$400 million system will have landing points in Britain. France, Spain, Canada, and the United States. The cable's main transatlantic section will have the capacity to carry 75,000 simultaneous phone calls.

The new cables will help British Telecom to meet the continuing

growth of the number of transatlantic phone calls, which has been doubling every five vears.

MERCURY SERVICE THROUGH VANDERHOFF

Mercury Communications Ltd. a wholly owned

subsidiary of Cable & Wireless PLC, has appointed Vanderhoff Business Systems to be the first distributor for the Mercury 2200 telephone service. This usas a Smart Box to connect customers to the Mercury natwork. The Mercury Smart Box is installed on the

exchange side of customers' PABX equipment. Its purpose is to work to the customer's advantage in deciding when a call can be more economically handled by Mercury and eutomatically routeing it accordingly. Mercury 2200 customers benefit from call cost savings of an average of 15% on long-distance connections and itemized billing at no

extra charge. In addition to the Mercury Smart Box. Vanderhoff are also national distributors for Mercury Paging and are undertaking the billing of air time to subscribers

Eurther information from Vanderhoff Business systems Ltd . 19 Station Approach . FLEET GU13 8QY.



LOGARITHMIC LCD VU METER



A compact, versatile AF signat level indication unit with a dynamic range of 60 dB, a dot or bar graph read-out, and a peak hold function.

Not so long ago, coloured LED bars were welcomed as the more robust and faster replacement for moving coil meters in VU (volume unit) indication units. An additional, important, advantage of the LED VU meter was that it enabled realizing the peak hold function, which is useful, if not indispensable, for determining the recording level on tapes. The major drawback of the LED based VU meter is its relatively high current consumption, which poses considerable problems in portable equipment. The VU meter described here is based on a liquid crystal display (LCD) with modest power requirements. The read out is logarithmic with a scale of 60 dB, which is adequate for the dynamic range of, for instance, a CD player. The built-in peak hold function has an option for automatic reset after approximately 2 seconds. Wire links or jumpers make it possible to select dot or bar indication, but it should be noted that the peak hold function operates in the bar mode only

The proposed LCD VU meter is composed of 2 units, namely a logarithmic amplifier and a linear LC display driver. The printed circuit boards for these have the same size to enable building a compact indication unit using a sandwich construction-see the introductory photograph and Fig. 1. The amplifier board holds 2 logarithmic amplifiers for stereo applications. Both the amplifier and the display board can, of course, also work as a separate module in applications other than that described here; the amplifier, for instance, is also suitable for driving a moving coil VU meter. which is arranged to display a linear dB scale. Similarly, the LC display board may be used as an indication unit in, say, an electronic thermometer.

The linear LC display

The circuit diagram of this part of the VU meter is given in Fig. 2. It would have been possible to use a single display driver chip with a suitable multiplexing circuit for the LCD but this would have been at the expense of the peak hold function. The inputs of the relatively expensive driver ICs are protected against overvoltages by networks D: D2-R5 and D3-D4-Rs. Selection between the various available display modes is accomplished with the aid of wire links, jumpers or a switch as summarized in Table 1. The

LCD board has only 4 inputs,

which are readily connected to the respective points on the amphifier board-Fig. 3 shows the completed sandwich construction. The linear scale of the LCD gives a read-out which is directly proportional to the input voltages applied to points L and R, varying between the voltage on the respective REF LO and REF HI input (05 and 4.5 V). The level of the supply voltage applied to the LCD board is governed by the maximum permissible supply for the LC display (6 V), and the minimum supply level for correct operation of the driver chips (8 V).

Table 1

l1	12	13	DISPLAY MODE					
L	L	х	dot mode; 18 steps					
L	Н	Х	dot mode; 9 steps					
н	L	X	ber mode; no peak value					
н	Н	L	bar mode, peak value, automatic reset					
н	H	н	bar mode; peak value, reset. tz=low					

The logarithmic amplifier

Figure 4 shows the circuit diagram of 1 of 2 identical diagram of 1 of 2 identical logarithmic amplifiers, and the power supply for the VU meter. Opanp A: raises the input signal and feeds this to a peak rectifier circuit. The logarithmic amplifier, composed of As, As and matched transistors T2 and T3, is driven with Uca, which is directly related to the amplitude of the input signal. The matched transistors are housed in an IC Type CAS346.

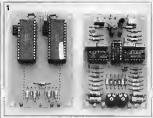


Fig. 1 The completed display driver board (I) and the logarithmic amplifier board (r) have the same size.



Fig. 3 The 2 boards can be fitted in a sandwich construction to make the VU meter as compact as possible.

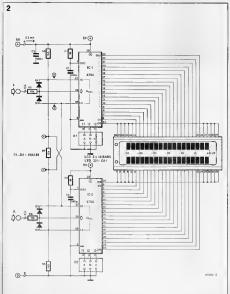


Fig. 2 Circuit diagram of the linear LCD driver.

the 4 opamps in an IC Type

LM324 The linear variation of the rectified input voltage is converted to logarithmic by means of an opamp with a feedback circuit that comprises a conventional bipolar transistor. Under certain conditions, the collector current of a bipolar transistor rises exponentially with the base-emitter voltage. Figure 5 shows how this phenomenon is exploited: the transistor forms the resistance in the negative feedback circuit of an opamp. which thus functions as an amplifier that translates its linear input signal into a logarithmic output.

The voltage transfer of this circuit is written as

$$U_0 = -\frac{kT}{q} \log_0 \frac{U_1}{\alpha I_{00} \mathbb{R}}$$
 [1]

in which α is the current amplification of transistor T, and kT/q at room temperature works out at about 26 x 10-3. The weak point of this circuit is that the term Iso is strongly temperature dependent. Figure 6 shows a slightly more complex circuit whose voltage transfer is less affected by temperature variations. The voltage transfer of this circuit is

$$U_0 = -\frac{(R_0 + R_{10})kT}{R} \log_e \frac{U_iR}{U_{roi}R_0}$$
 [2]

The factor kT/q is the same as in equation [1], while aleo has been eliminated, ensuring reasonable temperature stabthty. Compensation of kT/q was found unnecessary for the given application, since it proved to have little effect on

alaktor India (anuary 1988 1.23

the relatively low resolution of the LCD Returning to the circuit diagram of Fig. 4, operational amplifier A4 inverts the logarithmic voltage, so that the LC drivers receive a signal with the correct polarity.

The resolution of the display is fairly low at 18 bars. The logarithmic amplifier is dimensioned such that a variation of the input voltage of 3 decades results in an output voltage variatton of 0.5 to 4.5 V. This corresponds to 1.33 V per decade. The full range then corresponds to a scale of 60 dB (-50...+10 dB) as shown in Table 2 and Fig. 7. Considering that 0 dB-775 mV on C2, a dynamic range of 60 dB means that the minimum voltage for illumination of the lowest bar is 2.45 mV, which is about equal to the offset of the input voltage. It should be noted that the value of 775 mV on Ca is not related to the definition of 0 dB as 1 mW (775 mVcms) in a load of 600 Q. The drive margin of the logarithmic amplifier is ensured by feeding it from the input voltage for the 5 V regulator. ICa. Resistors Rr and Rs are dimensioned such that the output voltage of A4 can not rise above the supply level of the LCD board.

Construction and setting up The components are fitted as

per the directions in the parts list and Figs. 8 & 9. The LCD used is the type LTD-321-C01 from Mullard/Videlec. The outer 2 bars of each row of 20 on this LCD are not used in the present application. The virtually symmetrical pinning of the display, in combination with the layout of the printed circuit board, make it possible to fit the display upside down also. The contrast of the LCD is maximum when this is viewed straight, or from one side. The dlaplay is fitted either normally or reversed -but always at the copper side-depending on whether it is to be viewed from above or below. It is recommended to use terminal strips for mounting the LCD. Take note of the position indicator, which is at the left side of the LCD when this is viewed in the normal position, i.e., facing straight or from below. The PCB should be held such that the EPS number is always upside down. In the nor-1.24 elekter india january 1888

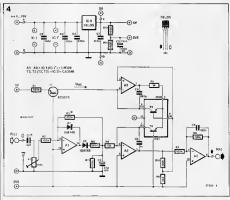


Fig. 4 Circuit diagram of the power supply end 1 of 2 identicel logarithmic amplifier channels.

fable 2		
Indication [dB]	U(C2) [mV]	Uo (A4) [V]
+10	2450	4.5
00	775	3.83
- 10	245	3.17
20	77.5	2.5
-30	24.5	1.83
-40	7.75	1.17
50	2.45	0.5

mal position, the display (2×26) pins) is fitted as far as possible to the right-hand end of the terminal strips (2×28) pins). In the reversed position, the PCB is still fitted as stated before. Howwert, the position indicator on the LCD is then at the right, and the LCD itself is fitted as far as possible to the left-hand end of the terminal strips.

Preset P: is adjusted such that of dB corresponds to a direct voktage of 775 mV on junction R-Ra. This results in 3.83 V at the output of the logarithmic amplifier, and illumination of 15 bars on the LCD. A different dB scale can be set up by redimensioning of Re. .. Ro inclusive. If the input signal has a DC component, blocking capacitor C:

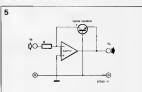


Fig. 5 The most rudimentary form of the logarithmic emplifier.

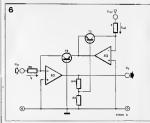


Fig. 6 This circuit is derived from that in Fig. 5, but is less affected by temperature variations.



Fig. 7 Showing the position of the dB values on the face of the LC display.

is fitted with the corresponding polarity. The reset period of the peak detector is the product of Rn and Ca. This period can be kept relatively short thanks to the fact that peaks in the input signal are already retained and displayed with the aid of the peak hold function. The input signal level can be adjusted with Pt. If necessary, At can be dimensioned for a higher amphification by increasing the value of Ra. TW

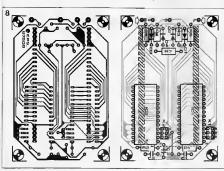


Fig. 8 Track leyout and component overlay of the LCD board (circuit diagram: Fig. 2). The LCD is fitted AT THE TRACK SIDE.

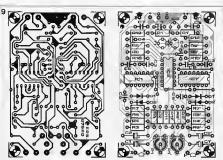


Fig. 9 Track layout end component overlay of the stereo logarithmic amplifier board (circuit diagram: Fig. 4).

Parts list

DISPLAY BOARD (FIG. 8):

Resistors (±6%): R1.R2 = 1M0 R3;R4=1K5 Rs:Rs = 10K B7 = 12K

Capacitors: C1:C2 = 390p C3=100n

Semiconductors 01...04 mcl = 1N4148 IC1:IC2=HEF4754

Miscellaneous: LCD = LTD - 321 - C01 (Mullard/Videlec; for distributors are infocerd 507 in the April 1987 issue). Kt;K2= jumpars or terminal strips; 2×3 contacts. PCB Type 87505 (available through the Readers Services).

Available from Universal Samiconductor Cavicas Limited • 17 Grenville Court • Granville Road . Hornsey . London N4 4EP, Talephona: (01 384) 9420, Telev: 25157 usdco q. Fax: 01 348 9426.

STEREO LOGARITHMIC PREAMPLIFIER (FIG. 9): Resistors (± 5%):

R1:R1' = 390K R2:R2';A7;R7',R10;R10'; 81t:811' = 100K R3;R3' = 220K 84:84' = 390B Rs:Rs'=1KO Ba:Ba' = 3K3 Ra:Re' = 120K R9, R9' = 15K B12:B13=560B Pt;Pt'= 100K preset

Capacitors.

Ct.C1'=4a7: 16 V C2;C2' = 22 µ; 16 V Ca,Ca' = 1n0 C4; C4" = 680p Cs;Cs' = 100p Cs;Ce1,C7 = 100n Ce = 330n Ce= t00µ, 3 V; tentalum bead

Semiconductors. D1:01':D2:D2'= 1N4148 Tt:T1'=8C6578 IC1:3C1'=LM324 IC2=CA3046 ICs=78L05

Miscellaneous:

PCB Type 87520 (available through the Readers Services).

TEST & MEASURING EQUIPMENT

The field of electronic test and measuring equipment is large and still growing. Although not so long ago even an electronics engineer could get by with a multimeter, an oscilloscope, and a signal generator, nowadays even a small laboratory or workshop is

equipped with an array of general purpose instruments, such as multimeters and power meters, various signal generators, a frequency counter, distortion meter, wave or spectrum analyser, and one or two oscilloscopes. In many cases, this is complemented by an LCR meter, Q meter, waveform recorder, a storage oscilloscope, and others.

To help readers find their way in this sometimes bewildering variety of equipment, we start this month a regular series of reviews of such equipment. Since the oscilloscope after the multimeter, is probably the most frequently used instrument in an electronics environment, the series is started with a review of a number of dual-trace oscilloscopes. The author of the series is Julian Nolan.

Part 1: dual-trace oscilloscopes (A)

Hitachi V-212

Hitachi is a Japanese company which is perhaps best known for its consumer products, aspecially in the video and hi-fi fields. The V-212 is one of a comprehensive range of oscilloscopes manufactured by the company, covering from the V-058G, a dual trace 5 MHz ultra compact scope, to units such as the VC-615S, a 100 MHz DSO. The V-212, which can be purchased for £320+VAT, is the dual trace version of the cheaper V-211. The accessories available include carrying cases, rack mounting kits, and viewing hoods. High-quality probes are also available, but at Flg. 1. Hitechi Type V212 oscilloscope



£27.50 each (x10/x1) it is well worth considering alternatives such as the Coline range of modular probes, which start at £13.66 (x1); the switchable x1/x10 version costs £17.84. high voltages, x10 probes have to be used, especially in the dual trace mode to prevent over-scanning of the trace. Although not restricting the versatility of the instrument, it can cause a small amount of inconvenience; a 20 V/div range as fitted to many instruments would have helped solve this problem. A x5 magnifier control extends the range of the Yamplifiers to 1 mV/div, and, in-The packaging of the V-212 takes into account the instru-

Teble 1. Specification

ELECTRICAL CHARACTERISTICS: - Protection class 1. Line voltage. - 110,120,220,240 VAC ± 10%, Externally adjuetsble. Power 30 Wests. Line frequency 50,60,400 Hz

MECHANICAL CONSTRUCTION Dimensions: - W 310 mm, H 130 mm, D 370 mm

Housing - aluminium sheet Weight - approx. 6.5 kg

Y AMPLIFIER ETC.

Operating modes: -CH 1 alone

CH 2 alone or inverted alternate or chopped (250 kHz| CH 1/CH 2. CH 1 + CH 2.

Frequency range 0 . . 20 MHz (-3 dB). Decreases to 7 MHz at 1 m// Risetime ≤ 17.5 nsec.

Deflection factor 10 steps 5 mV/div . . . 5 V/div ± 3% extends to 1 mV/div; by ×5 control, increases error by 2%, Min sensitivity 12.5 V/div; variable control; fully anti cw. input coupling AC, DC or Gnd.

Input impedance 1 MQ/25 pF; Max input voltage 300 V (peak including DC voltagel, or 500 Vp-p AC at 1 kHz or less.

X Y MODE

CH 1 X-axis, CH 2 Y-axis. X Bandwidth DC to at least 500 kHz. Less than 3" phase shift at 50 kHz.

TIMEBASE

Deflection factor 0.2 µsec/div . . . 0.2 sec/div ± 3% with 1/2/5 divisions. Expansion × 10, extends mex. timebase speed to 20 nsec/div;

expansion error \(\pm \) \(\pm \) extre. Uncalibreted control full cw extends range to 0.5 sec/div

TRIGGERING

Trigger modes: - Auto | bright line), Normal, ective TV (line and frame) sync. Trigger coupling: - AC only.

Trigger sources: - CH 1, CH 2, Alternete Line, Ext. Triggering slope: - positive or negetive, switchable Triggering sensitivity - Internal ≤ 1.5 div et 20 MHz, External

≤ 800 mV at 20 MHz, Normel mode.

MISCELL ANEOUS

CRT make Toshiba, maasuning screen 100 x 80 mm. accelerating voltage 2 kV; beam rotation by front penel

Compensation signal for divider probe; amplitude approx. 0.5 Van (± 3%), frequency 1 kHz. Z modulation 5 Vp-p noticeable modulation: Max input voltage

30 V (DC + peak AC). CH 1 output at least 20 mV/div to 5 MHz

Covered by 2 year warranty.

and the cardboard box in which it is packed is, therefore, not much bigger than the instrument ifself, which is held securely in place with polystyrene cutouts. On inspection, the Hitachi turned out to be a relatively small at 310 mm (W) x 130 mm(H) x 370 mm(D): the absence of the normal swivel stand adds to its compactness. A small one-position stand is fitted to the underside of the instrument to facilitate tilting, and for those who reguire the scope to be easily portable there is a carrying strap on one of the sidepanels. One advantage of the small stand is that it can easily be tucked under the scope, thus making the stacking of other pieces of equipment above or below the scope possible. The V-212 is supplied with a good length of mains lead, connecting to the scope via a standard IEC socket. Power consumption is low: only 30 W at 240 V. Unusually, the scope is also equipped with a vertical signal out BNC socket, which provides at least 20 mV/div into 80 0 The specifications are shown in

Table I. From these it can be

ment's very compact design,

seen that the maximum Y amplefier sensitivity is an excellent I mV/div, while the minimum is 5 V/div (calibrated) or approx. 12.3 V division (uncalibrated). When measuring relatively deed, increases the versatility of the Y-amplifiers throughout the range, providing a caltbrated step cutside the standard 1-2-5 sequence, i.e., m a 2-4-10 sequence. This does however introduce a + 2% increase in Y amp error, bringing the total to a maximum of ± 5%. Putting this into perspective in relation to, say, a waveform of typical total deflection of 40 mm, it may be that with even a + 5% Y amp error, a total deflection error of more than + 2 mm is unlikely to be generated. In the x5 mode, however, the Y amps are limited in bandwidth to 7 MHz (-3 dB). On the whole I did not find this restriction limiting as the hill bandwidth is still usable at 5 mV, at which sensitivity the Y amps performed very well at their maximum bandwidth, being well inside their -3 dB specified limit. The input amps also exhibited practically zero drift during their warm up



Fig. 2. Clase-up of V212 controls

Table 2.

CATEGORY	Gectory .	Sans Sactory	Geed	Geed	Excellent
TRIGGER EACHTRES					
FRIGGER PERFORMANCE				×	
ORT ERIGHTNESS				×	
IRT FOCUSING					×
AMP PERFORMANCE				X	
NTERNAL CONSTRUCTION EXTERNAL CONSTRUCTION			×		
EXTERNAL CONSTRUCTION EVERALL SPECIFICATION				×	
ASE OF USE					×
MANUAL					
X/Y PERFORMANCE			^	×	

TABLE EXPLANATION

TRIGGER FACILITIES-The triggering facilities offered by the scope, eg alternate triggering, TV sync, auto trigger, etc.

TRIGGER PERFORMANCE-Is an indication of how well and easily the scope triggers on a wide variety of waveforms, as well the maximum triggering frequency.

CRT BRIGHTNESS-This is an indication of the brightness available on a fully triggered waveform at the maximum deflection speed. Note: some scopes have internal brightness presets, setting the maximum and minimum brightness; this is not taken into account.

CRT FOCUSING-The standard of the focusing over the whole range of deflection speeds and display modes.

YAMP PERFORMANCE-An indication of the maximum sensitivity of the Y-amp, along with its performance across the bandwidth.

INTERNAL CONSTRUCTION-This rating assesses the scope's internal construction, the main criterion being the quality of the PCBs and other components, the general neatness and layout with a view to servicing and the mechanical robustness.

EXTERNAL CONSTRUCTION-The strength and quality of the materials used, along with the finish are among the criteria here

OVERALL SPECIFICATION-This takes into account other features which may be provided on the scope, such as trigger hold-off or a third channel, as well the general specification of the scope.

EASE OF USE-This assesses the general layout of the controls, and ease of use for a first time user, and not the ease of operation of the range switches etc. MANUAL-Takes into account the actual information included in the manual which is likely to be useful to the user.

period making instant measurements easier.

The vertical modes of the V-212 are fairly standard, including and alternate chopped (250 kHz) modes for dual trace operation. Only one channel (2) of the V-212 is invertable for subtraction purposes, this being implemented, as are some of the other functions, by pulling an associated control (in this case CH 2 position) to its out position. This does have advantages in that it helps provide an uncluttered layout, but it also means that when this 'secondary' function is operated, it is very easy to offset the 'primary' function from its original value. Triggering on the Hitachi is of a very high standard, Incorporating the unsual feature of an alternate channel triggering mode. This permits stable, fully triggered traces to be produced in either dual trace mode from two non-synchronized sources, as each channel is triggered independently. This is invaluable for taking measurements where more than one signal source is being used within a circuit, and is also helpful for single trace measurements, enabling the stable display of either channel without having to manually alter the triggering channel. Active TV frame and line triggering are also provided on the V-212. making triggering on video signals an easy task. The performance of this was good, triggering even at very low levels and over an acceptable range of line and frame frequencies. Two notable exceptions from the V-212's triggering facilities are HF and LF coupling, and although it is possible to get around this problem when these functions would normally be required by fine adjustment of the triggering threshold, the necessary filters would have made operation easier. Selection of the triggering criteria is made by a number of lever operated switches, making for fast, reliable and convenient operation of the scope. Trigger sensitivity was satisfactory at 5 mm internally and 200 mV externally in the 20 Hz to 2 MHz range, increasing to 10 mm internally and 800 mV externally in the 2 MHz to 20 MHz range. Generally the triggering per-

formance was very good, with

the alternate triggering being a

mally only found on models outside this price range A stable trace was produced in nearly all cases, the Intger threshold control did, however, prove to be sensitive and it was very easy when pulling this control out (for inggering on the trailing edge of a signal) to offset it outside the triggering threshold, thus causing the timebase to free run, producing an unboled track.

Maximum timebase speed is 200 ns/div: this is however extendable to a maximum deflection speed of 20 ns/div (not 100 ns/div as stated in the manual) by means of a x10 control, although naturally this is at the expense of trace intensity. Speed selection is by means of a 19-position rotary switch, the minimum speeds being 0.2 s/ div (calibrated) or roughly 0.5 s/div (uncalibrated). On the maximum deflection speed of 20 ns slight defocusing occurs towards the end of the trace, which is unfortunate, because for the remaining speeds focusing from the Toshiba tube is excellent for a 2 kV acceleration voltage Despite this, the performance of the scope in this area is particurly good, many of its rivals not offering a 20 ns/div sweep speed, although, as I have said, accurate measuring over the last third of the trace at this speed is limited by the 2 mm wide trace over this area. The screen itself is filtered a light blue and has full graduations for risetime measurement. The V-212 is equipped with Zmodulation and CH 1 vertical signal out facilities; the BNC connectors for both these functions are mounted on the back panel. For noticeable intensity modulation a 5 V p-p signal is required, the input bandwidth for this function going up to 2 MHz. The CH I output on the other hand provides a buffered output from channel I which could be used to drive for example, a counter/timer, thus providing an accurate readout of frequency, etc.

Aluminium plays an important part in the V212's construction, both the outer housing and frame are manufactured from this, which contributes to the scope's light weight of 6.3 kg Plastic is used for the front fascia surround, and this could prove to be fragile, especially around the top corners if the scope is used in rugged con-



Fig. 3. Internal view of V212

ditions. Robust feet/cable holders are featured on the rear panel and protect the instrument to a large extent from any damage which may occur if, for example, the instrument is dropped while being carned. In contrast to many other scopes, all the controls have a very positive and fairly light action, making for easier, more precise operation. Some, however, notably the Y amplifier fine controls, protrude a good distance from the front panel, making accidental damage more likely in the event of a fall. My only major criticism of the Hitachi, if it can be called that, is the internal construction. The main circuitry is mounted on two PCB's of equal size, but larger components, such as voltage regulators, etc., are mounted on the chasses itself for good heat dissipation. This wide variety of mounting points coupled with the three remaining PCB's housing the tube base, etc., necessitates the use of a large number of wire connections and links, giving the inside of the Hitachi an appearrence not dissimilar to one of the company's tapes. All of the interconnections appear to be of a very high quality, however, and I have been assured by Hitachi that the number of interconnections in no way affects the reliability of the scope. This is proved by the fact that Hitachi oscilloscopes using the same construction technique are offered for hire by some of the electronic equipment rental companies, where reliability is obviously at a premium. Ignoring the number of inter-

connections, internal construc-

tion was general'y good, the large number of connections making the mounting of all high power dissipation components on the subframe possible. The internal construction itself are substant to the translation of the internal construction that the property of the instrument. Unlike the Yamphiffers, the ERT section of the circuit is completely shrouded, thus helping to prevent the build up of dust, as well as helping to prevent and the substantial translations.

possible shock should be outer housing be removed. Not surprisingly, most of the semiconductors are manufactured by Hitachi themselves; other components come from a variety of manufaturers and are fairly standard, ranging from miniature resistors to the industry standard 78 and 79 series monolithic fixed voltage

regulators.

The 56 page manual contains a number of detailed sections, among which how to set up to scope initially, and a particularly good section on measuring procedures. There are no sections on calibration or servicing, and the roughly A5 size of the manual makes the circuit diagrams small and and in place difficult to understand as they are spread out over a number of pages. There is also no circuit description. Overall. although containing some good sections, the manual missed out on several important points and could have been accurately summarized in a considerably shorter space.

Conclusion The Hitachl V-212 is generally a

Other Hitachi scopes under £1000

20 MHz

V-222: As V-212 plus alternate magnify, swivel stand, scale illumination, uncal. indicators. Probes are also included. £395+VAT

V-223: As V-222 plus sweep delay, 1 μsec to 100 msec. £450+VAT.

V-225: As V-223 plus on-screen cursor measurement of voltage and time difference. £550+VAT.

40 MHz

V-422: As V-222 plus signal delay line, 12 kV accelerating voltage. £580+VAT

V-423 and V-425: As V-223 and V-225 respectively, but with increased bandwidth, V-423: £650; V425: £695.

60 MHz

V-650F: Similar to V-422 + dual timebase, trigger view, delay multiplier: £780+VAT.

PORTABLE

V-209: 1 mV sensitivity 3.5" tube, lightweight miniature format, battery/mains, N:Cd batteries included: £680+VAT well-thought out scope, with I some advanced features, and is competitively priced at £320+VAT. Triggering is particularly good, having an alternate triggering facility and this coupled with the scope's excellent CRT, which is one of the best 2 kV tubes I have seen,

The scope's small size also makes it eminently suitable for those users who include portability high on their list of priorities. It does have one or two minor shortcomings, such as its internal construction. which, although of a fairly high standard, does possess a large make it well worth looking at. number of interconnecting

wires, which could make ser- | vicing difficult. However, I am quite happy that they will in no way affect reliability. The manual is not really up to standard, despite its length, providing only the initial 'set up' information, which admittedly is good. Summing up, the V-212 should represent a good choice

for most users who need a flexible scope for a wide variety of

The Hitachi V-212 was supplied by Hitachi-Denshi (U.K.) Ltd . 13-14 Garrick Industrial Centre . Garnek Road . Hendon . London NW9 9AP.

Crotech 3133

The company of Crotech was formed in 1981, and now designs a wide range of test equipment from frequency counters to signal generators. The 3133 is one of a range of six oscilloscopes manufactured by the company. The range extends from the single trace 3031 at £199 to the 3339 which features a 30 MHz bandwidth, as well as a VDU mode, enabling the scope to act as a monitor, at £570. The new 3133 is priced at a competitive £319, The 3133, which replaces the 3132, is unique in its price range in that it incorporates a component comparator and a power supply outlet in its design, and has a bandwidth of 25 MHz (-3 dB). Probes are also supplied, but these are of the 'crocodile clip' x1 design, so their usefulness for RF work is limited. A x1/x10 probe may be purchased as an optional extra, along with a light hood and trolley.

in its layout, with the CRT ! situated in the centre of the scope and the Y-amp and timebase/triggering controls positioned at either side of it. This gives the scope the average size of 330 (W) x 395 (D) mm, although the height is somewhat higher than normal at

165 mm. The weight of the 3133 is also on the somewhat heavy side at 8.5 kg. A three position swivel stand is fitted, which, given the external graticule of the tube, is just as well, since to enables the scope to be positioned to minimize the small parallax error. Mains connec-

tion is by means of a fixed lead, i.e., no socket, which is a pity. since it is of only average length so that in some cases it may be necessary to extend its length. As I have already mentioned, the 3133 incorporates some rather unusual features, these in the main being the power supply, component comparator and the more common trigger hold-off facility. The front panel layout is fully

colour coded, and this should make first time operation no problem, as well as contributing very significantly to the scope's ease of use. Most of the functions are selected by a series of push-button switches. which are arranged in four groups: CH | input coupling; CH 2 input coupling: Display mode: and trigger functions etc. While these provide an easily identifiable, and in some ways more flexible, method of function selection, I found that operation is perhaps slightly more time-consuming than the more usual 'slider' type switches.



Table 3. Spacification

ELECTRICAL CHARACTERISTICS

Line voltage: - 115,220,230,240 VAC, internally adjustable. Power 40 Watts. Line frequency: 27-65 Hz.

MECHANICAL CONSTRUCTION

Dimensions - W 330 mm, H 165 mm, D 395 mm Housing - aluminium sheet

Weight: - approx. 8 5 kg

Y AMPLIFIER ETC. Operating modes: -

CH 1 alone.

Inversion capability on CH 2 only Alternate or chopped (120 kHz) CH 3/CH 2. CH 1 + CH 2.

Frequency range 0 . 25 MHz (-3 dB). Risetime ≤ 14 nsec.

Deflection factor 12 steps; 2 mV/div . . . 10 V/div ± 3%; no variable attenuation controls.

Input coupling AC, DC or Gnd. input impedance 1 MΩ/25 pF; Max input voltage 400 V (DC + peak AC).

X-Y MODE

CH 1 Y-axis, CH 2 X-axis. X Bandwidth DC to 1 MHz (-3 dB). Phase shift at 50 kHz ≤3".

TIMEBASE

Deflection factor 0.2 µsec/div... 0.5 sec/div ± 3% with 1/2/5 divisions. Expansion × 5, extends max, timebase speed to 40 nsac/dly (vari-

able control fully anti-cw), expension error \$ 2% extra; typical variable control error ± 2%.

TRIGGERING

Trigger modes: - Auto (bright line); Normal; active TV (line and frame) sync.

Trigger coupling: - AC, DC, HF reject. Trigger sources: - CH 1, CH 2, Line, Ext.

Triggering slops: - positive or negative, switchable Triggering sensitivity - Internal ≤ 0.5 div at 25 MHz, External ≤ 1 V at 25 MHz. Auto mode.

MISCELLANFOUS

CRT-make NEC, 13 cm front faced round lube (viewing area approx. 100 x 80 mm); accelerating voltage 2 kV, beam rotation by front panel adjustment

Compensation signal for divider probs, amplitude approx. 0,2 Vpp (± 3%), frequency 1 kHz. Z modulation 20 Vp-p for complete blanking (-).

Power Supply: 5 V at 1 Amp, +/-12 V floating at 200 mA continuous.

Component Comparator -- test voltage B.6 V r.m.s., test current 28 mA max; line frequency = 1est frequency. Covered by 2 year 'Blue Chip' warranty.

The Yamphifiers, which are positioned to the left of the tube, surprisingly have a bandwidth of 25 MHz: 5 MHz more than the 20 MHz offered by its direct competitors. Performance of the Y-amps is certainly good, meeting the 25 MHz bandwidth well inside its -3 dB limit. The 2 mV/div maximum Yamp sensitivity is effective across the whole bandwidth. allowing accurate measurement of low amplitude RF signals. This range extends up to a useful 10 V/div. I found calibration accuracy on all of these ranges very good, and well within the quoted ± 3%. It is a pity, however, that both Yamplifiers have no variable control. This among other things makes accurate risetime measurements difficult, unless the deflection amplitude of the signal matches that of the risetime graticule. Both Yamps have a 14 ns risetime to accommodate their wider than usual bandwidth and this does, of course, help in giving more accurate high frequency pulse deflection representations than the more common 17.5 ns risetime. This reduced risetime is largely due to the use of faster FETs in the input stage and in my view is well worth the trouble, not only having the advantages outlined above, but also that at 20 MHz the attenuation is way below the -3 dB level, enabling more accurate vertical measurements to be made across the whole upper bandwidth.

The display modes on the 3133 are fairly standard, with the excepuon that in single trace mode only CH I can be displayed, instead of the more usual switchable CH 1/CH 2 option. This is certainly not a major setback, but it can entail a certain amount of lead swapping, or trace repositioning, if. for example, it is necessary to display a signal connected to CH 2 for a full 8 cm vertical deflection amplitude. A 1 kHz 200 mV (+ 2%)p-p divider probe compensation square wave output is provided.

An ever increasingly popular feature is the targer hold off faculty, which is now finding its way into the 'under £350' price bracket. This, along with the increase in bandwidth and slimine appearence, is one of the main differences between the now 3133 and the older 3132.



Fig. 5. Internal view of 3133 (right-hand)

Trigger hold-off facilitates stable triggering on complex and irregular waveforms, and as such is useful for displaying, for example, complex pulse trains in digital work over a wide range of timebase speeds. The 3133's hold-off facility coped with a wide variety of timebase speeds and waveforms, ranging from a simple double pulse to a complex pulse train. Other triggering functions include the more standard HF reject and TV synchronization. Triggering performance is good for the vast majority of waveforms. When the scope is in auto mode and the TV frame sync is in operation, however, it is difficult to lock on to the frame sync pulses during a steady video signal: a changing video signal with a low signal content makes this next to impossible. No problems were encountered in the line sync mode and reliable TV (both frame and line) triggering was present in Normal

Normal and Auto modes are also provided, making triggering effective across a wide range of signals. Alternate, or Vertical triggering, is not a feature of the scope, and consequently non-synchronized waveforms cannot be stably displayed on both traces. Triggering sensitivity is very good, typically being 2 mm upto 25 MHz internally, which is well inside the 0.5 div deflection guoted, or approximately 700 mVp-p externally, again well inside the guoted I Vp-n. To obtain these sort of sensitivities, fairly entical admsiment of the triggering threshold is, however, required, although triggering on the quoted sensitivities is more easily accomplished.

with a low signal content nakes imebase speeds range from this next to impossible. No 02 secsetiv to 0.5 speeds to problems were encountered in the line sync mode and reliable creased to 200 needs to the the sync mode and tillno tirgi-gering was present in Normal mode. AC and DC coupling and timebase is ± 3%, the variable timebase is ± 3%, the variable content of the complex of

control, when fully clockwise, adds about 2% to the error. A x5 control is provided, which increases the maximum deflection speed to 40 nsec/div and brings the maximum error at this speed to approximately 7%, which I found acceptable for all tests carried out on the scope. The maximum sweep speed of 40 nsec/div gives a 1 division horizontal resolution for a 25 MHz sine wave and should be enough for most purposes. The 3133 is one of the few scopes which still use an external graticule CRT. On the 3133 parallax error is kept to a minimum by sticking the graticule template directly onto the CRT, and, although a small parallax error is obviously still present, I found that the extra measuring error incurred when taking measurements is practically zero, if the screen is viewed from a constant angle. The external graticule does, however, slightly obscure the trace along its markings to a small extent, and in some circumstances it may be necessary to slightly alter the viewing angle to clearly observe the whole of a low intensity trace. The 2 kV CRT itself is round and because of this the trace cannot be observed at the corners of the viewing screen. However, under normal conditions, this in no way affects the measuring capability of the instrument, as most measurements are taken at or around the centre of the screen. It may, however, shahtly affect dual trace operation, causing a small adjustment in waveform amplitude or position on, for example, a pulse wavetrain, where viewing of the initial leading edge could otherwise be partly obscured. Automatic focusing is not incorporated, and consequently a small adjustment is necessary when, for example, changing deflection speeds 50 msec/div to 40 nsec/div in order to maintain the optimum focus of the trace. The focusing of the CRT at low to medium intensities is quite good, although at higher intensity's slight defocussing did defocussing did occur, although with the good brightness available this is not surprising. Despite this, the tube's performance on the focusing side does not quite match that given by some of the

better 2 kV internal graticule.

rectangular tubes. The CRT is



Fig. 6. Internal view of 3133 (left-hand)

protected by a deep blue plastic faceplate and is mounted in a bezel which also has camera mounting cut-outs. The cost saving on the external graticule tube allows extra features, such as the power supply, to be incorporated. This has three outputs which consist of a negative ground 5 V 1 Amp supply, suitable for driving TTL etc., and to floating ground outputs which can be configured as + 12 V (200 mA each), +24 V or -24 V supplies, suitable for driving a whole host of devices from op-amps to CMOS logic. This facility should prove useful to most users, even those who already have their own power supplies, mainly because in contrast to the average power supply, with perhaps 1 or 2 supply rails, the 3133 has 3. already configured to supply simultaneously both analogue and digital circuitry. For those users who already have a comprehensive power supply, this feature may be of more limited use, but I feel still worth while. The component comparator consists of two component testers, which generally display a V-I type curve of the component under test. The test signal is an 8.6 V r.m.s. sine wave, which produces, for example, a sharp right angle for a typical diode, or ellipse for a capacitor. Although it does not provide any accurate information as to the component's value, it does provide a very clear indication of whether the component is operational, if it is, for example, 'leaky'. Component comparison is also possible with the 3133 two testers, enabling a known good device to be accurately compared with other examples. It is also possible to compare complete circuits with this technique, each circuit effectively having its own 'signature'. Initially, I was a little sceptical of the component tester, mainly because I was unsure if its usefulness, in view of the fact that the vast majority of scope users possess a multimeter. This opinion was however, quickly changed by the component tester, which proved to provide a quick and very clear method of both testing and comparing compo-

Table 4.

CATEGORY	Unsate- fectory_	Sans- fectory	Good	Very Good	Excotano
TRIGGER EACRITIES TRIGGER PERFORMANCE				×	
CRT INIGHTNESS CRT FOCUSING				×	
Y AMP PERFORMANCE			×		
INTERNAL CONSTRUCTION EXTERNAL CONSTRUCTION				×	
OVERALL SPECIFICATION					
EASE OF USE					k
REARUAL				i i	
X/Y PERFORMANCE				w 1	

nents, allowing the user in most cases to see their actual charac-

teristics Both the internal and external construction are of a very high standard. Internal construction is based around a relatively large number of PCBs, totalling seven in all. The timebase, Yamplifiers, power supply, etc., are all mounted on different boards, thus making servicing greatly easier. All the PCBs are silk screened with the various component identification numbers, and, where appropriate, their function. Both the attenuator stage in the Yamplifiers and the EHT section are fully shrouded, as is the CRT. The components themselves come from a wide variety of sources and all appear to be of a good quality. All internal wiring is neatly grouped, giving the inside of the scope a very neat appearance. External construction of the scope is to the same high standard, being almost completely aluminium. This also includes, unusually, the front panel, which is silk screened with the appropriate markings None of the front panel controls extends beyond the display bezel, which further increases the robustness of the scope. With the construction in mind, it is not surprising to learn that among the users of the 3133's predecessor, the 3132, are British Nuclear Fuels, GEC, UK AEA and several large industrial companies. A comprehensive manual and a

book entitled Getting The Best From Your Scope are included with the 3133. Both are very good, the manual covering initial setting up, servicing and calibration, while the book deals with a wide range of applications, including TV servicmg. The manual also includes a

full circuit diagram, as well as diagrams of both mechanical construction and PCB layout.

Conclusion The Crotech 3133's extra functions and higher than normal bandwidth turn what otherwise would perhaps be an unexceptional scope into one which is well worth looking at especially when the price of £319 is taken into account. While the CRT gives a reasonable performance, its external graticule can make accurate measurements slightly more time consuming. It is probable, however, that the extra bandwidth and functions offered by the 3133 over its rivals will be worth this to the many users who require a scope which can be used for a large number of applications. The high standard of construction is also one of the 3133's assets. The 3133 is particularly suited to new users of scopes as it is particularly easy to operate and it is supplied with two good manuals. To sum up. the 3133 certainly represents value for money, offering as it does a number of useful extra functions and a reasonable performance, while maintaining a very high standard of construction. If you require a versatile scope, with a wide range of features along with good construction quality. I can certainly recommend it.

I have been informed by Crotech that they intend to improve the TV triggering performance of the 3133; the review model was a pre-production prototype.

The Crotech 3133 was supplied by Crotech Instruments Ltd., 2, Stephenson Road, St. Ives, Huntington, Cambridgeshire PE17 4WJ. Tel. (0480) 301818

Other Crotech scopes DUAL TRACE

3132-Preceding model to 3133 main differences 20 MHz bandwidth, no trigger hold off. design. Currently £285+VAT.

3337-30 MHz version of 3132. main differences 10 kV acceleration voltage, no component tester, signal delay, Currently £425+VAT.

3339-Same as 3337, except VDU mode facility and the addition of a component tester and power supply. Currently £570+VAT.

SINGLE TRACE

3031 and 3032-single trace, 20 MHz, component tester, 3031 9.5 cm rectangular tube, 3036 13 cm round tube. Currently £199 and £220 respectively (both + VAT).

Next month, Julian Nolan reviews the Gould OS300 and the Grundig MO20 oscilloscopes.

PHILOSOPHIAE NATURALIS PRINCIPIA MATHEMATICA

MATHEMATICAL PRINCIPLES OF NATURAL PHILOSOPHY

ISAAC NEWTON 1687 — A TERCENTENARY CELEBRATION — 1987

by Dr. T.R. Carson, University of St. Andrews, Department of Physics & Astronomy.

December 1642 in the manorhouse at Woolsthorpe, near Grantham, Lincolnshire. He died on 20 March 1727 at Kensingon, London and was buried in Westminster Abbey. Thus he lived under seven monarchs, as well as two protectors, in what can surely be described as an age of revolution. Against this politically turbulent background the world of learning was undergoing, after a similarly turbulent start, its own albeit quieter evolution. The ancient philosophy of Aristotle, despite the efforts of Aquinas, had already sunk into decline. Of the three Philosophies, Metaphysical, Moral and Natural, the latter was poised for its most dramatic development. Man's place in the physical universe had been redefined by Copernicus and Brune, Bacon and Galileo had initiated a new science, based on observation and mathematically precise description, so immediately exemplified in Kepler's three laws of planetary motion. The most influential philosopher of the seventeenth century was Descartes, whose attempt to construct an allembracing philosophy of the world, failed even to resolve his own conflict between reason and authority. Nevertheless it had a lasting impact on the

1.32 etakter indea jamusry 1988

Isaac Newton was born on 25

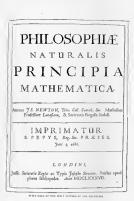


future development of natural philosophy lutrough its reduction of all reality to matter and motion. Newton's "Principia" represented the next step along this road. Matter was invested with certain intrinsic properties, both active and prassive, while motion became a

series of events in space and time subject to quantitative analysis based on premises of cause and effect. Later on the combination of Descartes' analytical geometry and Newton's differential and integral calculus would become powerful tools in forging a compowerful tools in forging a com-

plete mechanistic philosophy. Due to the death of his father two months before his birth. Newton spent his early years with his maternal grandmother in Woolsthorpe. In 1654 he entered the grammar school in Grantham, but left in 1656 to help manage the family farm, returning to school in 1660 to prepare for college for he showed a remarkable precocity in mathematics. In 1661 he matriculated at Trinity College. Cambridge, where he became a scholar in 1664 and graduated BA. in 1665. He became a fellow of Trinity College in 1667 and in 1669 was elected Lucasian professor of mathematics in succession to Isaac Barrow whom he had impressed as "a very ingenious person" and "a man of exceptional ability and remarkable skill". He was elected to fellowship of the Royal Society in 1672 and represented the university in parliament in 1689 and in 1701, and was finally appointed to the post of Warden of the Mint in 1696 and Master in 1699. In 1703 Newton became president of the Royal Society, which office he retained for life. He was knighted by Oueen Anne on the occasion of her visit to Cambridge in 1706.

During the years 1665-1666, at a time of enforced absence from Cambridge due to the plaque, at Woolsthorpe, Newton made a number of advances in optics. mathematics, mechanics and gravity, ft was mainly with the last three topics that the "Principia" would be later concerned, but it was during this rural retreat that the seeds of that bountiful harvest were sown. Newton himself wrote later "... from Kepler's rule of the periodical times of the planets [Kepler's third law] ... I deduced that the forces which keep the planets in their orbs must be reciprocally as the squares of their distances from the centres about which they revolve; and thereby compared the force requisite to keep the Moon in her orb with the force of gravity at the surface of the earth, and found them answer pretty nearly. All this was in the two plague years of 1865 and 1668... for in those days I was in the prime of my age for myention and minded Mathematics and Philosophy more than at any time since ... between the years 1676 and 1677 f found the proposition that by a centrifugal force reciprocally as the square of the distance a planet must revolve in an ellipse about the centre of force as focus [Kepler's first law] . . . and with a radius drawn to that centre describe areas proportional to the times [Kepler's second lawl". Christian Huygens had already published in 1673 the rule of centrifugal force for uniform circular motion. What Newton did was to define the concepts of quantity of motion (momentum) and force, and the laws relating to them. He also made the conceptual move from centrifugal to centripetal force and generalized from the circle to the ellipse, having already postulated the universality of the gravitational force on the falling terrestrial body and that acting on the Moon and other heavenly bodies. The story of the apple falling from the tree in the garden at Woolsthorne was told by William Stukely in recounting his conversations with Newton in 1728, and also by Voltaire who obtained it from Newton's step-mece. The tree was cut down in 1820 but a nortion of the trunk may be seen in the library of the Royal Astronomical Society in Burlington House, Piccadilly. The events leading up to the publication of the "Principia" began with the visit to Newton



ILLUSTRISSIM Æ

SOCIETATIREGALI

a Serenissimo

REGE CAROLO 11.

PHILOSOPHIAM PROMOVENDAM

FUNDATÆ.

ET AUSPICIES

POTENTISSIMI MONARCHÆ

JACOBI

FLORENTI

Tractatum hune humillime D. D. D.

7 S. NEIFTON

assistant secretary of the Royal Society and editor of Philosophical Transactions) to pose the question, prompted by a discussion with Robert Hooke and Christopher Wren, as to what orbit a planet would follow if attracted to the Sun by a force varying inversely as the square of the distance. Halley, impressed by Newton's immediate answer, asked for the proof, which Newton sent and was received by Halley with such great satisfaction that he visited Newton again to discuss the matter. He reported to the Royal Society the 'curious treatise, De Motu (On Motion)" which Newton had promised to send to the Society. This was received in February 1885, Halley's intention being to secure the position until Newton could publish his work. as he was encouraged to do by Halley and by the Royal Society. in April 1686 the Royal Society received a manuscript, in the hand of namesake and amanuensis Humprey Newton, of what Halley referred to as an "incomparable Treatise on Motion" entitled "Philosophiae Naturalis Principia ematica" and dedicated to the Society by Newton. This was in fact the first part of the "Princtpia", comprising the "Defini-tions", "Axioms or Laws of Motion" and "Book 1 - On the Motion of Bodies", bearing the full title of the whole work. The Society resolved to have the manuscript printed without delay at its own expense, and furthermore entrusted Halley to supervise the printing. For fmancial reasons the Royal Society shortly ordered that Halley print it at his expense which he engaged to do. In lune 1688 Newton informed Halley that he had intended the "Principia" to consist of three books, of which the third would concern the system of the world, which he now proposed to suppress because "Philosophy is such an impertinently litiolous Lady that a man had as good be engaged in law suits as have to do with her". Newton realized that the title of the whole work would no longer be as appropriate, considered changing it, but on second thoughts retained the former title to help the sale of the book. Halley begged Newton "not

to...deprive us of your third alabor indususpus 1968 1.33

in 1684 of Edmund Halley (soon

book", adding that it would ! make the "Principia" acceptable to "those that will call themselves philosophers without mathematics, which are by far the creater number" Newton deferred to Halley and duly delivered to him "Book II On the Motion of Bodies in Resisting Media" in March 1687 and "Book III -- On the System of the World" in April 1687. On 5 July 1687 Halley wrote to Newton that he had "at length brought your book to an end, and hope it will please you" Halley had written a latin ode, dedicated to Newton, with which he prefaced the work. In his own preface Newton paid a glowing tribute to the assistance which Halley had given him. The title page bore the "imprimatur" of Samuel Pepvs. President of the Royal Society. The number of copies printed is unknown but has been estimated as high as four hundred. Newton received twenty for himself and forty for disposal through booksellers. The price to the trade was six shillings in sheets, reduced to five shillings for cash, but nine shillings leatherbound and lettered

News, emanating from Halley and John Flamsteed (first Astronomer Royal), of the impending appearance of the "Principia" had generated much excitement. Reactions to the book were quick to follow publication. Two reviews appeared in French (Journal des Scavants, Bibliotheque Universelle) the latter being attributed. to John Locke, one in Latin (Acta Eruditorum), and one in English (Philosophical Transactions) by Halley, Readers were left in no doubt as to the scope and scale of Newton's achievement. Newton's work appealed particularly to mathematicians like James Gregory (St Andrews and Edinburgh) and his nephew David Gregory (Edinburgh and Oxford). Perhaps the first continental student of Newton was Nicolas Fatio de Duilher, a Genevese mathematician who was instrumental in spreading news of the "Principia" to Huygens in Holland and to Leibnitz, otherwise known for his controversy with Newton regarding the calculus, in Germany. An early casualty was Descartes' philosophy, particularly as it applied to mechanics, including his theory of vortices relating to celestial motions. However, to Newton the concept of action at a distance without mediation was an absurdity, a point of some importance when considering the later revolution due to Einstein, Newton also made it clear that while in-

voking gravity as a cause of (change of) motion, he was making no statement regarding the cause of gravity itself and permitted himself but one reference to God in the first edition. Richard Bentley, Master of Trinity College, in his Robert Boyle Lectures (1692), noted that the dispositions of the planets relative to the Sun were critical for the sustenance of life thereon, leading him to "discern the tokens of Wisdom in the placing of our Earth". George Berkeley, Bishop of Cloyne, attacked Newton's concepts of absolute space, absolute time and absolute motion as inadmissible since they entertained "something besides God which is eternal, uncreated, infinite, indivisible, unmutable". Joseph Addison too upheld the thinking of Descartes, although both Berkeley and Addison would later publish defences of the Newtonian philosophy. Leibnitz considered that gravity "without any mechanism... or by a law of God... without using any intelligible means, ... a senseless occult quality...". Roger Cotes (first Plumian Professor of Astronomy).

editor of the second edition

(1713) of "Principia" under

Bentley's supervision, advised

Newton to counter the criticism

of Leibnitz. For the second edi-

taining the sentence "... And thus much concerning God: to discuss of whom from the appearance of things, does certainly belong to Natural Philosophy...". Herein too is found the famous declaration .. hypotheses non fingo... frame no hypotheses)", which must be taken only in the context of the cause of gravity, for Newton framed many hypotheses. The third edition (1726) was prepared for Newton by Henry Pemberton who also, a week after Newton's death, announced a translation of the "Principia". This was never published and the first English translation was that of Andrew Motte in 1729. Fatio was the author of the epigram on Newton's tomb: "Sibi gratulentur Mortales, Tale tantumque extitisse Humani General Decus (Let mortals rejoice that there has existed such and so great an ornament of the human race)". Amongst the many tributes that have been accorded Newton's "magnum opus" few are as generous as that of Laplace in referring to the causes "which will always assure the "Principia" a preeminence above all the other productions of the human in-

tion Newton thus prepared the

famous General Scholium con-

stereo pan pot

This circuit offers the possibility of stereo image width control from stereo, through mono, to reverse stereo. The circuit comprises two emitter followers and a linear stereo potentiometer.

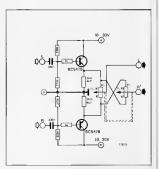
If x is the ratio of the resistance between the

If x is the ratio of the resistance between the sliders of the pots and the lower ends of the pots to the total resistance then it follows that the outputs L' and R' are given by:

L' = R(1-x) + Lx R' = Rx + L(1-x)

Therefore, when x = 1, L' = L and R' = R (normal stereo), when $x = \frac{1}{2}$, $L' = R' = \frac{1}{2}$ (L + R) (mono); when x = 0, L' = R and R' = L (reverse stereo).

The low output impedance of the emitter followers ensures that, when the potentiometer is in either the extreme clockwise or anticlockwise position, crosstalk travelling along the potentiometer tracks cannot appear at the outputs. Good channel separation in the stereo and reverse stereo modes is thus mantained.



tellect".

INFORMATION THEORY AND ENCRYPTION

by B.P. McArdle

Anyone who is, or becomes, involved in encryption operations ond cryptosystems must wonder obout their connection with Information Theory. In this article, Brian McArdle briefly explains the areas of overlap and difference.

Consider a channel where a message xi drawn from a set x1,x2,x3,...,xn of n possible messages, as illustrated in figure I, is transmitted between sender A and receiver B. The message could be just a letter from an alphabet of n letters or a symbol. However, it is information of some type and is exchanged between A and B. The electronic representation of xcould be a particular waveform or a set of binary digits (bits) etc. For example, the English alphabet of 26 letters requires a set of 5 bits to represent a letter and since 25=32 there are 6 redundant combinations. For the present the method of signalling is not being considered. If each x has probability Pr(x_i)=p_i of being chosen for transmission by A the information antropy of the channel is given by the equation

$$H = -\sum_{i=1}^{n} p_i log_2(p_i)$$
 (1

The minus sign makes H positive because every $p_1 {\le} 1$. The base of the loganithm does not have to be 2 but this means that the dimension of H is bits. If a particular message x_1 has $p_1 {=} 1$ which means that $p_1 {=} 0$ for $i {\ne} 1$, then $H {=} 0$. If all messages are equally likely to be transmitted (uniform distribution)

$$H = log_2(n)$$
 (2)

which is the maximum value and is also the number of bits required to represent a message. The larger the value of H the greater the uncertainty in the information transmitted over the channel. If H=0 there is no uncertainty and the re-



information. If the channel is very nousy such that the signals are corrupted during transmission this adds to the problems of the receiver. However, the techniques used to reduce the effects of noise are not being examined in this article. And the technical limitations of the channel, such as in the Hartley-Shannon Law are not considered.

ceiver B does not receive false

A priori and a posteriori information The receiver may have some

The receiver may have some advance information before the message is sont. This is known as a priori information and the a priori probability is the probability that it is correct. The a posteriori information and the associated a posteriori probability refer to the transmitted message after it is received. These are commonly used parameters in Information

Theory but are not used in this article.

Encryption operation If n is encrypted as illustrated in figure 2

$$\tilde{E}_{E}(x_{i}) = y_{i}$$
 (3)

the electronic representation of v. instead of x. is transmitted. It is easier to keep track of the explanation by taking the x1's and y, 's to be letters but this is not essential. The encryption operation is varied by changing the parameter K called the key. This is the secret information and should be known only to sender and receiver. The plaintext (or cleartext) and conhertext are x, and v, respectively. An unauthorized listener (cryptanalyst) on the channel would probably know the method of encryption but not the actual key in use. The strength of the encryption operation is determined by the difficulty in deducing the key from the ciphertext. Modern cryptosystems also require that the key should not be found to matched plantoxic point in our developed further because we are not acrually analysing particular systems. The relationship between En-Theory is now considered by outlining the results of a famous paper.

Shannon's theory

Shannon (Ref.l) in his paper compared the effects of secrecy operations to the problem of noise. The letters of the ciphertext should appear random with no preference for any particular letter(s) but only the correct key will produce a meaninoful message after decryption. He assumes that a cryptanalyst knows or can deduce the method of encryption (which we will not examine here) and has unlimited ciphertext but no plaintextciphertext pairs. He explains the requirements of a cryptosystem using the following

parameters:
(a) entropy of the plaintext H(X)
computed as per equation (I);
(b) entropy of the ciphertext
H(Y) computed as per equation

(c) key entropy H(K) computed as per equation (I);

(d) key equivocation computed according to the equation

H(K/Y) = $-\sum Pr(K,Y)LOG_2Pr(K/Y)$



with joint and conditional prob-

abilities being used. We need not be concerned with the various steps in the analysis but he deduces the following result

$$H(X/Y) = H(X) - H(Y) + H(X) = H(X) - nD$$
 (5)

where D is defined as the Redundancy. The dimension is bits per symbol which is usually a letter. This parameter requires further explanation because it is central to the theory and conclusions.

Most languages have a peculiarity that certain letters occur more often than other letters. Consider a message which is encrypted by replacing each letter with another letter according to some rule. A good cryptosystem will result in a ciphertext which has a uniform distribution of letters such that no letter or group of letters occurs too often. This means that H(Y) and H(X) have different values. Although they are both measures of uncertainty, the difference between them is a measure of redundancy. Obviously, H(Y) will be larger than H(X) from equation (2). For English D=3.2. If H(Y)=H(X), then D=0 and H(K/Y)=H(K) and even unlimited amounts of ciphertext encrypted with the same key do not reveal the key. Thus one of the most important conclusions of the paper is that cryptanalysis is only possible because of language redundancy. The 2nd important parameter is

the Unicity Distance given by $U = H(K)/D \qquad (6)$

whose dimension is symbols or letters. This is the number of letters of the ciphertext required to determine uniquely the key (remember that Shamon assumes that eryptanalyst knows the method of encryption and has unlimited ciphertext but no plaintext).

Example 1

Consider a simple substitution

as follows:
Plaintext: A B C D E F G H I J K
L M N O P Q R S T U V W X Y Z
Ciphertext: G Z W J P A B I M T
D N E C S O Y X V H L F K Q R U.
The number of possible tables which is the equivalent of the various keys is 26' such that

$$H(K) = LOG_2(26^{\frac{1}{2}})$$
 (7)

.: U = 28(approx.) .

which agrees very well with practical results. Any reader who wishes to know the actual techniques to deduce the tables should refer to (Ref.2).

Example 2 Consider the Data Encryption Standard (Ref.3) which turns a 4 bit plaintext block into a 64 bit ciphertext block using a 56

bit key block (Fig. 3).

 $H(K) = log_2(2^{66}) = S6$

.: U = 18(approx.). (10)

From Shannon's Theory, this

means that 18 blocks are required to establish that a decrypted message is meaninaful text. If 8 bit ASCII is used. then 18 blocks = 144 symbols. However, since the ASCII alphabet has 128 symbols, the result is not too different from example 1. In reality, a cryptanalyst could not try each key of 256 possible keys but the example does illustrate the principle quite satisfactorily. The other parts of Shannon's paper need not be considered in presenting a short overview, However, his results are deduced using many of the parameters and formulae which are now part of Information Theory.

Conclusions

Encryption is not really a branch of Information Theory. There are important areas of overlap but the theories and ischniques for the evaluation of modern cryptosystems, such as the Data Encryption Standard or RSA Public Key Cryptosystem (Ref.4) have become subjects in their own right. Any student who wishes to study Cryptology would be well advised to start with basic Information Theory and Shannoit's paper.

Appendix

Equation (4) can also be written in the form

H(K/Y) =

 $-\sum_{i=1}^{m}\sum_{j=1}^{n} Pr(k_i,y_i).log_2Pr(k_1/y_i) (11)$

where m is the number of possible keys. The other form is commonly used m text books.

References

(l) Shannon, Claude E.; Communications Theory of Secrecy Systems. Bell System Technical Journal, volume 28 (1949). (2) Friedman, William F.; Elements of Cryntanilysis

Elements of Cryptanalysis, Aegean Park Press: U.S.A. (1976).

(3) Data Encryption Standard. FIPS PUB 46 National Bureau of Standards, Washington DC, U.S.A. (1977).

DC, USAN, (1971).
(4) Rivest, R.L. and Shamir, A. and Adleman, J.; A Method for obtaining Digital Signatures and Public Key Cryptosystems. Communications of the ACM, volume 26, number 1 (1982).

(B)

ELECTRONIC AND MAGNETIC QUANTITIES.

Guantity	Symbol	SI Unit	Abbr	Quantity	Symbol .	Si Unit	Abbr
electric current	1	ampere	Α	refuctance	B, R _m	reciprocal henry	1/14
alectric charge quan- tay of electricity	0	coulomb	C	permeance phase displacement	4	henry	н
electric field strangth	ε	volt per metre	V/m	number of tuens on			
potential difference	U	yolt	v	winding	N		
electromotive force	ε	volt	V				
electric flux	nv.	coulomb	c	impedance	Z	ohen	Ω
capacitance	C	faced	F	reactance	x	phin	Ω
magnetic field strength	н	ampere per metre	Arm	quality factor	a		-
magnesic potential	$U_{\rm ro}$	ampere	A	admittance	Y	sigmens	S
difference	-110			susceptance	В	siemens	S
magnetomotive force	F Fm	ampere	Α.	active power	P	wa11	w
magnetic Nux density	A	resta	T	apparent power	S 1P21	watt	w
magnetic flux	0	weber	Wb				1- V
self inductance	i	henry	H	reactive power	Q. (Pat	watt	w
mutual inductance	M L12	heavy	н	wavelength	4 "	metre	m
coupling coefficient	k			frequency	v. 1	hertz	Hz
velocity of light in a	-	metre per second	mis	angular frequency	w	hertz	Hz
vacuum				penod	Ť	second	5
resistance	R	ohm	0	time constant	r	second	8
resistivity	D	ohm metre	9m	thermodynamic	r	kelvin	K
conductance	G	APPROPRIE	S	1071001-0040			
conductivity	k. y.(0)	siemens per metre	Sim	energy	E	joule	J

CUMULATIVE INDEX 1987

_	
Computers & microprocessors	Feedback in loudspeakers 5 19 Headphone amplilier 9 45 Integreted stereo amptiner 9 42
	Headphone amplifier 9 45
16 Vives CMOS DAM for CRA 10.53	Integreted stereo amplifier
16. Key input for MSY micros 947	Intarcom for motorcyclisis
32 Khyte pseudo ROM	Limiter for guitars 9 /2
4-bit D-A converter	Linkwitz Titters 5 30
16 Kbyre CMOS RAM for C64 10 83 16 Key input for MSX mores 9.47 16 Key input for MSX mores 9.47 16 Key input for MSX mores 9.47 17 Key input for MSX mores 9.50 17 Key input for MSX mores 9.50 17 Key input for MSX mores 9.50 18 Key input for MSX more for MSX mor	Integrated stars o amphifer 9.42
A-D converter for joysticks . 9 60	Low-noise microphone presmplifier 11.24
BASIC computer	MW receiver 1.53
Bidiractional parallel for C64 9 124	Patch cetcher 9.77
Bidirectional serial-parallel converter	SMD headphone amplifier 8 71
Bus direction edd-on for MSX extensions	ST
Communication progrem for C64	Single-chip 40 W amplifier 9 88
Computerscopa (2)	Stereo indicator
Digital to analogue convertar	Stereo presmplifier with
for I/O bus	tone controt 9 98
Discrete DAC	
Lucia Custos	
JUNIOr Synthesizer	
Lovel adentes les analogue sustaines 9 49	Generators, oscillators and test equipment
Lebt detector 9.38	Generators, oscillators and test equipment
Logic families 9.96	8-channel voltage display 9 80
Computerscopa (2) 2 51 Optidat to analogue convertar 1.21 For VO bas 3.87 Departe 8.87 Departe 8.87 Julion's Synthesizer 4.82 Lovel adaptor for paniogue joystics 3.48 Lovel adaptor for paniogue joystics 3.93 Lopin defector 9.38 Lopin formies 9.56 MSK attensions — 5.41 4.41	Analogue wettmeter
EPROM progremmer (1) 4 31	Autorenging digital multimeter
MSX extensions → 5	Capacitance meter
Logic familias 9.96	Senante votage despire 9 80
MSX extensions 4	Divider cascade 8 130
I/O and timer cartridge	Duty factor analyser 8.35
ROM/RAM card for Electron Plus One	Electronic balanca
Rem Extension for	Function generator
Quentum lesp	HC-based oscillators
Serial data converter 8 68	HCU/HCI-based oscillator . 9.09
Simple D-A converter 9 43	Instrumentation amplifier
Synchronization separetor 9.67	Logarithmic sweep generator
Text display on junior computer . 5.47	Low curent ammeter
Quentum lesp 8 108 Strail data converter 8 68 Simple D-A converter 9 43 Synchronuzation separetor 9.67 Text display on junior computer 5.47 The dr Compatter se e frequency counter 3.45	President executed excillator 9.75
34,50 Synchronization separator 3,67 Text display on jumor computer 5,47 The Jr Computer es e fraquency counter 3,46 Universel EPROM simulator 10,41 Universal control for atexper moters 2,31	Precision rectilier 9 84
Universal control for atepper motors 2.31	Praction recolher 99
	Sample & hold for
Domestic	analogue signals 8.127
Domestic	Simple sweep generator . 8 100
7-digit code lock	Spot sine-wave generator - 1 825
Autometic cer serial	Spot sine-wave generator — 2 7.33
Battery sever	Tempereture probe for DMM
Centre! heating control	True RMS meter
Current monitor and siarm	Two tone RF test oscillator
Drill Spaed control 1144	VLF add-on unit for oscilloscopes
Electronic send gless . 9 128	Variable Wien bridge oscilletor
7-degr code look	Wien bridge oscillator9 74
Formed conting for	
refrigerator 9 131	Informative articles
Forced cooling for refrigerator . 9 131 refrigerator . 9 131 refrigerator . 9 130 refrigerator . 9 110 sc. 20 refrigerator . 9 150 refrigerator . 9 105 refr	midiliotive articles
Headlight alarm	A tidal generator for energy end jobs 10 34
In car ioniser	Background to hollow-smitter technology 11 54
LCD panel meter 4 54	Bursting the bubble myth
Light sensitive trigger 9 79	Computer science's Holy Greif 12 48
Mains failura alarm	Digital signet processing
Starting-pistol simulator	Electronmicroscopy comes to life
Synchronized slide changer . 9.109	Hexi cell to beat battery weight
Talaphone light 9114	How goes the numan computer works
Thermomater	Lacerc on overnew 8.29
Tollet pointar	Local area petworking . '5.44
	Measurement of ventricular distances
Audio & music	Mobile satcoms for the future 8 21
	Informative articles
AF waveform ganarator	Computers
Active phase-linear cross-over network	Noise at high frequencies
Audio line drive	RIMS: counting atoms 5 33
Active phase-linear cross-over network	Remote control in astronomy 4 28
Biginatism 4 39 20 20 20 20 20 20 20 20 20 20 20 20 20	Research colleboretion to boost IT 11.52
Compressor 986	Mobile salcoms for the future
Diorest audio relector 9 39	
Digital values control 9.61	answered
Flactronic noteoticmeters 5.22	scientists
	scientists

Secondary breakdown in power transistors	Fast sterting wiper delay 9 122
Secondary brankdown in power transistors \$25	Fost sterung waper delay. 9 1/22 Flashing lights. 9 83 Flashing rear light. 9 59 Halogan lamp dimmer 9 65 Hybord ossoode. 8 45
Softwere for the 88C computer — 2:	Flashing rear light
the BBC Buggy	Halogen lamp dimmer 9 85
Softwere for the BBC computar — 3.	Hadden High High
PC8 design	Infrared light gate
applogue arount decide 4.	Metal disector 8 de
Solar nower generation 8 24	Non interfaced picture
Speach racognition system from Marconi 6 39	for electron
Stream encryption 1D:28	PWM Driver for DC motors 9 126
Strachable convace mirrors	Power Switch for cars 9 107
Switch-mode power supplies	Section indication for model railway 9 48
Testing the instruments of tomorrow	Selective calling in CB radios
The Inmos transputer and Occam . 12.41	Speed control for DC motors
The birth of satellite communications	Timer for favors both 9.38
The deal for approximates	Timer for soldering iron
The digital audio tape recorder 12.34	Tracking window comparator 9.81
The future balongs to the photon	Wiper delay
The megnatic way to painless brain	
stimulefion	
The razor edge of the excimer laser . 3 25	Power supplies & ancillaries
The ultimate solid-stete memory? 4 24	Baffery charge / discharge
Where alectronic messages have the edge . 5 37	indicator
Where students make their own chips 8.42	Band-gep voltage raference 8.52
The UNIX operating system 12.38	satery central research 9.113 and cator 8.52 Current indicator 1st 723 8.53 Current monitor and alarm 9.53 Opined votings / Current display 9.55 Dimmer for inductive loads 12.43 High-current swift-brane regulator IC for 12.43
Communication security	Current monitor and starm 9 53
	Digital voltage/current display 9.35
	Dimmer for inductive loads
Design principles and ideas	
Design principles and ideas	Loss-free supply protector 9 86
ATN Filmnef decoder	Low voltage drop regulators
An introduction to DC power supplies . 6.47	Pracision power supply
Band-gap voltege reference 8 52	SMPSUs
Decoding satellite TV signals	
Decoupling in logic circuits	RF, infra-red & video
Driver IC Twee TD 47280 for class D	
Design principles and ideas	ATN Fathres decoder 8 4.4 Decoding setallists of signifies 6 4.2 Four-way serial swifch 976 Front-end for PM receiver 887 Front-end for SW receiver 8.33 High level possive DBM 3 51
Eight-channel multiplexer	Decoding antallita TV aignofs
Electronic pofentiomefers	Four-way serial switch 9 76
Field-effect optocoupler 3.37	Front-end for FM receiver
Filters theory and practice — 1	Front and for SW receiver
Filters-theory and practice · 2	High level wideband RF preamplifier
Filters-theory and practice 3 11.30	Indoor unit for satellite TV reception 2.21
HCI HCT beed excillator 9 89	MW receiver
High-current switching regulator IC	Morse filters
for SMPSUs	RF module for IDU
Linkwitz filters	SCART adapter for IBM PC
Low volfage drop regulators 9.82	SSB adapter 11.42
High-current switching regulator IC	Switchable bandselector
Micro-squeaker	
	Synthesizer for SW receiver 9.91
All and the Country Co	Synthesizer for SW receiver
Negative Supply from Popular supply 6.28	Synthesizer for SW receiver
Negative Supply from 6 28	Synthesizer for SW receiver 991 Top of the range preamplifier 1 44 Video distribution emplifier 9 118 Weather satellite interface 9 111
Negative Supply from 6 28	High Isee! wideband RF preampillier 9.24 Indoor unit for satellite TV aception 2.21 MW archiver 1.53 MW archiver 1.54 MW archiver 1.54 MW archiver 1.55 MW archiver 1.54
Negative Supply from 6 28	Synthesizer for SW receiver .9 91 Top of the range praemplifier 2 .1 44 Video distribution empirifier .9 118 Weisher satellite interface .9 111
Negative Supply from 6 28 Pontifus supply. 6 28 OpAmp-based current source. 8 58 Pierce oscillation .965 Precis motor spead regulator chip 12 46 Printad resistors 7.31	
Negative Supply from 6.28 OpAmp-based current source 9.58 OpAmp-based current source 9.55 Pierce docillator 9.95 Precisa motor spead regulator chip 12.46 Printad resistors 7.31 Simple 555 taster 6.41	Corrections
Negative Supply from 6 28 OpAmp-based current source 8 58 Pierce oscillator 9 65 Pierce oscillator 12 46 Precus motor speed regulator chip 12 46 Pinitid resistor 7.31 Simple 556 taster 0.41 The positive impedance converter 11 80	Corrections
Negative Supply from Poetilve Supply 1 6 28 OpAmp-based current Source . 8 58 Perice oscillation . 2 48 Printed resultor . 2 48 Printed resultor . 7 2 48 Simple 565 taste . 6.41 The postive impedance converer . 1 150 Transists rate of . 4 64 A 64 6 48	Corrections
Negative Supply from Poetive Supply. 6.28 OpAmp-based current source. 8.58 Perices cediblicities 9.55 Precisa motor spead regulator chip 1.2.46 Precisa motor spead regulator chip 3.1 Simple 565 taster 6.5 Transistor color call regulator 6.6 Transistor calls call regulator 6.6 Voltons from 6 steator 6.5 Voltons from 6 steator 6.52	Corrections
Negative Supply from Poetilve Supply 1 9.28 OpAmp-based current source . 8.58 Perice oscillator . 2.46 Printed resultor . 2.48 Printed resultor . 7.24 Find resultor . 6.41 The postive impedance converer . 11.50 Transists relater . 6.46 Transists relater . 6.46 Transists relater . 8.60 Voltage arcy desired . 8.52	Corrections
Low-house stephener your 20 2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Corrections
Negative Supply from Poetilve supply 9.28 OpAmp-based current source 8.58 Perice oscillator 3.00 Perice oscillator 3.00 Portice stations 7.23 Simple 565 tester 6.41 The postive impedance converter 11.50 Transists to size in converter 9.00 Voltage drop detector 6.52	Corrections
Poetitive supply	Corrections
Poetitive supply	Corrections
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Poetitive supply	16 Kbyle CMOS RAM for C64 11.80 Active bhase-finent cross-over network 12.72 Active bhase-finent cross-over network 2.72 3.74 Computerscope 374.572 772 (0.80 1.90
Positive supply	Corrections 18 Corrections

READERSHIP SURVEY

Since our last readership survey Elektor India has undergone a number of changes, brought about in the main as a result of your responses to the questions asked then, and also to keep

abreast of our fast changing electronics environment.

To make sure we remain on the right track, we ask all of you to take a few minutes to answer the 20 questions here by ticking the relevant box and post the completed form to:

Elektor Electronics Pvt. Ltd.

52-C, Proctor Road, BOMBAY-400 007

Tools

E	our likes and distike	99					Reading hebits		
	What ereas of elect to you? Audio & hi-fi Electrophonics TV & video HF/VHF/UHF		ere	of mo	st int	(1) (2) (3) (4)	11 On everage, how thoroughly do you ne Elektor India? all articles most articles a few articlee I only leaf through		(1) (2) (3) (4)
	Computers Computing science Telecommunication Test & measureme Domestic applications Hobby applications	nte ons				(6) (7) (8)	Could you estimate how many hours, average, you spend on this?	ours	
	low interesting do eatures?	wouldn'i nead religion to without	would like	Γm	would rather	waste af baset	12On averege, how thoroughly do you I et the advertisements? (check them all I look through most of them I study e few I only (eaf through I never look et them	00k	(1) (2) (3) (4)
4	Editorial News 8ook reviews		121	131	J4)	(5)	Could you estimate how much time, average, you spend on this?		
6	New products Letters						13 How do you usually obtain Elektor in	dia?	
8	Reviews of com- mercial equipment Constructional pro jects (nformative articles						on subscription from e newsagent from a specialist electronics shop borrowed from a friend, library		(1) (2) (3) (4)
(E) (c)	on lelectronics lelecommunication computing science liphysics general science		000000	00000	000000	000000	14If you are a subscriber, since when? recently about a year about two years about three years since it started in India		(1) (2) (3) (4) (5)
-	Buying habits						15if you buy your copy, how many other people read it?	r	
-	OWhat do you look Components Measuring equipm Computer hardwar Other commercial video, domestic,	ent e and equipr	softv	vare		(3)	just myself one other three others four others		(1) (2) (3) (4)
	Books					(5)	16How many other specialist periodical	s uo	

(6)

you read regularly

17 Why do you buy Elektor Indis? Appealing cover Professional appearance Interesting articles For the advertisements For the INFO care For want of something better Because I've read it for years For my hobby For my study For my scupation	(1) (2) (3) (4) (5) (6) (7) (8) (9)	19 Education in electronics: No formal qualifications Qualified technician Professionally qualified Corporate engineer 20 Give the name of your
Readership profila		Town
18Is electronics your profession? study? hobby?	(1) = (2) = (3)	City

State

(1) (2) (3) (4)

Please use this space for any further comments.

MULTI-FUNCTION FREQUENCY METER

An advanced, versatile and user-configurable test instrument capable of accurate measurement of frequency, frequency ratio and time interval. In addition to all this, it can be used as a period and event counter.

The multi-function test instrument described here is based on the 8-digit counter/timer IC Type ICM7226B from Intersil (GE/RCA). This chip combines all the functions expected from a good and versatile counter, and requires very few external components. The chip handles frequency measurement from DC to 10 MHz, period measurement from 0.5 us to 10 s, unit counting up to 10 million events, frequency ratio measurement, and time interval measurement. The inputs of the proposed instrument can accept a wide range of alternating (analogue) voltages as well as digital pulses at TTL or CMOS levels.

Circuit description

The circuit diagram of the frequency meet re jown in Fig. 1. It would be beyond the scope of this article to give a detailed description of the internal operation of the Internal operation of the INMIZER, and the following is, therefore, an outline of the simple perspheral circuitry needed to obtain a complete instrument. A prescaler for extending the input frequency range to 12 differ will be discussed in a forth-coming issue of Elektor India.

The ICMT226B has internal timebase circultry, display decoders, segment and digit drivers The 8-digit read-out is composed of common cathode 15D displays multiplexed at 500 Hz and a duty factor of 0.125 per digit. Leading (non significant) zeroes are blanked when the meter is set to frequency measurement in kife or period measurement in kife or period measurement in the SED Discours are not set to frequency measurement in kife or period measurement in kife or period measurement in kife or period measurement in kife or Discours at the SED Di



The counter, ICs, has an onchip timebase oscillator which operates at 10 MHz (X1). It is possible to use a 1 MHz quartz crystal provided Sn is closed. Similarly, \$7 makes it possible to apply an external clock signal of 100 kHz or more to pin 33. When switch Se is closed. the position of the decimal point on the display is controlled externally via the respective input, pin 20. The decimal point can thus be positioned as required for the prescaler used. Switches Se-Sr-Se and the associated diodes De-Dy-Ds, are intended for the

above options on the frequency meter, and may be omitted when the relevant function is not required. It is, of course, also possible to replace the switches with wire links for permanent operation in a particular

mode.

The maximum input frequency applied to input A of the instruents is 10 MHz in the frequency and unit count modes, and 2 MHz in the other modes. The counter modes and functions that can be selected with the RANGE SWIGH, Stab., and the PUNCTION SWIGH, Stab., and the marized in Table 1. Position 6 of

So is used for checking whether the internal oscillator works, but not for veryfing the frequency of oscillation. It should be noted that input it is only used for measuring frequency ratios and time intervals. The frequency of the signal applied to input A should be higher than that applied to B. Smillarly, the pulse transition on input A should occur before that on input B.

The protective networks fitted at the inputs of No and No enable applying alternating voltages as well as CMOS or TTL (digital) pulses. For small alternating voltages applied via C1-C2, diodes D1-D2 or D3-D4 do not have a limiting effect, so that inverters Ns-Ns operate as amplifiers. When the input amplitude is greater than about 2 Vpp, the inverters operate as buffers. Limiting of the input signal takes place when the input signal at the digital inputs is lower than -0.6 V or higher than +5.6 V. This means that AC coupled input voltages are clipped to about 6 Vpp. The input sensitivity stated in the circuit diagram is an average and frequency dependent value. When the Type 74HCT04 in position ICs (No...Nio incl.) is replaced with a 74HCU04, the input sensitivity increases by a lactor 6 to 10.

The circuit around Nr., Mo incl. and XOR gates Ns.Ns is used for measuring time intervals, i.e., the period that lapses between the positive edges of the signals applied to impus A and B. A bistable internal to the ICMT226B is set and reset by the pulse transitions at input A and B, respectively. When the bistable is set, the oscillator pulses are internally fed to the counter input. Evidently, the

	Switch S2: FUNCTION
Position	Function
1 (K1)	frequency (fa)
2 (K8)	period (TA)
3 (K2)	frequency ratio (fa/fs; fa > fs)
4 (K5)	time interval (fA-fB)
5 (K4)	unit counter
6 (K3)	oscillator test
	Switch S4. RANGE
Position	Accumulation time / cycle(s)
1 (K1)	0.01 s / 1 cycle
2 (K2)	0.1 s / 10 cycles
3 (K3)	1 s / 100 cycles
4 (K4)	10 s / 1.000 cycles

longer the bistable remains set,

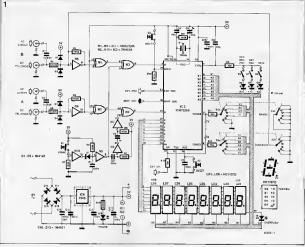


Fig. 1 Circuit diagram of the multi-function frequency meter.

the more pulses are counted. and the higher the read-out on the display. Push-button PRIME is pressed before measuring the time interval for a single Inverters N10-N8 event. generate a brief pulse for chip input A; Ns-N7 a slightly delayed pulse for input B. The internal logic in the ICM7226B is thus primed ready for measuring the interval for one event, delimited by the positive edges of the pulses applied to instrument input A and B. Pressing PRIME is not required when these inputs are driven with a repetitive signal, as the first alternating signal states cause automatic priming of the counter chip.

The read out can is retained ("frozen") as long as the Houb switch, Ss, is pressed. The counter's internal circuits—and hence the read-out—can be cleared at all times by pressing the RESET key, Ss. Capacitor Cr is connected in parallel with Ss

on. The 3 push buttons can be fitted on the counter's front panel as suggested in Fig. 2. The power supply for the frequency meter is of conventional design, and requires no further detailing.

Construction Virtually all parts shown in the

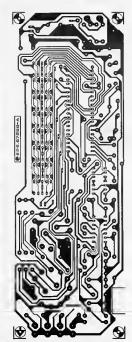
circuit diagram are fitted on a single printed circuit board, whose track layout and component mounting plan are shown in Fig. 3. Commence the construction with fitting all the ware links. Do not forget the 8 short ones underneath the displayed Electrolytic capacitor Cir is fit ted at the track side of the

board. Make sure that it is fitted

securely and slightly off the board to prevent sharp solder points pleroling the insulating material and causing short-circuits with the grounded metal can. It is recommended to use good quality sockets for all integrated circuits. The displays are also fitted in low-plays are also fitted in low-plays are sistens of the displays are sisting to It-way IC sockets. Use



to prevent hang-ups at power ! Fig. 2 Lay-out of the ready-made front penel foil for the frequency meter.



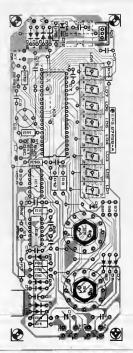


Fig. 3 Track layout and component overlay of the PCB for building the frequency meter. Capacitor C12 is fitted AT THE TRACK SIDE.

short lengths of strong wite to ensure the correct height of the eduplacy above the board, LED D₀ is a high brilliance type whose leads are lengthened make in top is level with the displays in the sockets. Wollage regulator ICG should be mounted with a heat-sink. The MANCE and FUNCTION switches, St and Sa, are soldered direct onto the board, or with short lengths of left over component.

wire, to minimize stray induct ance and capacitance. This measure effectively prevents unwanted effects such as in determinate illumination of digits ("ghosting"). As already stated, function switches So-So may not be required on the front parel of the instrument. In puts A and B are made in flange-type or single hole BMC sockets. Two more of these are required when it is intended to



required when it is intended to The completed printed circuit board (prototype version).

Parts list

Resistors (±5%): R1;R4;R15=330R R2;R3;R5;R7;Ra;R10=100K R6=150K R3;R12;R13;R14=10K R11=10M

Capacitors:
C1;C2=1y; 63 V (non-electrolytic)
C3=150n
C4=1n0
C5:C7;C10;C11;C13;C14:
C15=100n
C8;Ce=39p
C8=40p trimmel

C12=470pt 25 V
Sembonductors.

D1...Ds mcl.=1N4148
b0,07;09=1N4148 (see text)
De high billhance LED (red)
D10...D13 incl.=1N4001
D14= red LED
L1-744NCB0 or 74HCT06
IC2=74HCU04 or 74HCT04
IC3=ICM7226BIP,+
IC4=780D
LD1 LD5 incl.= common cethods LED display

H011D70 *

Miscellaneous
Si Si Si Si P push-to-make
button.
Si = 2-pole G-way lotary switch
for PCB mounting.
Si= 2-pole 4 oi 6-way lotary
switch for PCB mounting.
Si Si Si Si P miniature SPST
switch fisse P miniature SPST
switch fisse I series crystal.
TO 220 Style heart ink for IC4.
Visiobox enclosure Type
4775-1411.

PCB Type 87868 [svaliable through the Readers Services]. Fron1 panel foil Type 87286F (available through the Readers Services]. Meins transformer 8 V: 0.5 A Mains entience socket, fuse (100 mA), and DPDT switch. Knobs foi rotery switches. 9NC sockets as recurred.

* Aveilable from Universal Semiconductor Devices Limited • 17 Granville Court • Granville Road • Homsey • London N4 4EP. Tellaphone: 101 3841 9420. Tellap. 102 3841 9420. Tellap.

* Switch with adjustable stop, e.g. Lorin Type CK1045 (Cirks) stock no. 53-21045).

extend the frequency meter with the prescaler to be in-troduced. Inputs Ext. core, get to an and output surv car, can be made in suitable sockets on the rear panel of the enclosure. The signal at surv cer can be used for setting the oscillator fresh and the conflator fresh and the setting the conflator fresh and of returning the conflator fresh to the conflator fresh and of the setting the conflator fresh and for driving other circuits, provided the surve course, provided the surve course, provided the surve course, the setting the survey course, the setting the sett

The supply voltage for the prescaler is available on 2 soldering pins next to the extrementary or input.

or input. The completed PCB is mounted vertically in the moulded guides provided in the bottom plate of the Vero enclosure. The ready-made front panel foil for the frequency meter can be used as a template for drilling the metal front panel provided with the enclosure. The shafts of the rotary switches, S2 and St, are cut to size to enable fitting suitable knobs. The LED displays are fitted in a rectangular clearance cut in the front panel. The visibility of the read-out is enhanced by the semi-transparent bezel in the ready made front panel foil. The overflow indicator, Do. 15 fitted immediately below the

right hand side of the display bezel. The position of the various controls and indicators is evident from Figs. 2 and 4. It is, of course, possible to use a ready made mains adapter with 8 VAC output for powering the instrument. In many cases, this is safer and less expensive than incorporating a mains transformer. When it is still intended to furnish the frequency meter with its own, internal, mains supply, the mains socket and fuse (100 mA) should be fitted at safe locations onto the rear panel of the enclosure. The mains transformer should be preferably an 8 V. 0.5 A type. The current consumption of the circuit is about 65 mA with all displays blanked, and 175 mA with all displays illuminated. H

Reference:

Component Data Catalog 1987: ICM7226A/B p. 480 ff. Intersil GE/RCA International Limited Beech House • 373-399 London Road • Camberley • Surrey CU15 3HR. Telephone: (0275) 688911. Fax: (0276) 688285,

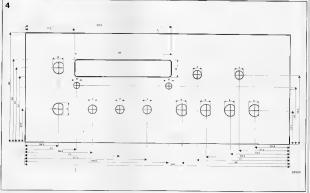


Fig. 4. Dillling diagram for the front penel. 1.44 elektor index penussy 1926

NUMBERS AND THE MACHINE

by C.H. Freeman

Camputer science depends largely an the praperties of real numbers. The carrect use of these requires an understanding of the mathematic basis of the real number system. Unfartunately, many peaple shy away fram anything mathematical, even if it has anly la da with numbers. This article attempts to allay these misgivings.

Modern man counts in base ten, that is, he uses the ten in-dividual symbols 0, 1, 2, ..., 8, 8. Obvious, you might say, but it hasn't always been so. Some races have been known to count in base 20 Gy using their toes and fingers in arithmetical operations) and the concept of zero (self is quite new; consequency, the Econats, on he and the country of the country of

$$No = d_nR^n + d_{n-1}R^{n-1} + \dots + d_1R^1 + d_0R^0$$

0

$$No = \sum_{r=0}^{n} d_r R^r$$

where d^1, d^2, \ldots, d_n are the decimal symbols in the couning system and R is the number base we are working in. When we count in decimal, the symbols used are $0, 1, 2, \ldots, 9$ and R, the base (or radix), is 10. Thus we can represent, say, 50468 as

$$5 \times 10^{4} + 0 \times 10^{3} + 4 \times 10^{2} + 6 \times 10^{1} + 5 \times 10^{0}$$

Now, although this suits us humans very nicely, digital systems do not use ten discrete values when representing numbers: the engineering problems introduced in using such a system would be too great. Instead we use base two, more commonly referred to as binary. The binary system has just two decimal digits in its counting system; 0 and 1. Now, this is handy because a switch. be it electronic, mechanical, hydraulic, pneumatic etc., can be either on or off and can thus be made to represent a single binary digit (or bit the deriva-

tion arising from Blnary dlgiT). So, using the above expression, a string of binary digits, such as 11001, can represent the decimal number

$$1 \times 2^{a} + 1 \times 2^{b} + 0 \times 2^{a} + 0 \times 2^{1} + 1 \times 2^{0} = 25$$

and we can now see how any positive decimal integer can be represented in binary, and also how to transform a binary number into its decimal equivalent. But what about translating decimal into binary? What do we do then? The answer is, we simply divide our decimal repeatedly by 2, recording the remainder at each stage of the division. The series of remainders, when read 'from the bottom up', form our binary number. Let's try an example, converting 229 into binary

thus 229 = 11100101. Check this for yourself by converting the number back into base 10. What we have just discovered about converting decimal to and from base two applies equally well to base 3, base 7,

about converting decimal to and from base two applies equally well to base 3, base 7, base 9 n, finact any base you care to name. Actually, two other bases, base 8 and base 16 (Rnown as oratif and hexadecimal respectively), are important, but more of this later, For the moment, though, you may well be wondering about base 16 (or hox as it is usually known Affer all base 16 will abse 16 will abse 16.

have I6 individual symbols in its counting system and after going through the symbols 0.9 we run out Mathematicians, when faced with problems such as these, invariably do the decent thing — and chear In this case the letters AF are pressed into service and the full counting system runs 8, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F.

Binary addition

Two binary numbers are added together in the same way two decimal numbers are added together. By adding together in dividual digits, paying due at tention to any carry digits generated. As there are just 2 digits in the binary system, there are 4 possible sums which can be formed. These are

	sum	carry	
0 + 0 =	0	0	
0 + 1 =	1	0	
1 + 0 =	1	0	
1 + 1 =	0	1	

Using this principle, we can generate a table of binary numbers alongside their decimal equivalents. Part of such a table for a 4 bit binary is shown in Table 1.

TABLE 1.

23	22	21	20	DECIMAL	HEX
0	0	0	0	0	0
0	0	0	1	1	1
0	0	1	0	2	2
0	0	1	1	3	2 3 4 5
0	1	0	0	4	4
0	1	0	1	5	5
					1 .
1	1	1	0	14	E
1	1	1	1	16	1 F

It is at this point that octal and hex become interesting. By collecting adjacent bits into groups of four, we can write just one hex digit in place of each group. To illustrate, consider

and it can be seen that hex notation can act as a useful 'shorttand' when writing long binary sequences. Note how we pad the number out with leading zeroes in order to obtain two 4 bit groupings. A similar technique can be adopted using octal by gathering bits into groups of three thus

59 = 111011 = 111 011 = 73 (octal)

Signed integer representation

So far we have considered binary representation of positive integers only. What happens if we want our computer to hold a negative integer? Our system has so far made no allowance for such eventualities so what can be done? Fortunately, three possibilities exist. They are

(a) sign-magnitude representation. This is the simplest possible

method and relies upon the fact that computers fold numbers in fixed length registers. These registers are usually 4, 8, 16 or 32 lits in length, but the import ant fact is that their length is constant. If we have an n bit register, we can use the most significant bit as an indicator (or FLAG) to represent a positive or negative number. It is usual for

elektor india january 1988 1.45

this bit to be set (i.e. I) when representing a negative number and reset (i.e. 0) when representing a positive. The rest of the n-1 bits hold the absolute value of the number. The greatest absolute value which can be held in such a register is 2n-1-1 so it follows that if a number is held in an n hit register in this form

 $range = 0 to + (2^{n-1}-1)$ inclusive

(b) diminished radix

complement. For an a digit number No in base R. we can form what is known as its diminished radix complement by applying the formula

$$DRC = (R'' - N_t) - 1$$

The name of the complement depends upon the base in which operations are being performed and takes the name of the highest decimal digit in the system. Thus the DRC of a decimal number is known as its nines complement, whilst that of a binary is referred to as its ones complement. With the above emiation 28 springboard, it is not difficult to show that the ones complement of any binary can be formed simply by inverting each bit, that is changing I to 0 and 0 to 1. For example,

0001 represents 1

1110 represents -1

Thus in an n bit system the greatest positive number will be held by only n-I of the bits. Therefore, the greatest positive number = $2^{n-1}-1$. The greatest negative will be represented by a 1 in the most significant bit followed by n-1 zeroes. Hence range = 0 to $\pm (2^{n-1}-1)$ inclusive

(c) radix complement representation.

The radix complement of an n digit number Nr in base R can be calculated using the equation

$$RC = R^n - N_x$$

and the radix complement of a binary number is referred to as its twos complement. It should be clear that adding a number to its twos complement will r result in all zeroes plus an overflow carry. If the system in use ignores any digits in excess of n then the above equation reduces to

$$RC = -N_t$$

in other words, the radix complement represents the negative of a number in the same number of bits. Computer circuitry can easily

form a twos complement by firstly inverting all the bits of the number (to obtain a ones complement) and then simply adding I to the least significant bit. For us mortals there exists an easier method of translating a binary into its twos complement. Starting with the least significant bit, we copy all the bits in the number up to and including the first occurrence of one. The remaining bits are then inverted. Table 2 shows comparative representations for a 4 bit recoster. Note that in the case of twos complement representation

range = $-(2^{n-1})$ to (2n-1-1) inclusive

and the minimum negative number cannot be negated.

Why bother?

TABLE 2.

1011

1010

1001

1000 8

10

9

-5

-6

If all this seems as if it is merely some abstract mathematical stuff, then let me assure you that it is not. All this maths has a very practical consideration in the design of computer hardware. You see, it is easy to build circuitry which can perform inversion of a binary and addition of This looks more complicated

ly. This means that the process of subtracting one binary number from another is invariably reduced to two distinct operations: forming the complement of the subtrahend, and then adding this complement to the minuend. This leaves us with the decision as to which complement to use: ones complement or twos complement? If we choose to use twos complement, we simply add and then discard any carry which may arise from the most significant digit. If we use the ones complement, however, any such carry must be added to the least significant digit. If this generates further carry digits, they must be also added until further carries are generated. This end-aroundcarry means that arithmetic performed with the twos complement system is a much simpler business than all that mucking

two binaries, but it is far less i

simple to build circuitry which

can perform subtraction direct

representing negative numbers. Let's look at an example. subtracting 13 from 42 to leave 29: $42 = 101010 \quad 13 = 001101$ 29 = 011101

about in ones complement.

Consequently, twos comple-

ment is the method computers

will normally use when

Ones comp, of 13 = 110010. +1 = twos comp. = 110011 101010 + 110011 = 1011101

Discarding carry leaves 011101 = 29

then it actually is. To a person, performing such a process seems quite alien, but computer circuitry finds the process beautifully simple. And speaking of simplicity, the world of numbers is not limited to the simple system of integers. We must now examine how we can represent the system of natural numbers in binary

The real world

In our earlier look at binary numbers we saw how an n digit integer, No, in base R could be represented in the following manner:

 $No = d_nR^n + d_{n-1}R^{n-1} +$ $+ \dots + d_1R^1 + d_0R^0$ We now extend this to enable

us to represent any finite length real number using the following representation: $No = d_nR^n + \ldots + d_1R^1 +$

 $+ d_0R^0 + d_{-1}R^{-1} + d_{-2}R^{-2} +$

Now, when we use binary to represent such a number, we are using $No = d_n \times 2^n + \dots d_1 \times 2^1 + \dots$

+ dox20 + d-(x2-1 + + d-2×2-2 + ... and it should be easy to see that

we can hold a binary fraction in a register, using the most sigmficant bit to represent 2-1,

MSB				
2^{-1}	2-2	2-3	2-4	etc.

REGISTER BIT PATTERN	POSITIVE INTEGERS	ONES COMPLEMENT	TWOS COMPLEMENT	SIGN MAGNITUDE	HEX	OCTAL
0111	7	+ 7	+7	÷7	7	2
0110	6	+6	+6	+6	6	,
0101	5	+6	+5	+5	5	
0100	4	+4	+4	+4	4	3
0011	3	+3	+3	+3	3	4
0010	2	+ 2	+2	+2	2	3
0001	1	+1	+1	1 1	1	
0000	0	+0	+0	+0	0	
1111	15	-0	-1	-7	2	17
1110	14	-1	~2	-6	F	16
1101	13	- 2	-3	-5	D	
1100	12	2		~ 0	D	15

-5

- 6

- 8

-3 В 13

-2 A 12

-1

-0

Table 3 shows a three-bit register holding binary fractions in just such a way, along with the decimal equivalent of its contents. Thus table also shows the method of converting a binary fraction into a decimal fraction. By Inspection, it should also be easy to see that such an in bit register can hold values in the range

0.0 to $(1.0 - 2^{-n})$ in steps of 2^{-n} .

Before going any further, we'll take a look at how we convert decimal fractions into binary fractions. Decimal integers, as we saw earlier, are converted into their binary equivalents by repeated division by two, recording the remainder at each stage. Decimal fractions, on the other hand, are repeatedly multiplied by two. At each stage, the resulting integer part is separated from its fractional part and forms a bit in the resulting binary. The process is better illustrated by example than by words so let's convert 0.375 into binary:

0.375 x2 0 + 0.750 x2 1 + 0.50 x2 1 + 0.0 process completed. 0.375 = 0.011 Try converting a few decimals

into their binary equivalents, and then convert them back into decimal again. You'll soon got the hairs of it how we know bow to convert a decimal real into a binary real let's think about how we can store such a number inside a compitier. If we think of a binary number as having two separate constituents, an integer part and a fractional part, then it would seem an obvious move to store such numbers in the following such numbers in the following such numbers in the following

M BITS	N BITS
INTEGER	FRACTION.
PART	PART

way:

leading us to suppose we can store numbers in steps of 2^{-N}. In fact, this is a very poor way to store numbers. When very large or very small values are represented in such a way, it is nowthat there can be wast amounts of wasted space. This is inevitable whenever such a fixed point system is adopted and a much better bet is...

TABLE 3

	BIT TTE	RN	SUM .	DECIMAL
0	0	0	0+0+0	0.000
0	0	1	0 + 0 + 2	0.125
0	1	0	0+2+0	0.250
0	1	1	0 + 2 + 2	0.375
1	0	0	2 + 0 + 0	0.500
1	0	1	2 + 0 + 2	0.625
1	1	0	2 + 2 + 0	0.750
1	1	1	2 + 2 + 2	0.875

Floating point representation

Floating point representation relies on the fact that any number in base R can be split into two parts, its mantissa, M, together with its corresponding exponent, E, and depicted as

$M \times R^E$

In decimal, of course, this corresponds to the familiar exponential notation (powers of ten) we all know and love. If we consider the binary number 101.1101 (for example) then it can be written in mantissa and exponent form as

101.1101×2¹ or 10110.1×2⁻³ or 0.1011101×2⁴

The last notation is, in fact, the usual method of writing binary reals, with the most significant set bit coming immediately after the decimal point. Such a number is said to be NOR-MALIZED. In point of fact this is almost exactly how computers do store real numbers within their memories, as two distinct series of bits representing mantissa and exponent. You've probably also noticed by now that numbers stored in this way will have the most significant bit of the mantissa apparently redundant (as it appears it will always be zero). This is no accldent. Negative as well as positive mantissas and exponents must be catered for and in such cases the mantissa is held in twos complement form, the most significant bit being taken as a sign bit. Remember that such a bit will usually be set if the number it represents is negative, and reset if the mantissa is positive. The exponent is also held in twos complement form (see below)

SIGN MANTISSA

SIGN EXPONENT

Let's try and illustrate this by considering a fictional computer holding numbers in two 4 bit recisters:

SIGN 2-1 2-2 2-3

SIGN 4 2 1

The mantissa, remember, is normalized so it can take the bit patterns (ignoring the sign bit and considering 2-1 as the most significant bit.

III, II0, I01, I00 corresponding to the decimals

0.875, 0.790, 0.625, 0.500 and for negative mantissas the possible range which can be stored is

-1.000, -0.875, -0.75, -0.625

and we get the range for our mantissa

-1.0, -0.875, -0.75, -0.625, 0, 0.5, 0.625, 0.75, 0.875 The exponent can take the

range +7 to -8 in steps of 1, of course. The numbers we can represent in our 'machine', therefore, will be

 $0.875 \times 2^2 = 112$

 $0.750 \times 2^7 = 96$

 $0.625 \times 2^{-8} = 5/2048$ $0.500 \times 2^{-8} \approx 4/2048$

0

 $-0.625 \times 2^{-8} = -5/2048$

 $-0.750 \times 2^{-8} = -6/2048$

- -0.875 × 2⁷ = -112

 $-1.000 \times 2^7 = -128$

Notice that because of the twos complement method of storing our mantissa there are two numbers which cannot be negated: the minimum positive real and the minimum negative real.

A 'proper' computer would, of course, use many more bits than 4 to represent numbers but the principle is exactly the same as that outlined for our 4 bit example above.

Range and accuracy It is clear, judging by the

example above, that there are some decimals which can never be represented exactly, for the reason that there simply aren't enough bits available to fit the number in. For example, 109 = 1101101

 $109 = 0.1101101 \times 2^{\gamma}$

But the binary equivalent is too big to fit into our 4 bit mantissa. In cases such as these there are two options open. We can simply 'chop off' (or TRUNCATE) the excess bits and store as

 $0.110 \times 2 = 96$

or we can ROUND the number up (or down accordingly) to

 $0.111 \times 2 = 112$

Whatever happens, it should be realized that there will invariably be a degree of error in computer arithmetic. Usually such errors present no big problems and can be allowed for.

As far as range is concerned, if

a machine stores numbers as M bit mantissas and N bit exponents, the greatest possible positive mantissa will be equal to

0.111 . . . 111

and will be equal to $1-2^{-(M-1)}$ and the greatest possible exponent will be given by $2^{N-1}-1$.

elektor india şanuary 1988 1.47

greatest positive number - $(1-2^{-(M-1)})\times(2^{N-1}-1)$

As far as the smallest positive real is concerned, the mantissa will equal 2^{-1} and the exponent will be $2^{-(2^{N-1})}$

Smallest positive number = $2^{-1} \times 2^{-(2^{N-1})}$

Try and work out the largest I and smallest negative reals which can be represented. So there you have it! Computer arithmetic is not just so much arcane theory, but is a fascinating branch of mathematics; a branch which is in constant daily use in fields as mit that we need them!

diverse as spacecraft navigation | to preparing and printing your gas bill. The modern world is so very heavily dependent upon computers that it is doubtful whether it could function without their assistance. Love them or loathe them, you've got to adThe author would like to acknowledge the help of Mr. G. Parkes, dept. of compute science, University of Hull, fo. his assistance in the preparation of this article.

multiple voltage supply

Many circuits using, for example, both opamps and logic circuits, require more than one supply voltage. The circuit described here is designed to supply four voltages of +12, +5, -7 and -12 volts, with a maximum current of 50, 300, 50 and again 50 mA

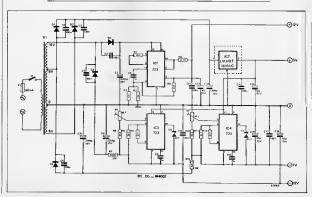
respectively. The positive supply voltages are produced in the normal fashion, using positive voltage regutator ICs; for the negative voltages it would be possible to use the special ICs which have been designed for this purpose, however these are both fairly expensive and often difficult to obtain. For this reason an alternative solution was sought. Although the 723 was designed for positive voltages, it can also be adapted for negative output voltages if, instead of being used as a seriesregulator, it is connected as a shunt stabiliser (IC3 and IC4).

Shunt stabilisers suffer from the dis-advantage that a constant power is taken from the mains transformer, irrespective of whether they are feeding a load. This

means that this type of circuit is not particularly efficient, however in this case, where the maximum current is only 50 mA, the power loss is negligible

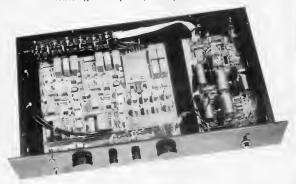
The negative output voltages can be adjusted by means of P1 and P2. After adjustment, the series-connected potentiometer and resistor can be replaced by two seriesconnected resistors. All the voltages supplied by the circuit are short-circuit proof; that is to say that shorting the outputs will not damage the supply. The positive outputs are provided with the usual current limiting, In the case of the shunt regulators for the negative voltages, the short-circuit current is determined by the dropper resistors R7 and R13. These should be rated at 2 W (or more) to prevent overlieating

Note that it will not always be necessary to use such a complicated transformer (8-0-8-16 V). If the 5 V supply does not have to deliver much current, a 0-8-16 V (i.e an 8-0-8 VI) transformer can be used. D2 and C2 are omitted in this case,



TOP-OF-THE-RANGE PREAMPLIFIER-3

This concluding part of the article deals with the construction of the preamplifier. Additionally, it gives a detailed discussion of the various types of capacitor, both myths and realities.



The preamplifier contains three printed-circuit boards: mother board, bus board, and supply board. The dimensions of the boards have been chosen to allow the unit fitting in a standard 19 inch cabinet with a height of 2 units (88 mm). The mains transformer is fitted in a separate alumnium enclosure. the dimensions of which are not critical. In addition to the PCBs two foils are available through our READERS SERVICES: one for the front panel and one for the rear panel.

High-quality components

It is important to use only highquality components to ensure optimum performance. All resistors should be metal film types with a tolerance of 1%, although that of Ry and Re should preferably be 0.1%. If these prove unobtainable, select a pair of 1% resistors that are

identical in value, or very nearly so, with the aid of a digital multimeter. All opamps are Type OP-27.

while the dual transistors are MAT-02s. Do not use the OP-37 in the line amplifier, because this type has off-set compensation only for gains greater than 14 dB. All capacitances in the signal

paths are formed from a parallel combination of an MKT and an MKP capacitor (M=metai: K= plastic; T=polytherephthalate; P=polypropylene). Frequencydetermining capacitors in the IEC compensation section (Ca. C10. C11) are 1% MKS (S=polystyrene) types. Electrolytic capacitors in the power supply are all PCB mounting types. Decoupling capacitors shunting electrolytic capacitors may be MKT or ceramic types. It is advisable to use silve?-

or gold-plated phono apput sockets: these quarantee freedom from oxidation and conse- relays

quent contact potentials between plug and socket.

The relays on the bus print must, of course, be of prime quality. Four possible types are shown on the component list. The excellent SDS type is unfortunately polarized, and its coil connections are exactly the reverse of the others: if this type is used, therefore, its coil connections must be reversed.

The volume control potentiometer must be of the highest quality: in the prototype a stereo version from Alps was used with excellent results. The balance potentiometers are

rather less critical, but should still be of very good quality: they should definitely not be carbon types, but conductive plastic or cermet. Bourns or Spectrol models are recommended.

switch direct voltages to the

A few tips to make the total cost come down somewhat. The OP-27 may be replaced by a 5534, which is a lot cheaper and still a good-quality device, but it may give off-set problems. The MAT-02 may be replaced by an LM394, but the overall quality will come down slightly. In this context, if moving-coil pick-ups are unlikely to be used, only one MAT02 per channel is required as already explained in Part 2. Cost reductions on the capacitors should be well considered: whatever you do, never use electrolytic capacitors in the signal paths - at the very least MKT types should be used

Construction

The mains transformer, which can either be of the laminated or of the toroidal type, should The switches are not critical components, since they only be mounted in an alumnnum case (see Fig. 13). From one end of this case the-non-earthed-

elektor indie jenuery 1388 1.49

mains cable should emerge, and from the other a fairly heavy three-core cable terminated into a suitable plug. This plug mates with a corresponding three-pin socket at the rear of the preamplifier enclosure. This arrangement is absolutely essential to keep any hum from the preamphfier circuits.

Next, the supply board should be completed. The voltage regulators should be fitted onto adequate heat sinks, which can be fitted to the board with selftapping screws.

When the board is completed. it can be mounted at the nighthand side of the enclosure. Do not forget a screen between it and the mother board. The alternating voltage from the mains transformer is taken to the board via the double pole mains switch.

Mains on-off indicator Dig should now also be connected

to the supply board. The earth connection on the supply board is then connected to the enclosure via a short length of heavy duty cable.

The supply may then be switched on to test whether the direct voltages are present; if so, they should be set to + 18 V with the aid of the two presets

_P, and P. The bus board can be complete fairly quickly. First screw all the phono sockets to the board (inputs at the track side). Tighten them by hand and then solder them lightly to the board: this prevents them coming loose when later the conesponding plugs are withdrawn and

plugged in again. Then tighten the socket nuts with a suitable spanner. After that all other components, including the relays, can be fitted onto the hoard Some resistors are soldered direct to the centre terminal of the sockets

The connections between socket and board at the tape and line outputs are made with a short length of equipment wire. The remaining connections are provided with soldening pins to make them easily accessible during the re-

mainder of the work. Remove any resin from the board with a brush dipped into white spirit or alcohol, and then seal the track side with a suitable plastic spray. Take care that no spray gets into the sockets or relays. This cleaning and insulating of the board reduces the risk of cross-talk to a minimism

The board is then mounted to the rear panel of the enclosure with the aid of insulated spacers: this obviates any possibility of the tracks or sockets touching the enclosure.

The earth connection adjacent to the sockets must be connected securely to the enclosure to become the case earth: this same point should be connected to the central earth point on the supply board via a short length of cable.

The mother board should be completed in the following order: resistors, capacitors; mechanical parts: semiconductors. Make sure that noninsulated capacitors (if at all used) can not touch the screening at the top Do not use

sockets for the ICs. At the front of the board, three supply rails have to be provided. To do this, first fit soldering pins in all the holes: then cut narrow strips of brass or tin sheet, and solder these to

Parts list (Fig 15) Components for one channel only are given the corresponding ones for the other channel (dashed | in the

drawingst are, of course, identical Resistors tall metal film).

B. - 208F R2 = 49R9F

R₃=100RF Re Res = 1KOF B1=49K9F

B4 = 150RF Rr, Re = 1K5F Rs;R14,R11 - 392RF

Ru = 348RF R14 = 3K48F R14 = 3K16F Ru = 22K1F

B. . - 1K218 Ru = 16866 R1a, R41; R43 = 2K2F

R19 = 121KF Ras - 475KF R21: R32 = 20KF

Rzz, Rss = 15KF Baa = 4K75F B24 = 3K92F

Rea Rea Rea : 1MOF Bu = 475KF Ray # 27K4F

R23 = 182RF Res(Res(Res(Res(Res(Res,Res) Bu = 10RJ

Ber. Baz = 22B.1 R11 = 6K8.I Rev Rev Rev - tok F

Box = 100KB Pi = 10KJ log potentiometri te a Rouges Recognice1t

Pz=10KJ log stereo pot meter le.g. Alps RKGA-2 10k AX2**I

Capacitors C1 = 220pJ polystyrene Cz. Ca = 100pJ polystyrene Ca = 47pJ polystyrene Cs; Cs; C12; C14; Cs0 = 10 st. J MKT Cr:C12:C41 = 447.J MKP C4 = 10n0J polystyrene Cs; Crs = 330n0F polystyrene C14 = 1n0F Cvs = 2u2.1 MKP

C14:C12 = 478nJ MKT

C14 = 100µM 3 V, tantalum electrolyte Cast Cast, Cast, Cast, Cast, Cast Cast

C44, C41; C44, C41 = 220nJ MKT C41:C23.C24 = 100aM: 25 V Czz, Cas, Czs; Czz, Cas, Cas, Cas, Cas, C19 = 1000uM 25 V Cee = 100nJ MKT

 $C_{50}; C_{51}, C_{54}; C_{52}, C_{54}, C_{55} = 22n0K$ Semiconductors

Dr = 1N4148 D₄ = LED red T1, T2, T1 = MAT 02 IC1.IC2.IC4.IC4 - OP 27 1

Miscellaneous S1 = 2 pole DIP switch Sa = 8 pole DIP swetch Sa = SPST miniature Reg = sub-miniature PCB mounting

reley, 2 pole change-over; 12 V Is a Meiser M1-12 or M1B12H-Siemens W11V23102 A0006 A111 * *; Omron G2V-2; SDS DS2F M 12F 16 silver- or gold-plated phono chassis sockets PCB 96111-3

*Distributors in UK: Combridge Hi-Tok, telephone (02231213333 Sevenoake Jermyn Distribution: telephone (0732) 450144

Herlow STC Electronic Services; telephone (0279) 26777 "Arman Electronics, telephone 01-902 4321

+ + Electrovalue; telephone 107841 33603 or 051-432 4945 *May also be available from

Electromat telephone (05361 204585 General note many special audio components are available from Sage Audio, Bingley, West Yorkshire telephone (0274) 568647 or from

Kord Audio Products. The Green.

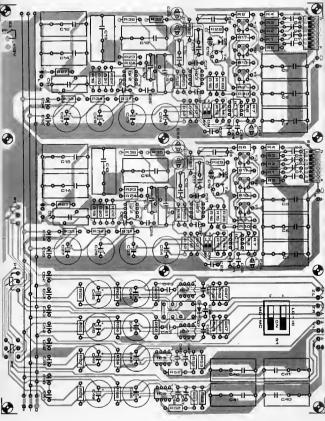
Nettleham, Lincoln, telephone 105221 750702



Fig. 13. The mains transformer must be housed in a separate metal case.



Fig. 14. Illustrating the three supply rails on the mother board.



15



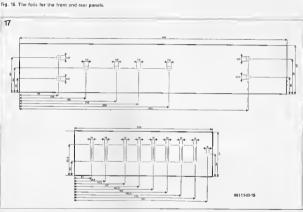


Fig. 17. Tempiate for drilling the tront and rear panels

the pins a few millimetres above the board (see Fig. 14). Next, all connecting points should be fitted with soldering pins. At this stage, only the pin for the earth connection to the supply board should be soldered to the screening laver at the top of the mother board. Finally, the board is cleaned. and its track side insulated with plastic spray, in the same manner as the bus board.

The mother board can then be mounted in the enclosure. All connections to switches and potentiometers can then be made, as can those between the mother and bus boards (at the right of the motherboard at the line section). Screened cable is not necessary for the latter, as these connections are only a few centimetres long. Next, the connections between

the supply and mother boards

are made. The switching connections to the bus board may be made from flat cable terminated at both ends into a plug to mate with the corresponding sockets on the boards. It should be noted that socket K1 on the bus board is fitted 180° different from the position shown in Fig. 4 on page 43 in the November issue of EE. In reality, pin 1 is located where pin 10 is shown.

Finally, the connections between the MC-MD sockets and the associated inputs at the mother board, and those between the MC-MD amplifier output on the mother board and the bus board should be made. These should be in good-quality screened audio cable or flexible coaxial cable (TV type). When all connections are made and checked, the mains may be

Technical specification

Input sensitivity

Phono: MC (low)

MC (high)

MD (low)

MD (ligh)

MD (high)

Tape, tuner, sux

MD (low)

Meximum input voltage at 1 kHz

IEC (RIAA) correction

± 0.2 dB over the frequency range of 20 Hz to 20 kHz. Standard input impedance. 47K, standard input capacitance: 50 pF. Values can be preset from 10B to 47K and from 50 pF to 500 pF.

Output (line out)
Nominal output voltage 1.2 V
Maximum output voltage 10 V
Output impedance <100R
Maximum output current 20 mA

Third-harmonic distortion

100 mV 12 V 10 V outnut voltage Phone, MC (low) < 8.1% < 0.01% < 0.02% MC (high) < 0.05% < 0.02% < 0.01% < 0.005% < 0.02% MD (low) MD (high) < 0.01% < 0.005% < 0.02% <0.005% < 0.02% Tape, tuner, aux < 0.005% < 0.005% <0.005% < 0.02%

(over range 20 Hz to 20 kHz and output voltage of 1.2 V) Phono; MC <0.02%

Taps, tuner, sux <0.008% CD <0.008%

MD

Intermodulation diatortion (60 Hz: 7 kHz; 41, SMPTE)

Tape, tuner, aux, CD < 0.003%

Signal-to-noise ratio

Phono: MC (low) >70 d8
MC (high) >76 d8
MD (low) >86 d8
MD (high) >92 d8
Tapa, tuner, aux >105 d8
CD >105 d8

Line amplifier (terminated into 47K)

Frequency range 10 Hz = 50 kHz (± 0.1 dB) 1.5 Hz = 500 kHz (= 3 dB)

Phase charactenstic < ±0.5° (15 Hz - 120 kHz) Cross-talk (at 10 kHz)

line inputs (L+−R) <−70 dB L/R to other inputs <−90 dB Slew rate >4 V/µs

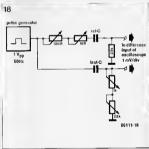


Fig. 18. Circuit for making comparative measurements of different types of capacitor.

switched on. Adjust P₁ and P₂ to obtain exactly ±18.5 V on the supply rails on the mother board.

Next, measure the direct voltage at the output (pn 6) of the LF411 (ICs), this should not be more negative than -14 V. If it is, lower the value of Ris till the reading is -14 V. This voltage depends to a large extent on what make of input transistors is used; normally, Richard and the control of the need not be altered from the

stated value. As a safety check, measure the direct voltage at the output (pin 6) of IC₂: this should be not more than 5 mV, and preferably 0 V.

The preamplifier should amply

meet the specifications given earlier, which are minimum values. The prototypes exceeded the figures given in almost all cases: for instance, distortion measurements gave values that were only about half the figures stated.

elektor india penuary 1989 1.53

ELEKTOR INDIA TEST EQUIPMENT



issue, this month we start a regular series of reviews of a variety of test and measurement equipment. The series starts with a review of a number of dual-trace oscilloscopes, and will continue with storage oscilloscopes, signal generators, power supplies, multimeters, frequency counters. pulse generators, LCR meters. and more. Since it is, however. appreciated that many readers schools and small workshops, laboratories and electronic design centres remain Interested in constructing some test equipment themselves as an attractive alternative to the more costly commercial equipment, we thought it helpful to remind you all of the number of test equipment projects that have been published in Elektor India over the past few years. The accompanying photograph shows that the majority of the Elektor India instruments are housed in a standard Verobox enclosure, which makes for a neat and uniform appearance.

A comprehensive series

Shown to the left in the photograph is the LCR Meter on top of the Computerscope, Below in the centre stack is the Loudspeaker Impedance Meier. Then come the Microprocessor Controlled Frequency Meter. the True RMS meter, the Digital Sine-wave Generator. The 2channel and standard, singlechannel, version of the VLF Add-on Unit for Oscilloscopes seen on top of the stack are housed in flat Verobox enclosures. The right-hand stack is composed of the Pulse Generator at the bottom, supporting the Digital Capacitance Meter, the Dual Variable Power Supply. the Function Generator, and the Spot Sine Wave Generator. Seen in front are, from the left to the right, the Altimeter/Barometer. the Autoranging Digital Multimeter, and the Temperature Probe plugged into a DMM. No attempt was made to photograph all published items related to electronic test and

measurement-not shown for

instance, are the analogue Capacitance Meter, the Audio Sweep Generator, and a host of smaller projects for testing components, AF, RF and digital circuite

Overview of publications:

- Audio Sweep Generator. December 1985 ■ Autoranging Digital Multi-
- meter: July 1987. ■ Capacitance meter (digital):
- March 1984 ■ Capacitance meter (ana-
- loque): June 1987. ■ Computerscope: November
- 1988 and February 1987. ■ Digital Sine-wave Generator. March 1987 ■ Function Generator: January
- 1985
- Loudspeaker Impedance Meter: October 1986.
- Microprocessor-controlled Frequency Meter. February 1985, and March 1985

- Pulse Generator, May 1984. ■ RLC Meter: March 1985.
- Spot Sine-Wave Generator: June 1987 and July 1987.
- Temperature Probe DMM: January 1987.
- True RMS Meter, January
- Variable Dual Power Supply: May 1986
- VLF Add-on Unit for

what's watt?

The most "interesting" figures on the specification list of an audio power amplifier are those relating to the frated output power. Final article reviews the various kinds of wat that one can meet in a specification. Since the purpose of using the amplifier is to reproduce music at a 'correct' level, it will also be necessary to consider the efficiency of the loudspeakers that are to be driven.

The assumption is correct, up to a point - the point at which the loudspeaker becomes the factor limiting a further increase in the 'undistorted' sound pressure.

Whichever factor sets the limit, there comes a setting of the gain control at which the reproduction is no longer 'undistorted'. Some listeners immediately detect this as a 'rough edge' to the loud music passages, others actually like the effect — and happily turn the 'fi' up 'thi'ar

When the system really saturates (so that there is quite unmistakable severe distortion) the usual reaction is to assume that the power amplifier is 'clipping'. That may well be - but it 'ain't necessarily so'. The discovery is invariably made too late, after an investment in new parts or in a new ready-built amplifier of higher rating has failed to noticeably increase the available 'racket'. What has happened is that more watts have become available for heating up the speaker's drive-coil (and possibly tearing the cone loose from its moorings). This can easily mean a further considerable investment - and this time without a trade-in!

It is one of the physical facts of life that a high quality loudspeaker of reasonable dimensions inevitably has an efficiency — i.e. the ratio of acoustic watts delivered to electrical watts consumed in the order of 1 . . . 5%. The balance is simply waste heat!

The distortion in the sound radiation from a loudspeaker, as a function of the applied drive-power, is a difficult thing to measure. One therefore rarely finds figures on this in the manufacturer's published specification. The situation regarding permissible drive power seems to be this, there are two limiting factors to the drive power a given loudspeaker will 'accept'; there is the instantaneous neak power input at which saturationdistortion or even actual mechanical damage will occur, and there is a considerably lower continuous power level (certainly in the case of mid-range and tweeter units) at which the continuous had production causes the maximum allowable temperature rise in the 'motor' (i.e. the moving coll). An measurement with a steady snewave as loudspeaker drive will encounter the later limit fairs, so that some kind of 'tone burst' seems to be required. The later limit source and the later limit fairs, so that some kind of the limit together which the later limit of the lat

permissible amplifier rating!

Manufacturem would clearly prefer a standardised procedure that would anable dissimilar units to be compared by prospective users. Presently used test by prospective users. Presently used test (Froquency-weighting) a widehoad notes signal until its spectral power-density (both 'fansatransous peak' and 'continuous') corresponds to that of 'type-al' music (whatever that may be). This solves the maximum-power problem enterly — but not the distortion-measure—

ment one. If the customer is going to use a power amplifier capable of overheating (or mechanically overdriving) any of the loudspeakers in the system, he will simply have to refrain from doing silly things with the volume and tone controls. Damage rately occurs before severely-distorted reproduction has given fair warning.

Amplifier sinewave rating

The amplifier's 'continuous' or 'sine-were 'arting fis, to put it crudely, its heating-ability. The rating is obtained by having the amplifier deliver a real selection into its rated load resistance — at a level into its rated load resistance — at a level into its rated load resistance — at a level into its rated load resistance in the from the input waveform (i.e. a specifical amount of distortion) is caused by non-linearities in the output circuit. Manufactures normally specify a level that the worst product made (due to component tolerances etc.) will reliably component tolerances etc.) will reliably

A stereo power amplifier is invariably

It is rather too easy to manipulate with the 'watts' in the power-amplifier specification to produce an inflated rating. The idea is to sell a relatively low-power job to the unwary - at a high-power price - on the premise that all the fellow wants is a higher wattrating than the chap next door. This technique has been developed to such a fine art that there are even protests from within the industry - from responsible manufacturers and their assiociations - hampered, unfortunately, by the fact that it is not immediately obvious just what is a realistic wattratine.

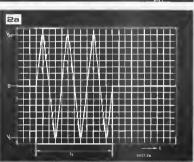
Well, . . . what's watt?

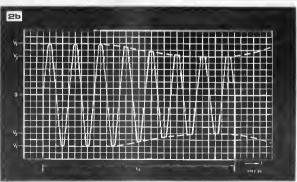
It seems logical to assume that the maximum 'undistorted' sound level a given combination of amplifier and loudspeakers will produce depends on the maximum amount of 'undistorted' drive power available.





Figure 2. When the duty cycle of the Input signel, in this case a tone burst, a sufficiently intoled due to the quescent apply voltage V₁ to the due to the quescent apply voltage V₂ to the due to the quescent apply voltage V₃ with the supply time constant. The looper interval; 2 (2b) corresponds to a higher every expourage voltage V₂ voltage to felf (ultimately to "ull loop" V₂), so that the originally unclepted weerform become distribution or originally unclepted weerform become distribution of the Company week voltage voltag





1.56 elekter radia january 1988

rated on the basis of 'both channels driven' simultaneously — the situation that makes the severest demands on the nower supply circuits.

What one actually measures in this test is the maximum gverage power, equal to the product of 'effective' ('RMS') volttage across and 'RMS' current through the load resistor. The Root Mean Square value of a time-varying quantity is its mathematically-derived 'effective value': the value of a steady direct voltage or current of the same heating ability. The intermediate values within a representative time-interval are 'squared', then the squares are 'meaned' (averaged) and the 'root' of this average taken as the result ('the root of the mean of the squares'). For a sinewaye the RMS value is known to be 'one over root two' (about 0.71) times the neak value.

One occasionally encounters a 'continuous peak' power rating. It is the product of peak voltage and peak current (i.e. 'squarewave power') and is precisely twice the sinewave rating – its only claim to (commercial!) merit.

The value of a 'continuous' rating is that it enables one to make objective comparisons between different amplifiers, it also provides a 'raference' output level at which a distortion measurement (necessarily a steady-state operation) can be carried out, Assuring that the system limitation is not in the loud-speakers, since if it were the whole matter would become mather complicated, the question can be raised to what extent is the sinewaye power with extent is the sinewaye power and the provided of the p

The waveform of a music signal is rarely even remotely similar to a sine waveform. The ratio of peak value to RMS value (the 'crest factor') can exceed 15 dB for much of the programme, depending of course on the kind of music involved and on the extent to which dynamic-range compression has been applied during recording and transmission. When the music signal is driving the amplifier momentarily just to its peak output (i.e. genuinely undistorted full-drive), one may assume that the average power delivered will be well below the amplifier's continuous rating. Let us not complicate matters by trying to account for the effect of current limiters in the output stage. The simple situation is that the amplifier's peak power capability is determined by the momentarily available supply voltage. There will come a point (see figure 1) at which the 'on' transistor 'bangs Its head' against the supply rail - the waveform being flattened ('clipped') by the inability to go higher.

Music power rating

The specification sheets of many commercial amplifiers give not only the continuous power rating, but also the 'music power'. This latter figure is then always higher than the continuous figure. The music power rating does not follow from any standard measurement procedure; it is samply an indication by the manufacturer of the output power his amplifier will momentarily deliver (i.e. during instantaneous peaks in the music signal)

one must therefore be careful when comparing amphiliers on the basis of their music power ratings. On the other hand, the rating is quite relevant to the unit's performance in a practical situation and cannot be dismissed as a mere commercial truck.

The essence of the 'music power' concept derives from what (walt) happens when the amplifier's nower supply circuit is not voltage-regulated. The situation is that an undriven class B output stage draws only a relatively small standing or 'quiescent' supply current. so that the fairly hefty reservoir capacitor has no difficulty in providing an almost ripple-free feed voltage close to the peak value of the transformer secondary 'open' voltage. When drive is applied there will be a lendency for the feed voltage to drop (and for the ripple to increase) - causing the 'clipping level' of the amplifier to fall. This process takes time however (because of the aforementioned hefty reservoir capacitor) - so that a momentary full power demand will be met at full voltage. Only when the average demand becomes appreciable will the supply voltage reduction noticeably reduce the available output power. Note that the relative power reduction is roughly proportional to the square of the relative voltage reduction, because a reduced voltage swing mevitably means also a reduced current swing - and the product of voltage and current is power. Figure 2 illustrates the on-load behavjour of the simple supply circuit of figure 3. Charge flows out of the reservoir at a rate proportional to the current demanded (charge is measured in ampère-seconds). The charge loss has to be made good by a surge-current, that occurs one hundred times per second,

Figure 3. A typical unresplated power supply circuits, in the case with symmetrical positive and negative output rails. The resistance shown as R is in fect due to 10he copper-resistance of the transformer windings and the warse resistance of the ractified nodes. At a given load demand, a larger value for the reservoir inscripticys will enable a longer Durd' to be met at full power — but the recovery time will then also be quantities.

whenever the instantaneous secondary voltage (minus the drop in the rectifier diodes) exceeds the voltage across the capacitor. The internal resistance of the rectifier circuit (actually the effective 'copper resistance' of the transformer windings) determines the magnitude of these surges - and therefore the drop in supply voltage that must occur with a given combination of capacitor value and load current. V₁ is the no-load (or hetter 'quiescent load') supply voltage, Va is the considerably lower full-load voltage (continuous full drive). The charging process occupies a greater part of the hundredth-of-a-second (mains half-wave) interval - and the voltage drops much faster during the full-load discharge process. It will not be difficult to see why power-electrolytics have a 'permissible ripple-current' rating in addition to their nominal capacitance! The designer of the power supply has to make a difficult choice here. A very low transformer winding resistance (both primary and secondary) will make for a very 'good' supply. It unfortunately also means a relatively bulky and expensive transformer - and a more violent

'ewitch-on' Note that providing electronic regulation of the power supply circuit will enable the 'continuous power' to be made equal to the 'music power' rating - but at the price of more transformer. more electrolytic and more heat sink! The only advantage of regulation is that the output stage can be continuously operated closer to the transistor voltage maxuma, without requiring allowances for mains voltage tolerances. In return for the hardware investment one obtains, in essence, that a power rating slightly higher than even the permissible 'music power' can be guaranteed under all load conditions. This may be justifiable under certain professional circumstances.

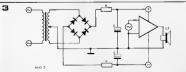
After all that . . . what's watt?

The 'continuous' and the 'music' power ratings of an amplifier give information that is relevant to the unit's ability to deliver an undistorted audio signal.

All other power ratings, such as 'square-

wave power', 'peak music power',
'±2 dB power' etc., reflect more upon
the abilities of the advertisement copywriter.

The amplifier's power rating is by no means the only parameter — or even the most important one — relevant to the enjoyment of undistorted music reproduction.



CHARGING/ DISCHARGING CURRENT METER

When the car battery is down without giving any prior intimation, you can always read it on the face of the car owner, trying desperately to start his car in the morning

Cursing the battery is not going to selve the problem It would have been a lot easier if there was a way to find out that the battery is getting discharged quickly. The electronics hobbyst can think of a solution for this problem, a simple micro ampere meter end a few components are enough for building the Chreging/Discharging Current Meer for your car

Current Meter for your car Figure 1 shows the construction and figure 2 shows the circuit of the epparetus. Our measuring circuit is connected in parallel with the sarching cable. A very small percentage of the current flowing between the minus pole of the battery and the permone setting count will.

pole of the battery and the common serthing point will now flow through our circuit Exactly how much percentage of the current flows through the meter is decided by the setting of the potentiometer P1.

The practical construction is shown in figure 1. The

potentiometer should not be left floating in air as shown in the illustration, but it should be fixed on a small bakelite piece. This bakelite strip can be fixed on to the meter liself through its screws.

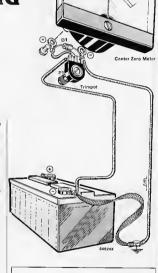
Make sure that no lights or any apparatus is connected directly to the minus pole of the battery. This must be so, because all the current being supplied by the battery, or being drawn by the battery during charging, must pass through the earthing cabe across which we have connected our measuring circuit.

As the meters available are of various different sizes,

Figure 1: A simple centre zero meter with a potentiomater and a diode is all that is required to build the charging/discharging current

charging/discharging current mater.

The circuit diagram of the charging/discharging current meter. The circuit is connected between the minus pole of thebattery and the common earthing point on the body of the cur. No lights or other apparatus should be connected directly at the muss pole of the battery.



Earthing Cable

everyone must think of his own procedure for lixing the meter and the circuit.

You can buy any center zero meter with 50-0-50 µA rating, that is, a meter which has a zero in the centre of the folial, 50 µA on left side full scale, and +50 µA on the right side full scale of the disl. As the accuracy required is not vary critical you can even use a cheaper centre zero mater used in radios and starense.

The left and right full scale points of the scale are to be marked with -30A and +30A

The center zero meter is a must because we want to indicate charging as well as discharging current from the battery. After completing all the connections, the ignition is

switched on For alignment. we can use the current that is used by the dimmers. First withall lights switched off the zaro adjustment is used to sat the needle of the meter to zero. Than the dimmer is switched on, knowing the power required by the dimmer is nacessary to calculate the currentdrawn, 45/40W systams ganerallydraw about 8.5 A in the dimmer position and 60/65 W systems generally draw about 11 A in dimmer position

of the current drawn, it is better to insert a paper between the ignition contacts during calibration of the meter, so that the current drawn is only the current for dimmers. The needle of the meter swings towards left, showing that the battery is supplying current. The potentiometer

To avoid misinterpretation

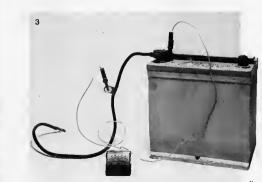
is adjusted so that the meter reads the known value in ampers on the dial marked form 0 to -30 on the left side

Now to see how the charging current is indicated by our metar, first remove the paper that was inserted batween the ignition contacts. Start the engine and kaap it running. The starting current is quite large but it does not damage the meter as we have connected a diode across the metar in our circuit. 7The diode is connected with cathode at plus pole and anode at the minus pole of the meter. The charging current can be checked even with a battery charger, with the minus pole of the charger connected to the common earthing point and the plus pole connected to the plus pole of the battery

Parts list

P1 = 1 K.f. trimpot with knob M1=± 50µA Center Zero meter D1 = 1N 4001 dlode

Figure 3.
Photograph of the charging/discharging surrent measuring sircuit



POWER AMPLIFIER

"I want to build a power amplifter for my cyclat"

"A power amplifier?"

"Yesl"

"For the bicycle?"

"Yes, I want more power from the dynamo, so that I can connect more lights to it, or I can get a more powerful headlight."

"Oh, If you think it was so easy, why no one else has thought of it before?"

"I don't understand myself, why no one else thought of it before "

"Because it is not practically possible. You can't amplify the power of the dynamo with an amplifier. You must install one more dynamo if you want more nower."

"But, with two dynamos, I have to work harder driving my bicycle."

"That's how it is. You cannot get more power out of anything without putting more power into it. Not avanfrom an amplifier."

"Than why do you call it a POWER AMPLIFIER?"

"An amplifier amplifies power, it does not generate power, it can amplify a week signal with the help of an additional power supply the signal from the record player or the cassette player is too weak to drive the loudspeaker, so it is amplified by the amplifier, and it draws the necessary power from the power supply."

"Exactly, something like that I need for my dynamo"

"Then you will also need a power supply for your

Dynamo Amplifier, and you will have to connect it to the mains!" $% \left(1\right) =\left(1\right) \left(1\right) \left($

"Oh, well, but if I could connect the mains supply to my cycle, I wouldn't need the dynamo either. I can connect the headlight diretly to the mains"

"You are right, moreover, the output power of an amplifier is much smaller than the input power."

"You mean power is lost in the amplifier?"

"Yes, a 90 + 90 W stereo amplifier takes about 320 W power from the mains and the remaining power is lost as heat."

"Heat is also power?"

"Naturally, power is required for generating heat"

"Now I understand, the power input is equal to the power output and the losses out together"

"Unless the device stores energy."

"Like an accumulator?"

"Yes, you are right, but avan in that case, the storad anergy is later given out by the accumulator. If you take this power output into consideration, the affective output will always be equal to the input."

"Does your stereo amplifiar always consuma 320 watts of power? That's a lot of power for an amplifier."

"No, it does not always consume that much power It is the specified power input when it is actually delivering the specified power input when it is extually delivering 90 + 90 Watts to the speakers, Generally it operates at much lower output power, and the power drawn from mains is also just what is required."

NEW PRODUCTS • NEW PRODUCTS • N

DIGITAL DISPLAY DD-3

"Ageel Enterprises has Introduced Digital Delay DD-3 for Entertainment & Orchestra Programme. This is analog type using BBD delay System. The delay time can be varied from 20 ms to 500 ms as nor enecifie requirement. For musical notes of longer duration long delay will be needed where as notes changing at a faster speed lesser delay time. All this is possible by controlling the 'DELAY' control and 'REPEAT' Control Microphones inputs have been

provided for Misc use. The Digital Delay DD-3 employ the latest and most advanced design and circuitory, Excellent performance and stability under extreme operating conditions and voltage fluctuations is ensured to maintain high quality and satisfaction for the user.

The Mixer can be put to varied uses. A good artist can achieve excellent sound effects by selection of various controls of the mixer

There are different models to suit different requirements (STEREO & MONO).



For further details please contact:

M/s AOFFI ENTERPRISES 404. Gali Matia Mahal. Jama Masiid Delhi 110.006 Phone: 267902

PROXIMITY SWITCHES

Hans Turck GmbH & Co. KG... situated at Mulheim in West Germany manufacture Inductive and Capacitive Proximity Switches

Proximity Switches with sensing distance upto 60mm. and with other technical parameters are available for use in every application.



For further information please contact: ARUN FLECTRONICS PVT ITD 2-E Court Chambers 35 New Marine Lines Bombay 400 020 Phone: 259207/252110 Telex: 11-6136 PREN IN

JOVSTICKS

"Oatec Pilot" computercompatible-joysticks are are reported to be only indegenously manufactured lovsticks for personal computers. The joystick is useful add-on for personal computer users in defining X. Y co-ordinates in CAD/ CAM programs, various controls, picture disposition and of course in spare time for playing games.

At present three types of joysticks ere manufactured. "Datec Pilot pc" for IBM

- PC & PC/XT compatible. " "Datec Pilot bb" for BBC & SCL Unicorn.
- * "Datec Pilot ap" for Apple Il computers.

The Datec joystics is indigenous. It does not need any interface and plugs directly to the game I/O port or the Analogin port of the computer. It is housed in a

sturdy cabinet and has sober colours to match computer environments. It has proportional-control and omni-directional capability. It has an autocentering mechanism and is built for easy handling and smooth operation.



For further information please contact: DATEC INDIA 3/23 Desai Building 83 Muchhat Lane Bombay 400 004 Phone 342787

LUXMETER

OPTO India has introduced sensitive and Portable. LUXMETER for measurement of light levels This is suiable for all photometric measurement in science and research as well as quality testing labs. Its response is claimed to meet with internationally accepted standard CIE observe's curve (equivalent to average-human eve response) with cosine



correction. The range of the Instruments are 0-1999 0.19990, 0.199900,

For further information nlease contact: AGARWAL SALES ENTERPRISE 34. Ganesh Bazar

Jhansi 284 002 PCR TERMINALS now Introduced PCB

Terminals which are specially designed for electronic Printed Circuit Boards, Named as Type MUT 2.5, these individuals can be stacked together for the required number to form a Multiway suitable for international standard module dimensions The connection is by soldering pins on the Printed Circuits Board and screw

Asia Electric Company have

clamning the wire termination. The size of the conductor is unto 2.5 sq. mm and is rated at 500V-15 Amns, The housing is moulded from special grade Industrial Polyomide.



For further information please contact;

ASIA ELECTRIC COMPANY Katara Mansion, 132A, Dr. Annie Besant Road, Worli Naka Bombay 400 018

NEW PRODUCTS • NEW PRODUCTS • N

MOTOR DRIVER

The L6202/03 is a high efficiency mixed technology motor drive IC (60V, 5A), MULTIPOWER 8CD is a new technology which combines bipolar, CMOS and POWER DMOS on the same chip. Both, technology and circuit have been developed by SGS.



For further information please contact:

M/s. SGS
SEMICONDUCTORS (PTE)
LTD.
28 Ang Ko Kio Industrial
Park 2, Singapore 2056

HIGH PRECISION DC

High Precision DC Shunts with accuracy class 0.2 calibrated on Micro-Processor based Test Banch is now available. Temperature stability in the order of 10 PPM ohms. Ratings upto 2000 Amps available, with 75 mV.



For further details please contact:
AUTOMATIC ELECTRIC LIMITED
Rectifier House, Wadala P. O. Box No. 7.130
Bombay 400 031
Phone 4129330
Telex 11-71548
1.64 leister and a pousey 1388

IDNAIRE

"Ionaire is an electronic negative-ionized-oxygen generator manufactured with knowhow from Innovative Systems, USA which creates a fresh. Invigorating and clean atmosphere by ionconditioning and cleansing the air of all pollutants and suspended particles. Health. giving lonized oxygen, which is depleted from the air due to various factors like pollution, is replenished by this device. Ionaire finds application in offices. photographic and other laboratories, computer rooms. homes restaurants hospitals clinics etc."



For further information please contact: M/s. ZEEBEETRONICS

M/s. ZEEBEETRONICS 16, Commissariet Road Bangalore-560 025 Phone: 572365

MARKEM PRINTER

The 527 system is designed for small production runs as well as special or pilot for applications. Capable of printing up to a 1" x 2" (25.4mm x 50.8mm) area, the 527 will mark your DIP's, card edge connectors and other large components having at least one flat surface, Print quality and registration are maintained.

by means of easy adjustments and a precision worktable assembly

Motor driven and actuated by a foot switch, the model 527 will cycle at rates of upto 3000 per hour. The ink plate system is compatible with the entire range of MARKEM inks and is extremely easy to clean.

Specifications: Imprint area 1" x 2" (25.4mm x 50.8mm), Max. part thickness 1:3/8" (34.92mm), Cycle rate Upto 3000 cycles/hour, Mount Bench, Weight (approx.) 35 lb (15.9 Kg.).



For further information please contact: KELLY CORPORATION 1413, Dalamal Tower Natiman Point Bombay-400 021 Phone: 244286 Teley: 11.8588 KELY IN

ELECTRONIC SOFTSTARTERS

Jeltron series I C1 manufactured in technical collaboration with M/S Motortonics, Inc. of U.S.A. are Low Cost High Performance Electronic Softstarters for three phase induction motors. The LC1 applies a gradually increasing voltage which in turn provides a smooth, stepless, adjustable acceleration at the time of starting the motor. A second starting ramp is available as an option for the applications where the moon has to start with different loads at different times. Also, for the

friction loads which stop too abruptly with a jerk, an adjustable smooth stop option is available which ramps down the applied voitage linearly in a predetermined time. The starting torques and start times for both the start ramps as well as the stop time are field settable. These starters are available for motors ranging from 2-700 HP.



For further information please contact:
M/s. JELTRON
INSTRUMENTS INDIA PVT. LTD,
G-3-190/2, Road No. 1
Banjara Hills
Hyderabad 500 034

AUTD RANGE PANEL METER

PRESTIGE ELECTRONICS introduce their Autornaping digital Panel Meter Display is 3% Digit 1.5mm Red, Green or Yellow, Range selection is automatic depending on input voltage, Ranges are 1.999, 1.9.99V, 4.9.99V, 4.9.99V & 750V DC overall securacy in 0.25% ± 1 Digit for DC & 0.7% ± 7 Digit for DC



For further details contact: PRESTIGE ELECTRONICS 62/A, Pushpa Park, Malad (E) Bombay 400 097 Tel: 693805

EW PRODUCTS • NEW PRODUCTS • NE

TAMAYA DIGITIZING AREA LINE METER

Planix 5000 Area Line Meter Works on a totally new concept developed through unconventional approach leading to unsurpassed performance standards.

The rotary encoder and the state of are electron (as makes Plank 8000, easiest, fastest area Line Meter. This Meter allows you to measure area and the langth of the line. The standard lines are easily measured by simply settling the trace point at each intersection of the figure and the rest is done by the built in computer with a resolution in computer with a resolution line sends to legislate of surveiline needs to legislate of surveiline eneeds to legislate of surveiline needs to legislate or surveiline needs to legisla

Planix 5000 is a TOTAL STATION for the draftsman, In addition to its own microprocessor, PLANIX 5000 will interface with the large computer or other RS-232C compatible units. PLANIX 5000 is a compact cordless instrument operating on NICI Batteries and comes in a carrying case,



For Further details please contact:

TOSHNI-TEK INTERNATIONAL 267 Kilpauk Garden Road Madras 600 010

SPIKEBUSTER

MAGNUM ELECTRIC COMPANY PVT. LTD, has introduced a voltage spike and noise suppression outlet strip called SPIKEBUSTER. It consists of an EMI/RFI filter and a voltage spike protection circuit built into

a power strip with three 5 Amp sockets and a control switch. By plugging SPIKEBUSTER into the electricity mains and your sensitive electronic amultoment into SPIKEBUSTER. electrical noise and voltage spikes are totally prevented from reaching the equipment and damaging it or causing it to malfunction. Uses are for colour TV sets, VCRs. computers, computer peripherals, medical equipment, electronic instruments, communication systems and other device containing sensitive integrated



circuits. The company

specialises in power protection

equipment and will soon be

coming out with a lowpriced

system aimed at the deskton

standby battery back-up

computer market

For further information write

MAGNUM ELECTRIC COMPANY PVT. LTO' 2 Ramavaram Road Manapakkam Madras 600 089

THICKNESS GAUGE General Tools offer a coating

Thickness Gauge. For measurement of a nonmagnatic coating on a Magnetic metal.

Application :— Measurement of following nonmagnetic coatings on magnatic metals.

 Plating~Gold, Copper, Zinc Tin Chromium, Lead etc.

Coating—Paint, Resinous coating, Metallic Coating.

 Lining—Resin, Rubber, Paper or any other films of non-magnetic material can be measured when placed on steel base metal.



For further details contact: THE GENERAL TOOLS CO.

7, Daulat Mansion Barrack Road, Behind Metro Cinema, Bombay 400 020

BARREL PUMP (HAND OPERATED) FOR CHEMICALS & DILS

A hand pump, in all plastic construction, namely Polypropylene (PP) and Thermoplastic Polyester (PBT), is introduced for the first time in India. It is ideally suitable for transfer of chemicals and oils from barrels, carbouys, jerry cans, jars etc.

The pump in PP is used for transfer of Aelds like Hydrochloric, Sulphuric (Upto 80%) Nitric (Upto 70%), Phosphoric, Aestic, Chromic, Spent Aelds etc. It is also used for Inorganic Salt Solutions, Hypochloric eand for Vegetable and Mineral Oils and certain Organic Amines.

The pump in PBT is used for all types of Aldehydes, Ketones, Glycols, Alcohols, Petroleum products and Oils, Acetone and Anilline and their derivatives, Benzene, Toluene, Xylene and their compounds, liquid perfumery products and pesticides, DDB, LAB, Hexane, Liquid Paraffin and other Acetates, Plastcisers, Chlorinated Solvents, Polyois, Isocyanates etc.

In general these pumps are ideally suitable for transfer of liquid chemicals and oils from barrels and carbouys. They offer suction lift of 3 mts, discharge heads of 15mts and capacity of 30 lpm.

They are extensively used at industries like chemicals, taxtile processing, pharmaceuticals, pasticides formulation, electronics, PCB Mnfg. supar mills, dye stuff mnfg., etching plants, degressing plants, research labb., offset presses, installation where oils, kerosene, diesel are used, and all other places where chemicals and oils are handled.



For further information please contact: CHEMINEERS 6 Jagnath Plot Rajkot 360 001 Gujarat State, India.

EW PRODUCTS • NEW PRODUCTS • NEW

DATA SCANNER

Advani-Oerlikon have developed a mini microprocess-based data scanner called UDS-30. This 30-point scanner is designed for scanning of temperature. voltage or any other parameters of water and steam boilers, windings of HP motors and high voltage transformers, distribution points in siles containing foodgrins engine test and reaction vassels in chemicals and process industries.

The system is field proven. versatile and compact. It is mounted in a standard RA 19 rack It can accent

multi-variable inputs such as Thermocoupies, RTDs and Analogues. The system has built-in 24 columns an alphanumeric 2 colous printer with re-rolling facility which gives out print out of scanned data and programmed parameters. The keyboard functions such as low level set point, control level set points, dwell time, high level set point, channel number. hysterisis, etc. are programmable individually for each channel. Display annunciation is provided for each channel. There are totally 90 LEDs, Each channel has a seperate Indication for alarm, senor break and control status. The system also has the facility to scan alarm conditions on a priority basis. Output relay contacts are provided for each channel One relay is provided for common alarm and one for senor break indication EEPROM memory is used and hence no battery back up is required for the programme. A real time calendar is also provided which gives date, month, year, day of the week and time. Nickel cadmium battery is provided for the back-up of the calendar. The system uses a floating point

and other mechanical calculations

Solid-state semiconductor switches are used for multiplexing thus contributing to reliability and compactness. STD cards are used for flexibility of oparation and ease of maintenance, thus ensuring minimal downtime. The plue-in PC8 and the STD mother board have minimised wiring in the Instrument. The unit has a bloded transparent unbreakable cover on the front space to avoid any accidental changes in the keyboard function.



For further information quote ref: PUB/2, contact: ADVANI-OFRLIKON LIMITED Post Box No. 1546 Bombay 400 001

SPECTRUM ANALYSER

ROFIN SINAR LASER UK LTD, announce the introduction of the high speed RSO 6240 Spectral Processor to operate with the current line of Optical Spectrum Analyser equipment. The new instrumant includes a more powerful processor, together with many systam improvements such as dual double-density double-sided 3%" disc drives, an improved monitor, and digitising electronics. The entire system has been

repackaged with an integral keyboard instead of the earlier seperate keyboard. In addition various accessories and software packages have been added to provide a very

powerful package to measure transmission, absorption. reflection and colour, in addition spectroradiometric and software package. Tha system captures a complete spectrum in 5 msec and stores it in 80 msec in the processor. The wavalenoth range is 200-5000 nm which can be covered at one time using the "marge" software facility.



For further information contact: TOSHNITEK INTERNATIONAL 267 Kilnsuk Garden Road Madras 600 010

THERMOCOUPLE VACUUM METER

The IBP Thermocouple Vacuum Meter is a simple. single head measuring device. SPECIFICATIONS Gauge Head: Chromium plated brass with octal socket. Vacuum Connections: Through standard 6 mm screwed union. Measuring Bange: 1-1000 Microns. Calibration: Calibrated for dry air using a Mclaod gauge. Power Supply: 230 Volts. 50 Hartz. ± 10%.

Dimensions: Small, compact construction with simple panal installation in Standard half module (H 135 mm x W 210 mm x D 145mm) Standard accessories supplied: Gauge head with cable of length 3 Metres. Applications: Used in Industrial Systems, Refrigeration Industry, Flask. Lamps, Capacitors and Condenser Industries etc.



For further information please contact: IBP CO. LIMITED A Govt. of India Enterprise Engineering Division Sewri (East) Bombay 400 015

"ROCKER TOGGLE" SWITCH

IEC has just introduced a range of "Rocker Toogle" switches with Black, Red. Blue, White, Yellow or Green colour knobs.

These Rocker Toggle switches are available in 6A, 10A, 15A. 250V AC or 28V DC in single and double pole with on off. changeover with or without centre off and momentary contact, to serve as Push Button, Special circuits are possible e.g. 1.2.3 or 1. 1+2 2+3, etc., avoiding the need of 2 or 3 switches.

Switches are supplied with screw terminals or push-in terminals (6,3mm).



For further information please contact: INDIAN ENGINEERING COMPANY Post Box 16551, Worli Naka Bombay 400 018.

classified ads

Instruments Transformers as per specifications. We can also design a single place for you. Contact. BEA Electronics, 57, Kanti Nagar Gwellor - 474,002, Ph. 24806.

AC/DC Solenoid magnate & Electromagnatic Clutches, Contect, BEA Elec-Linguist, Gwalior - 474 CO2, Ph. 24806

Avoid Delay - Get your Printed Circuit Boards designed and made from SHIV ENTERPRISES, P. Shagat Marg, Tukeram Nagar, Dombivali (E) - 421 201



Manufactured By

DESAL ELECTRONICS PVT_LTD

Voltage - - 63 VDC

Tan delta at -- 1 KHz - less Ihan 0 008
10 KHz - less Ihan 0 015

"JAY CHAMBERS" 501, Shivaji Road, Swargate, Pune 411 042. Ph.: (0212) 449148 Grams: DECTRONICS Tetex: 145-333 MCCI

advertisers' index

ABC ELECTRONICS	1-11
ABR ELECTRONICS	1-14
ABR ELECTRONICS ABR ELECTRONICS ACE COMPONENTS ADVANCE VIDEO LAB BALA JI ENGINEERING BMP MARKETING	1.73
ADVANCE VIDEO LAB	1.06
BALA JI ENGINEERING	1.08
BMP MARKETING	1.12
CHAMPION ELECTRONICS	1.76
	1.15
CYCLO COMPUTERS	1.04
DESAI ELECTRONICS	1-74
DI ELECTRIC	1.73
DYNATRON	
ELECTRONICS 1-10	1.14
ECONOMY ELECTRONICS EFFICIENT ELECTRONICS GRAFICA	1.10
EFFICIENT ELECTRONICS	1.10
GRAFICA	1.06
GRAFICA G.S. ELECTRONICS IEAP INSTRUMENT CONTROL	1-14
IEAP	1.14
INSTRUMENT CONTROL	1-08
JM ENTERPRISES	1-09
JM ENTERPRISES JR COMPUTER KIT JR COMPUTER BOOK KIRLOSKAR LEADER LOGIC PROBE MOTWANE PACIFIC PRECISION PLONEER ELECTRONICS PLASTART PRECIDIS KITS	1-71
JR COMPUTER BOOK	1-71
KIRLOSKAR	1.63
LEADER	1.04
LOGIC PROBE	1-16
MOTWANE	1.07
PACIFIC PRECISION	1-06
PIONEER ELECTRONICS	1-65
PLASTART	1.16
PRECIOUS KITS PRECIOUS BOOKS PRECISION INSTRUMENT PROMOTION	1-61
PRECIOUS BOOKS	1-67
PRECISION INSTRUMENT	1.08
PROMOTION SIFA ELECTRONIC SWASTIK TANTIA ELECTRONICS	1-04
SIFA ELECTRONIC	1-73
SWASTIK	1-13
TANTIA ELECTRONICS	1-12
	1-12
	1.73
VASAVI ELECTRONICS	1-16
VISHA ELECTRONICS	1.75

Printer & Publisher – C.R. Chandarana, 2, Koumari, 14th A Road, Khar, Bombay 400 052. Printed at Trupti Offset, 103 Vasan Udyog Bhavan, Tulsi Pipe Road, Lower Parel, Bombay 400 013.

Your Right Connection... Champian

The Champion Connectors

- o Single In-line IC Sockets o Pin Grld Arroy Sockets.
- - o Cage Jacks.

with hild thicking

Champion Electronics Pvt. Ltd.

S-17, M.I.D.C. Bhosan, Pune 411 026, India (0212) 82682, 83791 Cable : CHAMPION Telex 0146-343 CHMP IN 0145-333 MCCI IN 0145-505 MCCI IN