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The front cover diustrates a direct-coupled data circuit terminating equipment (DCE). popularly called a modem (contraction of modulator/ demodulator) and a tele phone the two essential units for linking a computer to the telephone network in this issue we describe what happens between transmitting and receiving computers that are linked by a telephone line Next month we after you the opportunity of building your own modern

Part one of the series on the BASIC PRINCIPLE" is featured in this issue. The remaining three parts will be published in our future issues.



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Had Electronics :

Electronics, the harbinger of a new technological revolution, sometimes meets with resistance from a misinformed section of the people who condemn any modernisation, fearing that it will cut into the existing job opportunities

A recent survey has thrown fresh light on the potential of electronics industry which is not well known even among the policy makers

An investment of about Rs. 1 crore in the field of electronics can create an estimated 3.12 jobs while the same outlay in chemical industry will provide only 33 jobs. Similar investment in ferrous products and automobile and beycele industries can give work for 66 and 91 people, respectively, according to the survey.

Electronics industry can also boast of the fact that it consumes meagre electricity and the capital required is comparatively less. The lixed assets for creating a job in electronics has been calculated to be around #8, 32,000 while in other industries, the assets should be at least four times more.

The importance of electronics industry has not been realised a day too early and the envisaged investment of Rs 500 crore by the government in this sector is welcome decision. The electronics production target for 1990 in India has been set at Rs 10,000 crores when the world production would have crossed Rs 74,000 crores To achieve the 1990 target, an investment of about Rs 2,000 crores is needed in India, according to experts A strong production base for the indigenous manufacture of components and policies such as elimination of the hurdensome indirect taxes on electronics will accelerate the growth of this industry at a laster rate

The Electrones Component Industries Association (ELCINA) has urged the government to prepare an integrated laseal and import policy package to evercome the difficulties caused by continuous changes in the import policy. Rationalisation of the customs duty structure and introduction of freight subsidy to step up export of electronic components are among the other suggestions made by the ELCINA to the government.

Computer Shops

Computer shops, which have the "flavour" of a coffee shop, will be set up soon in the country. All the requirements of computer users, irrespective of the type of computers and the brands used, are expected to be available under one roof.



Full computer systems, perphetals like indoppies, disc drives, printers, special computer furniture, software package, accordingtoning systems, computer from this shop. The new company, called "Computer Pont", is said to be modelling its project after the Computerand chain concept of the USA. The computer shops, with their beginning in Bombay will be set up before this and of the year in other hands of the control of the computer shops.

While manufacturers can use these shops as an assured outlet for their new products, computer users too have an advantage. They will have opportunities for companing offerent models in one place and the various models in one place and the various different systems can be easily verified. This is not loosable under the existing market conditions where the consumer is flooded with a barrage of offers, drowning him in confusion and delaying the decision.

Meanwhile, the government has exuded optimism in declaring that computer prices are tikely to fall futher as competition is growing among the newly licensed manufacturers. Over 140 units have been approved for the 140 units have been approved for the the mint -computers and microprocessor-based systems. As the technology for producing large computers is not available in the country, the depart-ment of electronics has floated global tenders let the purchase of the know-how The Electronics Corporation of Hold Elimited will undertable the pro-

duction programme. Though initially the ECIL will take up this job, depending on the demand, others may also be allowed to produce large conters.

Advisory Panel

A 25-member advisory council for electronics industry has been set up by the government of India Representatives from the industry, the planning commission and various ministries have been included as members

The council, apart from serving as a mediating agency between the government and the industry in solving mutual problems, will help in the formulation of th seventh five-year-plan for the industry, according to Mr. S.R. Vijayaker, secretary to the department of electronics.

Import of electronic components may be further curtailed during the seventh plan period. Steps are likely to be taken to limit the import of components to 10 per cent from the existing level of 26 per cent.

The government has also expressly recognised the reluctance on the part of foreign countries to sell the know-how as India planned one-time import of technology to be backed by indigenous R & D

The secretary also dispelled the fear that the department was imposing certain technologies on anyone. He defined centralisation of technology as a means to standardising components and lacilitating largescale production and it did not mean limited production. This clarification. comes in the wake of reports that the proposed centralised purchase of technologies by the department of electronics might hamper the private sectors' enthusiasm in the manufacture of telecommunication equipment, which has been thrown open to them recently

Struggling hard to enforce quality control on the colour television sets produced by the private sector in the country, the department of electronics has threatened to cancel the supply of colour TV tubes to those manufacturers who do not

the supply of colour TV tubes to those manufacturers who do not fulfill the "limited quality test". The department has aiready diluted its test control by switching ower to 48-hour-laboratory test called limited quality test from the 1,000-hour working of the set in the lab under the "comprehense quality test".

Medical Electronics

Production of medical electronic equipment, a much-neglected area, has caught the attention of the planners and the seventh plan envisages a production target of Rs 350 croses The target appears to be a tall order as in 1982 production of medical electronic equipment in the country hardly exceeded Rs 13 crores and in 1984, it may touch Rs 20 crores The estimated demand this year is put at Rs 45 crores.

The requirement of medical electronic equipment in 1985-86 is expected to be around Rs 76 crores and the indigenous production is likely to be about Rs 30 crores

The department of science and technology has estimated that a tentative investment of 18.60 crores has to be made to achieve the seventh plan target. More than all this, local manulacturers have to be given some protection "against the liberal import of these equipment through the ever-expanding list of "life saving devices" and the import policy has to be suitably amended it is felt.



Sunil Gaveskar Launches 'Diamond'



... and hands it over to the distributor

MVs Plastart introduced their four New Products — DIAMOND Desolder Pump & SUPER-81 T V Antenna Booster with unbreakable PVC moulded body VHF to UHF T V Channel converter and stand off insulators for T V Antenna Booster mounting that ensures loss-free gain. Well Known Cricketer Sunil Gavaskar launched these four products at a jam-packed function held on 25th August 1984, at Maratha Chamber of Commerce Hall,

PUNE Shri PS Deodhar. Chairman

ET & TDC Government of India,
presided over the function.

M/s Plastart are a constituent of the well-known Yaidya group of Industries of Pune and have been in electronics field for many years now, with genuine quality products to their credit.



Shri P.S. Deodher Inagurates Precious' Office

On the 9th of August 1984 Shir PS Deodhar, Chairman ET & TD C. Government of India. Inaugurated the new office of Precious Electronics Corporation & Elektronics Corporation Activates Building, 52 C Proctor Road. Bumbay 400 007 By taking this step. PRECIOUS: are now geared to give even better service to their valued patrons. The facilities now offered at the new office include a large counter for component sales.

A separate counter has been provided for Students, Hobbysiss & Technocrats where the magazine kiss. PCBs and data books are available Precious is a well known marketing house in electrons marketing house in electrons are thought to the provided of the provided by the pr

Electronic typewriters from Godrej For the first time in the country. Godrej and 80yce Manufacturing Co Ltd. has introduced a fully indigenous electronic typewriter.

The know-how for the new typewriter, which is marketed in three models, was developed by the company with the assistance of the Indian Institute of Technology, Bombay

The basic model (Model 11 has several features like eras ing centering, in carriage movement, margin setting tabulation and half spacing - all operated automatically Model No. 2 has an additional feature of built-in memory to store and print of built-in memory to store and print of the state of the setting of the settin

According to Mr. S. P. Godrej, the Chairman of the company, the prices of the three models placed at the lowest level range from Rs. 19,995 to Rs. 31,995 inclusive of all taxes. Accessories like ribbons, eraser, tapes and printwheels will also be supplied by the company.

Order has been received for 100 units and good demand is expected for the new machine The Companyis planning to manufacture about 8000 typewriters in the next two years



seledio

Scene of science

Landon's Science Museum is different from most musaums. It is, ae stated in one of its brochures for visitors, a crowded and friendly place. "It is not a place where people stand in silence before objects they feel they should, end possibly do, edmire, and move quietly, as in a church. It is somswhere where people fael free, and often excited; whera they talk loudly (sometimes too loudly) end even leugh." What is not widsly known is just how much highlyskilled work goes on behind the ecenes to maintain a constantlychenging panorema of ecience pest, present-and future.

London's Science Miseaum leads all others es to trust attraction. Visitors in 1981 ware some four million, twice a many es visitated the Tower of London or St Paul's Cathedral. Its only world rivis is the Deutsches Museum in Munich, and it can be said that the Science Museum has the biggest collection of science and technology in the world. There are carterinly other well known museums but not one that specializes entirely but not one that specializes entirely

in science its method of management has been changed recently. Up to now it has been financed by the UK Department of Education and Science, with administration wholly the responsibility of the Director, Deme Margaret Weston, and her staff. This is to changa, to bring the museum into line with others, and the administration will be in the hands of trustees appointed by the Prime Minister. Whether this will produce any significant change in policy remains to be seen. Another aspect of the Science Museum's activities underlinas how

widespread its reputation now is in Britain. This is the appearance of outstations in several parts of the country. The latest of these - the National Museum of Photography, Film and Television - was opened in Bradford, an industrial town in northern England earlier this year. It has one of the largest cinema screens in Britain and the very latest in film projection systems, called IMAX. The first film to be shown was an awardwinning history of flying. The theatre also has a multiscreen slida show controlled by computers. Such use of the most advanced

techniques shows how much we must revise our notions about what we call a museum. It need not be only a collection of 'old' material; the Science Museum is right up-to-data and looks into the future, too.

and looks into the future, too. There is also an exhibition at the Fleet Air Arm Museum in Somanset, where one can see the first Britishhouse the second of the second of the looks of the the looks of

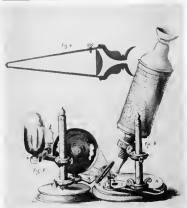
It is structed in what might well be dubbed museumown, a large pert of West London that includes the Victorie and Abert Museum, the Geological Museum and the famous Natural History Museum. The area covers about 120 000 square metres. We can ignore of the involved history—it has been anthusiasticity and say that the Sciance Museum es we know it regive bean with the

appointment of Colonel Henry Lyons as Director in 1920. By 1933 when he retired he had, as Follett wrote, "developed the Museum into one of the foremost technical museums in the world." By 1928 the first new building was ready for opening by King George V, and has been growing ever since.

ing ever since. But why have a science museum? Indeed, what purpose does a museum of any sort serve? There were none until a very few centures ago, end even those were more collections of curiosities culled by world traveliers and victorious generals than a museum as we now understand it. (The Ashnoleen Museum at Oxy, which celebrated its recrentantary last year, started in a similar way.)

New Culture

The people of aarlier times ware vandate: femous buildings almost disappeared to serve ambitious thieves, or crumbled from philisins neglect, it was not until the Renaissance wes well under way that people became intarested in the story of netional culture, first of all in artistic end culture, first of all in artistic end culture, first of prisal negative the IBth centure, Brisian opened the first campaigns of the Industrial Revolutor, which grow rapidly in the 19th



Hooke's Compound Microscope (Photograph from Hooke's Micrographia (1665))

selector

century. Science and technology became challenging new aspects of culture, and with them came a growing interest in earlier science. These developments made people reflect on the aims and functions of a museum. First, there was preservation. Machines and apparatus would disappear like old toys unless they were carefully preserved. Sometimes machines endura and are in a very dilapidated condition; then it is the task of the Museum's special staff of metal workers and cabinat makers, with the help of whatever old drawings may still be available, to reconstruct and present them in thair original state. The originals of many devices no longar exist or are not accessible, in which case the craftsmen build models to scala, oftan travelling far afield to look at surviv ing piaces. New materials and adhasivas are studied to keep the technical side up-to-date. Another task is education, which involves presentation. Objects in museums devoted to artistic pursuits speak for themselves and need little more than a name, but in a scianca museum this is not so. The purpose of a piece of apparetus and how it works must be explained in some detail.

detail. Finally, there is the job of entertaining, it would be of very little use to bunch exhibits together in an unorganized way. Furthermore, things with moving parts may have switches or other means of meking them work at the wish of a visitor. This is done at the Science Museum to a great degree; there is even a Children's Gallery where young people may do thair best to test devices to destruction without ever succeeding.

succeeding. Every schema imaginable is used at the Science Museum to make presentation entertaining. There are pictorial techniques, for exampla the diorema of mediaeval agriculture painted by a well-known artist. There are working models, such as a computer with print-out and visual display unit against which a visitor may try his skill. There are life size models of settings with sculptured men or women, as shown with the 18th century wooden printing press and a surgery of 1900, with a doctor measuring a patient's blood pressure. One can see a steel maker at work on the open-hearth furnace; careful use of colour and light gives a very convincing picture of red-hot steel. This technique is even more impressive whan displaying a redhot ingot of iron: one feels afreid to touch it

Continuous tapes looped into recorders give a running commentary on processes nearby. There are life-size models of space exploring satellites and in the Children's Gallery there is, among many other things of course, a persiscope of the kind used in a submarine, which can be opparated by any visitor.

There is a life-size model of a pharmacy of 1905 with two customers and one pharmacist, representing a time when many a chemist mada his own pills. There is a reconstructed ship's bridge. One could go on for a long tima selecting fascinating items that show the display skills of the 50-strong team of craftsmen, many of them artists, working in the wellequipped workshops of the Museum. Educationally the Museum is uneoualled. There can surely be no better method of taaching, say, physics than by teking pupils to the Science Museum. They can learn a great deal about microscopes, for example, by looking at axhibits that range from Leeuwenhoek's device and Hooke's to the most modern instruments for optical or electronic magnification. More can be learned in an hour than by many hours in a classroom. And it is not all old material. The Apollo 10 apacecraft is there, with a full-size copy of the Apollo 11 lunar 'lander' module and e one-sixth scale model of Surveyor, there is also a full-size model of the Sputnik. To encourege pupils in an educational way, many leaflets on various subjects are available with questionnaires which the pupil can answer on the spot and at home. The Museum acquires its exhibits in

severel weys. Some are given. Some are bought privately or by bidding at auction. Others are on permanent loan. One famous addition is the Wellcome Museum of the History of Medicine, which covers such matters as the story of antiseptics, enaesthetics, sciantific aids for doors, and the readication of smallpox

tors, and one resonation or shallpook in every part of the world. For good presentation tha architecture in meant be organized into families or groups such as motive power, from and steel, agriculture, sailing ships, magnetism and electricity, space exploration and orcektry, structure of matter, meteorology, locks and fastening, photography, atomic physics and nuclear power, computing and so or computin

Special Exhibitions
Among the crowded galleries on five
floors, space is provided for special
exhibitions. Some of them are open
for only a few weeks or months,

after which they may be taken round Britain. Last year there was an enormous special exhibition. Telecom munications, marking the centenary of Standard Telephonas and Cables (STC). It showed everything from lina telegrephy to redio and satellites, and optical fibres (the world's first telephone link by this means was opened by the UK Post Office, now British Telecom, in 1977). Very little was laft out in this fascinating story. from Oersted's work on electric current through to the first primitive telegrephy, followed by the earliest transatlantic cable, the coming of radio, the thermionic valve, then the transistor, electronic telephone exchanges, subscriber trunk dialling (STD), the use of lasers with optical fibres, and so on. Among the wealth of material there are many 'firsts' or near-firsts. There is, for example, the world's earliest surviving steam locomotive, Puffing Billy of 1813. There is the first locomotive to pull a passanger train. Stephenson's Rocket of 1829. The visitor can see the world's earliest photographic negative lbv Fox Talbot, the founder of photography as we know it). In basic physics there is J.J. Thomson's apparatus with which he 'discovered' the electron in 1897. There is also C.T.R. Wileon's cloud chamber, with which for the first time the trecks of atomic particles could be seen, work that speeded up tha study of the structure of matter probably by a decade at least. There are very early motor-cars, including a Rolls Royce Silver Ghost of 1909. (Incidentally, though the primary interest is science, many early exhibits are aesthetically interesting and many, indeed, baautiful: see early telephone receivers or sewing machines, for axampla, elegant in bress scrollwork.) But this is old

*The Rise of the Science Museum under Henry Lyons, by David Follett, 1978.

history and in a way contributes to

the notion that museums are stuffy.

Sciance and technology, however,

are developing disciplines. What is

new today will be old tomorrow. In

this respect the Scienca Museum is

power, is not yet proven, and there

are huge machinas still experimental;

nevertheless there is a display cover-

ing the technology.

up-to-date and even projects into the

future: nuclear fusion, as a source of

C.L. Boltz Spectrum 185

In the last few years the telephone network, which was originally intended for voice communication, is being used more and more for transmitting (digital) data. The growth in the popularity of home computers has been startling but it is not really surprising that their users should seek to use the telephone lines as a medium for exchanging programs and data. What happens between transmitting computer and receiving computer is, however, still a mystery to many people so we felt that is was time to clarify this. Also included in this article is a description of the AM 7910, which is a commonly used dedicated modem IC.

data transmission by telephone

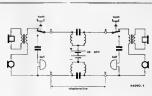
how do two computers 'converse' over the telephone lines? When Alexander Graham Bell first had the brainwave that led to the development of telephone he would have found it difficult to envisage the idea of two computers using his system to communicate with each other. Cassette tape, which, again, was designed for storing audio 'information, has long been used for storing computer data, and in the same way the telephone line can perform a duty other than allowing far-away friends to converse. There are certain limitations, of course, but using the telephone lines two computers can exchange messages, programs and data in digital form. It is of little interest for the purposes of this article to deal with the actual telephone network as what we are really concerned with is how data can be transmitted over telephone lines, what speeds are possible and what a modern does. As a start, however, we will talk about the telephone.

The telephone line

The normal telephone line, that runs into every subscriber's home, is what is known as a switched line. There are a number of switching points (mostly in the form of telephone exchanges) between any two subscribers. The frequency range of this

sort of line is from about 300 to 3400 Hz, which is quite sufficient for speech. This range limits the speed at which data can be transmitted to less than 2400 baud. however, as transmission rate is frequencydependent. Another type of telephone line is the hire line, which has a higher quality. The maximum transmission rate on a normal hire line is 2400 baud, going up to 4800 band for a local hire line and even 9600 baud on a high-quality hire line. Hire lines are not generally used by amateurs. Each end of the line is almost invariably connected to a telephone receiver. The basic principle of the telephone system (except for the dialling section) is outlined in figure 1. The actual connection is made via two wires, a and b. There is also an earth line present which our drawing does not show. The signal provided by the carbon microphone is superimposed on the d.c. voltage supplied from the exchange. At the other end the signal is extracted from the d.c. and causes the bell in the second telephone to ring. When the handpiece is lifted from the hook the 'a' line is connected not to the bell but via a transformer to the earpiece, where the signal is reconverted to the original audio information (generally speech). We are not interested in any other circustry in the exchange or the interconnecting line. We

Figure 1 in the normal (two-wire) telephone system the signels in both directions travel on the same line



now know, in any case, that the signal is superimposed on a d.c. voltage and that the same lines carry information in both directions. This latter fact is particularly important as special measures are needed if both sides want to transmit data at the same timo

A modern at each end

The connection between computer (or terminal) and telephone line is made via a so-called modem (MOdulator/DEModulator). Two basic types of modems exist: acoustically-coupled and direct-coupled. In the case of the first of these the information must be exchanged with the telephone handset via a microphone and a loudspeaker. The second type, as its name suggests, is connected directly to the telephone line. The direct coupled modem is much less sensitive to noise and interference so there are less faults during data transmission but it must be designed very carefully so as not to produce interference itself. Both types of modem must have type approval. The actual function of a modern is to convert serial digital information into an analogue signal that can be transmitted via the telephone line and to receive and reconvert information from the line. To enable different modems to be connected to the same network a standard is required. The CCITT (Consultative Committee for International Telegraph and Telephone) makes various recommendations for the different transmission rates and types of line. The V24 standard applies for the link between computer and modem. The modem itself should keep to the V21 and V23 standards. These standards specify whether the modem uses synchronous or asynchronous transmission, what the data transmission rate is what the procedure is for automatic call and answering, what tests should be carried out and whether there is a control (back) channel present. In short: they specify everything needed to enable two modems to communicate with each other on the same level.

The CCITT's V21 recommends a transmission rate of 300 band in full-duplex mode over a two-wire connection (allow ing simultaneous transmission and reception of data). V21 is used for all normal data transfer.

The V23 standard, on the other hand, recommends a dual-speed half-duplex transmission at a speed of 1200 and 75 baud. The 75 baud channel is then used for control purposes.

Bits in the telephone

Before the data can be transmitted via the analogue telephone line it must be coded. The modern does this by means of modulation. There are several different ways of doing this:

AM, in which the amplitude of the carrier signal changes with the logic level (see

2



(b) 0 1 0 0 0 1 1 0 1 1 0 AND ANNOUNCE AND AND



Figure 2. Verioue different methods of modulation ere used when deta is baing eent over an enetogus line, Indicated here ere: AM amplitude modulation -(e), FSK - trequency ehiti keying - (b) end DPSK - differential phase shift keying - (c).

data transmission by telephone

3





Figure 3. The carrier waves used in V21 mods (a) and V23 mode (b) must remain within the frequency renge used by the telephone system.

figure 2a). The simplest form of AM is 'on/off keying' in which case the carrier is present for a '0' and not for a '1' FM. in which the simplest form, FSK (Frequency Shift Keying), is generally used. The two logic levels are represented by the carrier having two different possible frequencies. Data transfer over switched lines almost always uses FSK. There are two more advanced techniques

worth mentioning, namely DPSK (Differential Phase Shift Keying) and OAM (Ousdrature Amplitude Modulation). The first of these uses phase shifting (figure 2c) and the second uses amplitude and phase shifting. Both of these techniques permit the data transfer rate to be

data transmission by

increased (relative to the others mentioned).

All of these techniques use one or a number of carrier waves so the free quencies used must be carefully decided. The frequencies recommended by V21 and V23 are indicated in figure 3, which also shows their position within the frequency range used in telephones. Full-duplex operation at 300 band uses two bands around 1000 and 1750 Hz, with 200 fit separating '9 and '11 in both cases. One channel carriers data in one direction. The man channel in the V33 norm is centred at 1000 Hz, and the back channel is at 420 Hz.

That is all that needs to be said about the actual transmission via the telephone line. A modem is, however, required at each end of the line so we will now have a look at a modern modem contained in a single IC.

The AM7910, a single-chip modem

Virtually everything in this IC that could be digitalised has been digitalised. Even the filtering and generating the carrier (a sine wave) is done digitally. A block diagram of the complete IC is shown in figure 4. As could be expected, it contains

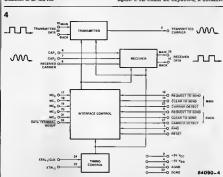


Figure 4. In the AM 7310 IC, whose block diagram is shown here, a complete modern is fitted onto a single only. The signals are processed completely digitally.



R4090_5

Figure 5. The transmitter section of the modern in slightly greater detail.

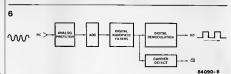
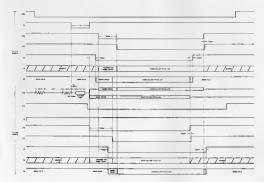


Figure 6. Here we see the various different subsections that make up the receiver section.







84090-7

a transmitter and a receiver, both of which are controlled by interface control and timing control sections.

Details of the transmitter are seen in figure 5. The serial data that are to be transmitted are fed in one side and leave from the other side as an FSK signal that can be sent over the telephone line. The FSK signals must be perfect sine waves in order not to clutter up the telephone linea. Sinewayes, at two different frequencies, are generated digitally and switching from one frequency to the other is only done when the signal is at its zerocrossing point. The digital FSK signal passes through a digital band-pass filter and is then fed via a digital to analogue converter to an analogue filter. All this filtering is needed to limit the amount of power fed onto the telephone line. This power must conform strictly to the reculations in order to reduce crosstalk and interference.

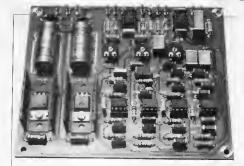
The nodem's receiver section, shown in figure 8, reconverts the FSK fapilat to digital ones. The signal arriving over the telephone line is first fed to an analogue to digital converter with an exceptionally high-speed of 468 kHz fit needs to be fast because of the FSK frequencies). In this way the effects of the higher harmonics in the FSK signal are reduced. The final two stages are a digital band-pass filter and a digital disabilitation rater which only the data remains. A carrier detector indicates when there is data present.

All the 'traffic' between computer (or terminal) and modem is controlled by the interface control. This section has several inputs, MCO... MC4, to enable the modem to be set to the various different standards (such as V21 and V23).

The exact frequencies for the FSK traffic are generated by the timing control. which takes its reference from a crystal. The block diagram clearly indicates the linea for main and back channels. This is only needed for V23 (I200/75 baud) as V21 only makes use of the main channel. An important feature of the IC is its autoanswer facility. This enables the modern to respond automatically to a telephone call, The communication protocol between computer and modern is very important and quite involved, as the timing diagram of figure 7 indicates. The example here shows the various signals and relationships for the AM 7910 when it is operating in V21 mode

At first sight it may not be entirely obvious why a modem needs to be so complex. It soon becomes apparent, however, that all this complexity is, in fact, needed to nessure that data is transferred without any errors even when there is interference on the telephone line. It is also vital to prevent the modem from generating noise that could affect other telephone users.

Figure 7. The timing diagram for the modem (shown here in V21 mode) shows ell the algness used and how they relets to each other.



active crossover filter

One of the first questions that has to be answered when considering a new loudspeeker system is what sort of filters are to be used. Should it be a 'normal' passive design or is an electronic active filter what is needed? When the pros and cons are weighed up it will be obvious that, from a musical point of view, an active filter is preferable. When finances are taken into account, however, the passive system's lower price may prove to be the deciding factor. For the purposes of this article wa will essume that finances take a back seat to faithful sound reproduction and simply concentrate on the design of this very versatile active crossover filter. Wa will, however, look at the differences between the two systems. The printed circuit board allows a choice between a two or three-way filter with e roll-off of 12, 18 or 24 dB per octave. It is also possible to use various different types of filter.

for active loudspeaker systems

In electronics we call a circuit 'active' if. along with the usual (passive) components like resistors, capacitors and inductors, it contains an amplifying element. It is obvious, therefore, what an active filter is, but the term 'active loudspeaker' may not seem so obvious. A loudspeaker, which really only consists of the cabinet and drive units (the actual loudspeakers), can, strictly speaking, only be passive, unless it has some mechanical feedback. In general, however, a foudspeaker equipped with an active crossover filter is called an active loudspeaker. This is partly due to the fact that power amplifiers are then often built into the loudspeaker cabinets. The differences between active and passive loudspeaker systems can be seen with the aid of the diagrams in figure la and figure 1b. In the passive system (figure la) the output signal from the preamplifier is passed through the power amplifier to the loudspeakers. A passive crossover filter, made up of coils and

capacitors, ensures that each of the drive units — woofer, mid-range and tweeter is fed the appropriate part of the audio frequency range.

The active system, shown in figure Ib, operates slightly differently. An obvious difference is that the filtering is done eatier, directly after the presumpline, in fact. The result of this is that the three filter outputs must each be followed by a power amplifier so that three of these are needed per channel instead of one. This makes the active system more expensive one.

Active or passive?

There is no definitive answer to the question Which is better, an active or passive loudspeaker system?: The active system has more pros than cons but that does not necessarily mean that it is the right choice for everybody. Basically the active system is more complicated, bulkier, and more expensive than a passive version but 1b

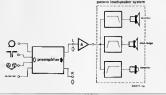


Figure 1a. In a passive budepeaker eystem the crossover filter, made up of coils and capacitors, is located between the power amplifier and loudepeakers.

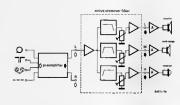


Figure 1b. The filter in an active system to placed directly after the preamplifler. Each drive unit then needs its own power amplifler.

these are the only drawbacks. That is not to say that the active system always sounds better. There are some passive loudspeakers whose sound cannot be faulted, list as there are some active systems that are very mediocre. In general, though, the active system is preferable. Its main advantages are:

It is very easy to match different

loudspeakers by amplifying the signal at one filter output or amplifier input. This is also very accurate, With passive systems matching involves adding resistors for extra attenuation, which is fine for tweeter and mid-range but it will not work with a woofer (because it affects the damping factor). An alternative is to use a suitable transformer (which will be expensive) but thus will mean that a superpassive but thus will mean that a superpassive position. The superpassive system.

The founds peakers are connected di-

rectly to the amplifier outputs (and not through big coils as in a passive set-up) so the damping of the loudspeakers is better. This results in more accurate reproduction, which is particularly marked in the bass range. This is probably the greatest plus point in an active loudspeaker system.

■ The impedance curve of the loudspeaker in an active system does not affect the behaviour of the crossover filter so this always operates as it should. Consequently there is no need for any sort of impedance-matching network.

■ Without the numerous coils and capecitors used in a passive system the load seen by the power amplifier is less complex, which means that the sound reproduction is improved.

■ The power amplifiers are located much closer to the actual loudspeakers (offer nothin the loudspeaker cabine) so the length of the loudspeaker cable is greatly reduced. This removes the need for special, expensive loudspeaker cable.

Basic circuits

Electronic crossover filters are actually quite easy to make nowadays, especially with the very good low noise opamps that are available. It is a matter of choosing the correct characteristics and a practical layout. The actual filters can be chosen from a number of standard types. The basic circuits making up our crossover filter are shown in figure 2. Any sort of crossover filter can be made simply by combining a number of these circuits. The upper two circuits (a and b) are lowpass filters; below them, c and d are highpass filters. Circuits a and c each contain two RC sections and are therefore known as second-order filters. Their characteristic curve has a roll-off of 12 dB per octave (6 dB per RC section). Circuits b and d are first-order filters with a single RC section and a roll-off of 6 dB per octave. If a and b (or c and d) are placed one after the other the result is a third-order filter with a rollactive crossover filter

Figure 2. These are the basic circuits that will be used in our active crossover filter. The upper two le and b) are low-pass filters while the other pair (c and d) are high-pass filters. The rot off of the filter cen be changed by combining these basic circuits.

off of 18 dB per octave. A pair of type a (or olicious in series form a 24 dB/octave fourth-order filter. When connecting filter sections in series the component values in the various sections must be carefully selected, as we will see later. In figure 3 we see a comparison between

In figure 3 we see a companson between three filter curves, of 12, 18 and 24 dB per octave. The filters in question have a lowpass characteristic with a cut-off frequency of 1 kHz.

The number of dBs of attenuation per octave that a fliter provides is not the only important factor. For acoustic applications there are several other important criteria, such as the precise shape of the filter characteristic, the behaviour within the ratin limits these qualities can be charged by carefully choosing the filter com-

ponents. This is very important as it means that the circuit does not have to be changed in order to have a different type of filter. Three of the most common filters are the

Chebychev, Butterworth and Bessel types; None of these conforms to the 'ideal' filter characteristics of constant gain and hear phase shift (within the pass band) very steep roll-off outside of the pass-band and neglipible undessiable oscillation. All these characteristics cannot be combined in one filter so the right filter for any application is chosen by first deciding what is the most important feature.

Figure 3. The three curves here show the frequency cherecteristics of the 'some' filter for three difterent values of roll-off slope.



characteristics for Chebychev, Butterworth and Bessel filters. In each case the fourthorder low pass filters have a cut-off frequency of 1 kHz; the continuous line is the amplitude curve (frequency characteristic) and the dotted line is the phase shift. When steep roll-off is most important the Chebychev filter (4a) is clearly the favourite. The gain within the pass band is not very constant, however, and the phase shift curve is not noticeably linear. These last two are creatly improved in the Butterworth type, whereas in the Bessel filter (c) a 'flatter' phase curve is achieved at the expense of a slightly less steep roll-off. The most commonly used filters in crossover networks are Butterworth and Bessel types. Very often the Butterworth is chosen for its more favourable frequency characteristic. For music reproduction a very important characteristic is the response to an input pulse, as figure 5 ehows. From this diagram we see that the Bessel filter's characteristic is the better of the two and it also has lees secondary oscillation. The Chebychev filter's characteristic is not shown here as it is far worse than the other two. We have designed our crossover filter in such a way as to enable it to be configured as either a Bessel or Butterworth type. It is then up to each user to choose

This is ably illustrated by figures 4 and 5.

The three diagrams in figure 4 show the

The complete crossover filter

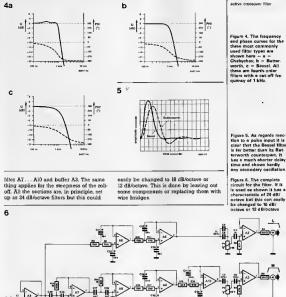
application.

which is more suitable for the particular

Having dealt with the basic theory we have now come to the practical part of this article, namely describing the filter in its final form. The mono version is shown in figure 6. The same circuit is simply duplicated for etereo operation. The power supply, seen at the lower lefthand side, has the usual format. In addition to this we see input buffer Al and three output buffers, A2, A3 and A4. The output levels can be trimmed by means of Pl (low range), P2 (mid range) and P3 (high range). The input to Al comes straight from the preamplifier, and the outputs from A2 ... A4 are fed directly to three power amphifiers. The actual filter is based on A5 . . . Al2 and is set up as a fourth-order three-way

and section and is seen as a fourth-order three-way system. The crossover frequencies are at value is mid-left frequencies are at values indicated. The three-way frequencies are at values indicated. The three-way frequencies are at values indicated. The three-way frequencies frequencies from the wooder (500 Hz) have, All and Al2 block all low and medium frequencies flees than 5000 Hz in this example) to the two-elect, and the mid-left frequencies frequencies frequencies from the high propriate output by the combination of high-pass filter AJA/8 and low-pass filter AJA/8 and low-pass filter AJA/8 and frequencies for the filter than 100 Hz.

no means fixed. A two-way system can be made simply by leaving out band-bass



Setting the component values

Applying the circuit of figure 6 to any sistence in early state to looking at table 1, which gives the formulae for all the frequency-felting components. If you've suddenly discovered that your calculater batteries are flat don't pane — we have taken the trouble to include a few tables giving the component values to use with the most commonly used frequencies.

Table 1. The values of the frequency determining components in the circuit are calculated from these formulae. The filters may be either Bessel or Buttenworth types.

The first thing to be decided is the slope of the filters' characteristics. If 24 dB/ octave is chosen the rest is very easy as the circuit remains just as it is. In the low-pass filter C21, C22, C23 and C24 correspond to C_B, C_B, C_C and C_D respect

ively in table I; for the low pass filter in the mid-range section these are C29, C30, C31 en C32. Taking 18 dB/octave, capacitors C23 and C31 in the low-pass filters are removed and resistors R10 and RIB are replaced by wire bridges. Similarly R14 and R22 in the high-pass filters are left out and C27 and C35 are replaced by wire bridges. If the roll-off required is 12 dB/octave the whole second section of each filter is removed and the opamps just work as buffers. In this case C23, C24, C31 and C32 (in the low-pass filters) as well as R14, R15, R22 and R23 (high-pass filters) are removed, and resistors RIO, RII. R18 and R19, together with capacitors C27. C28, C35 and C36, are replaced by wire When the roll-off, cut-off frequency and

When the roll-off, cut-off frequency and type of filter have been chosen the values of the frequency-determining components, $C_A \dots C_D$ and $X_A \dots R_D$, can be selected from the formulae in table 1. The values needed for a large number of Δf certain the first consideration of the free cut-off frequencies have already been calculated and are indicated in table 2 down pass filters) and table 3 (high-pass filters). In these tables the components in question have the same

ponents in question have the same designations as in table ! C21... C24 and C29... C32 are C4... C9.; and R12... R18 and R20... R23 are R4... Rp. We have purposely not rounded off the values of resistors and capacitors to the nearest standard values to enable acc values to be obtained by means of parallels.

We have purposely not rounded off the values of resistors and capacitors to the nearest standard values to enable accurate values to be obtained by means of parallel and senes combinations of components. Using E12 values makes the filters less than ideal so, if possible, use E24-series values.

Construction

Constructing this filter is simply a matter of fitting the correct components, selected according to the desired characteristics. into the printed circuit board shown in figure 7. For the 18 dB/octave or 12 dB/ octave versions a number of components are left out or replaced by wire bridges. Apart from the supply transformer everything fits onto the same printed circuit board. For stereo operation, of course, two boards are needed, one per channel. The way in which the project is finished is a matter of personal taste. It could, for instance, be housed in its own case but then there will be three cables coming from the case and going to the power amplifiers, which is not such a good idea. A more logical idea is to include filter board plus three power amplifiers in the actual loudspeaker cabinet. Each channel (left and right, in the case of stereo) is then fed from a single screened cable coming from the preamplifier. While on the subject of screened cable it is advisable to use this for interconnecting the filter outputs and the power amplifier inputs.

If the filters and power amplifiers are built

Table			
	Bessel	Butterworth	
iow-pass: 12 dB/octave	$C_{A} = \frac{0.9078}{2 \pi f \cdot R}$ $C_{B} = \frac{0.0008}{2 \pi f \cdot R}$	$C_{A} \simeq \frac{1414}{2 \times f}$ $C_{B} : \frac{0.2021}{2 \times f}$	10 km
low-pass: 18 dB/octave	$C_{A} = \frac{0.9548}{2 \times f} \times R$ $C_{B} = \frac{0.4898}{2 \times f} \times R$ $C_{D} = \frac{0.7960}{2 \times f} \times R$	$C_A = \frac{2}{2 \pi t \cdot R}$ $C_B = \frac{0.5}{2 \pi t \cdot R}$ $C_D = \frac{t}{2 \pi t \cdot R}$	R" have the sams value of about 4.7 10 kfl
low-pass: 24 dB/octave	$\begin{array}{c} C_A + \frac{0.7298}{2 \times \ell} \\ \\ C_B + \frac{0.699}{2 \times \ell} \\ \\ C_C = \frac{1.0046}{2 \times \ell} \\ \\ C_C = \frac{0.9672}{2 \times \ell} \\ \\ C_D = \frac{0.9672}{2 \times \ell} \\ \end{array}$	$\begin{aligned} C_A &= \frac{10624}{2 + \ell} \\ C_B &: \frac{0.9238}{2 + \ell} \\ C_C &= \frac{2.8130}{2 + \ell} \\ C_D &= \frac{0.9827}{2 + \ell} \\ C_D &= \frac{0.9827}{2 + \ell} \end{aligned}$	all RT have
high-pass: 12 dB/octave	$R_{A} = \frac{f \text{ fol?}}{2 \times f \cdot C}$ $R_{B} = \frac{14668}{2 \times f \cdot C}$	$R_{A} = \frac{0.207f}{2 \times f}$ $R_{B} = \frac{f}{2 \times f} \frac{414}{C}$	10nF
	$R_A = \frac{f \cdot 8474}{2 \times f \cdot C}$ $R_B = \frac{2 \cdot 0008}{2 \times f \cdot C}$	$R_A : \frac{0.5}{2 \times t \cdot C}$ $R_B = \frac{2}{2 \times t \cdot C}$	C" have the tame value of about 4.7
high-pass: 18 dB/octave	R _D = 13228 2×f C	RD = 1 C	adi s
	$R_{A} = \frac{f \cdot 370f}{2 \cdot n \cdot f \cdot C}$ $R_{B} = \frac{1.4929}{2 \cdot n \cdot f \cdot C}$ $R_{C} = \frac{0.9952}{2 \cdot n \cdot f \cdot C}$	$R_A = \frac{0.9239}{2 \times f \cdot C}$ $R_B = \frac{f.0824}{2 \times f \cdot C}$ $R_C = \frac{0.3827}{2 \times 1 \cdot C}$	all C ⁻ hay
high-pass: 24 dB/octave	$R_D = \frac{2.5830}{2\pi f}$	RD × 2.6130 2 x f C	L
		84	4071

Table 1

	r				,										1					
		low-p	2455		low pass						low-pres									
		12 dB/				18 dB/octave						24 dB/octave								
	Bess	se1	Butten	worth		Bessel		Bu	tterwor	th		Bes	sel		Butterworth					
- 1	$R = 5k6 (2 \times 1)$		$8 = 5k6 (2 \times 1)$		R = 5k6	5 (2 × I	R = 5k6 (3×)		13×1 8		R = 5k6 (3×)			R = 5k6 (4×) R = 5			R = 54	k6 (4×)		
_(Hz)	CA.	CB	CA	€B	CA '	CB	CD	CA	Cg	CD	CA	CB	CC	CD	CA	CB	CC	CD		
	(nF)	InFl	{nF1	(nF)	(nF1	(nF1	(nF)	InF1	InF1	(nF)	(nF1	(nF1	InF1	InFl	(nF)	(nF)	(nF)	(nF)		
100		193 5	401 9	200 9	271 4	142.0	214.9	568 4	142.1	284.2	207 4	190 1	285 5	1100	307.6	262 6	742.6	106.8		
	128 9	968		100 5	135 7		107.4	284 2	71.0	142.1	103.7	95 2	142 8	55 0	153 8	131 3	371.3	54.4		
300		64.5	133 9	86 9	90.4	47.3	716	169 5	47.4	94.7	69 1	63 5	96.2	36 7	102.5	87 5	247.5	35.3		
400	64 5	48 4	100 5	50.2	67.8	35 5	53 7	142.1	35.5	71.1	51.9	47 6	714	27 5	76 9	65 6	185 7	27.2		
500	518	38 7	80.4	40.2	54.3	28.4	42.9	113.7	28 4	56.8	41.5	38.1	57.1	22.0	61.5	52 5	148 5	218		
600	42 9	32 3	66 9	33.5	45 2	23.7	35 9	94.7	23.7	47.4	34 6	31 7	47 6	18 3	51.3	43 8	123 8	18.1		
700	36.8	27 6	57 4	28 7	38.8	20 3	30 7	81.2	20.3	40.6	29 6	27.2	40.8	15 7	43 9	37.5	106 1	15.5		
800	32 2	24 2	50 2	25.1	33.9	17.8	26.9	710	17.8	35.5	25 9	23 8	35 7	13 8	38 5	32 8	82.8	13.6		
1,000	25 8	19 4	40 2	20.1	27 1	14.2	215	56 8	14.2	28 4	20 7	19 0	28 6	11 0	30.8	26.3	743	10.9		
1,500	17.2	12 9	26 8	13 4	18.1	9.47	14 3	37 9	9 47	18.9	13.8	12.7	19.0	7.34	20.5	17.5	495	7 25		
2,000	12 9	9 68	20 1	10.0	13.6	7.10	10 7	28 4	7.11	14.2	10 4	9 51	143	6.51	15.4	13 1	37.1	5.44		
2,500	10 3	7 74	16 1	8 04	10.9	5 68	8 59	22 7	5 68	11.4	8 30	7 61	11.4	4 40	12.3	10 5	29.7	4 35		
3,000	8 59	6 45	13 4	6 70	9 04	4 73	7 16	189	4.74	9.47	691	6 35	9.52	3.67	10.3	8 75	24 8	3.62		
3,500	7 37	5,53	115	5 74	7.75	4 08	6 14	16 2	4 06	8 12	5 93	5 44	8.16	3.14	8.79	7 50	21.2	3 11		
4,000	6 45	4 83	10.0	5.02	6.78	3 56	5 37	14.2	3 55	7 11	5 19	4 76	7.14	2 75	7 69	6 56	18.6	2 72		
5,000	5 16	3 87	8 03	4.01	5 43	2.84	4 30	11.4	2.84	5.68	4 15	3 81	5.71	2 20	8.15	5 25	14 9	2 18		
10,000	2 58	1 94	4 01	20	2.71	1.42	2 15	5 69	1,42	2 94	2 07	1 90	2 96	1.10	3 08	2.83	7 43	1 09		

Toble 3

		high 12 d8/				high-pass 18 dB/octave						high-pass 24 dB/octave							
	Bes	sel	Butten	worth		Bessel		1 8u	tterwor	h	Bessel			Butterworth					
- 1	C =4n	7 (2×)	C = 4n	7 (2×1	C = 4n7 (3×1			1×1	C = 4n7 (4 ×)			C = 4n7 14 × 1							
(Hz)	RA	RB	RA	RB.	RA	RB	1 RD	RA	RB	RD	RA	I RB	RC I	RD	BA	R8	l Rc	1 RD	
100	IkΩ(373.1 186.5	(kΩ(497.4 248.7	(kΩ) 239 4 119 7	(kΩ) 479 8 236 4	(kΩ) 354.7 177.3	(kΩ1 677 5 336 8	lkΩ1 447 9 223 9	(kΩ1 169 3 84 7	(kΩ1 677.3 338.6	1kQ1 338 6 169.3	0kΩ1 463 9 231 9	(kQ1 505 5 252 8	(kΩ1 337 0 169 5	(kQ) 874.7 437.3	(kΩ) 312.9 158.4	(kΩ1 366 5 183 3	(kΩ) 129 6 64 8	884 8 442 4	
300	124 4	185 8	798	159 6	118 2	225 8	149 3	56 4	225 8	112 9	154.7	168 5	1123	291 6	104 3	122.2	43 2	294 9	
400 500	83.3 74.6	124 3 59 5	59 9 47 9	119,7 95 8	88 7 70.8	169 4 135 5	111 9 69 6	42 3 33 9	169 3 135 5	84 7 67 7	115 9	126 4	67 4	218 7 174 9	76.2 62.8	91 6 73 3	32 4 25 6	221 2 176 9	
500 700	62 2 53.3	82.9 71.1	39 9 34.2	79 8 68 4	59 1 50.7	112 9	74 7 63 9	28 2	112.9	56 4 48 4	77.3 86.3	84.3 72.2	56 2 48 1	145.8	52.1 44.7	61.1 52.4	21 6 18 5	147 5	
1,000	46 6 37 3	62 2 49 7	29 9	59 9 47.8	44.3 35.5	64 7 67 8	56 9 44 8	21 2 16 9	67.7	42 3 33 4	57 9 48.4	63 2 50.6	42 1 33 7	109 3 87 5	36 1 31.3	45.8 36.7	16 2 12.8	110 6	
1,500	24 9 18 7	33.2 24.9	15.8	31 8 23 9	23.6	45 2 33 9	29.9 22.4	11 3	45.2	22.6 16.9	30.9	33.7	22 5 16 9	58 3 43 7	20.9	24 4 18.3	8 64	58 9	
2,500	14.9	199	9.57	19 2	14.2	27.1	17.9	6.77	27 1	13.5	186	20.2	135	34.9	12 5	14.7	5.18	35 4	
3,000	12 4	16 6 14 2	7 9B 6 84	159	11.8	22 6 19 4	14 9 12 8	5.64 4.84	19 4	11.3	15.5 13.3	16.9 14.4	11 2 9 63	28 2 24 9	10.4 8.84	12.2	4 32 3.70	25 3	
4,000 5,000	9 33 7 46			11 9 9 58	8.87 7.06	16 9 13 6	11 2	4 23 3 38	135	8 47	11 6 9 28	12.6 10.1	8 43 6 74	21 9 17 5	7 82 6 26	9 16	3.24 2.59	22 1 17 7	
10,000	3.73	4 97	2 39	4 79	3.55	8 78	4 48	1 69	6 77	3 39	4 64	5.06	3 37	87	3 13	3 67	1 30	8 88	



into the loudspeaker cabinet a special section should be screened off to house them. This will provide an acoustic barrier to prevent the electronics from playing havoc with the bass and it will also make it easier to cool the amplifiers.

Final tips

In an article such as this we cannot deal with all the details about how to set up a complete three-way active system but there are a few practical points to note. When talking about high-quality sound reproduction (which we can take as read) you should not allow yourself to be tempted by attractively priced loudspeakers of unknown oncon. This will prove to be a false economy. Good names such as Kef. Audax, etc., are the ones to look for, especially as these manufacturers generally supply quite a lot of information with their loudspeakers. This information about output, frequency characteristic. recommended cabinet dimensions and so

on is of vital importance.
Experimentation is very easy with the
filter board shown here so our advice is to
try various different types of filter arrangement. Your own taste will then decide if
you prefer the 'sound' of a Bessel filter

Table 2 The component values for a number of commonly used crossover frequencies can elmply be read off this table. Only the low-pass filter values are given here.

Table 3. Commonly used crossover points for highpess filters are given in the liret column here. The other column here. The other columns list the appropriate component values that should be used. 7

Parts list

Resistors: R1.R2.R4.R6 = 47 k R3_R5_R7 = 100 Ω

R8 . R11, R16 . R19 = 5k6 R12,R20 = RA — see tables R13,R21 = Rg - see tables R14,R22 = RC - see tables R15,R23 = Rp - see tables P1 . . P3 = 22 k preset

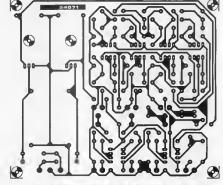
Capacitors: $C1 = 2.2 \mu (MKT)$ C2,C3 = 820 nC4 = 1 a IMKT) C5 = 470 n C6,C7,C9 . . C14, C19,C20 = 100 n C8 = 15 n Ct5,C16 = 1000 µ/25 V C17,C18 = 10 µ/25 V C21,C29 = CA - see tables C22,C30 = CB - see tables C23_C3t = CC - see tables C24,C32 = CD - see tables . C28,

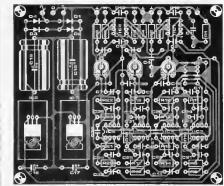
. C36 = 4n7 Semiconductors: D1 . . D4 - 1N4001 IC1 . . . IC6 = NE 5632N IC7 = 7815 IC8 = 7915

C33

Miscallaneous: F1 - fuse, 200 mA alow blow S1 = double-pola meina switch Tr1 = mains transformer, 2 x 15 V, 100 mA Heatank for IC7 and IC8 (optional)

Figure 7. All the various types end combinations of filter, se described in the text, can be made by using this printed circuit board.





over that of a Butterworth type. Really it is essential to try different things as it is no simple matter to set up a good three-way system.

A good pair of ears is very important when setting up a loudspeaker system but we do not advise you to rely totally on

them. An appropriate instrument for any aspiring loudspeaker builder is the 'realtime analyser' described in the March, April and May issues this year. This is the test instrument to use for optimising an active loudspeaker's frequency characteristic.

In the June 1984 issue of Elektor we mentioned that connecting the digital cassette recorder described in February of this year to the output of a ZX81 can cause problems. Since then we have worked day and night (in spirit at least) to find a solution to help owners of this computer. This article deals with what we came up with.

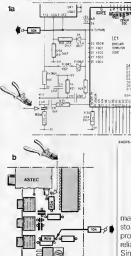
digital cassette recorder with the ZX81

A few changes are needed both to the cassette recorder board and the ZX 81 board and we will begin with the latter. Part of the circuit of the ZX 81 is shown in figure 1. The computer's output signal must be amplified by making a new cassette output. This is done by connecting a 10 k resistor to the TV/TAPE output (pin 16) of ICl. All this means is soldering the resistor to R29 on the printed circuit board, as indicated by figure lb. The output signal then has an amplitude of 150 mVpp. The digital cassette recorder operates by driving the tape into saturation but the ZX cannot do this very easily as the impedance of its EAR input is too high. All that is required to lower the impedance is to cut the connection of R34

Moving on to the cassette recorder board. the hysteresis of Al must be reduced by increasing the value of resistor R6 to 82 k. The amplification of the playback section is reduced by increasing the value of R20 to 10 k. The pause-level is improved by giving C8 a value of 10 n. The pair of relays, Rel and Re2, can better be operated by hand. In normal use the video signal is present at the cassette output of the Sinclair with the result that the circuit will always select recording. The easiest way to solve this problem is to cut the copper track close to pin 6 of Re2 and link pin 6 to +12 V via a switch. For those who want the ultimate, a band-pass filter (with a centre frequency of about 5 kHz) can be added in the playback section. In this case C6 = 10 n. C8 = 2n2. C9 = 100 n and R20 remains 1 k. All that remains then is to connect a 560 p capacitor parallel to

To enable the circuit to be calibrated a short program with a large loop must be swritten into the computer and the SAVE command is then given. The present at the input Pl., must now be trimmed so that must be supported by the computer of the program poil. AM, it used when playing a recorded program back to the ZXSI. For playback repeat PB must be set so that the program is read into the computer correctly. Thus can be seen by looking at the width of all the program of the screen. There about be beauty on the screen. There while the program is the program of the program of the value of the program of the program of the program of the value of the program of the program of the program of the value of the program of the program of the program of the value of the program of the program of the program of the value of the program of the pr

When reading in a program it is important that preset PI should be turned completely to ground or that the plug be removed from the input of the recorder



circuit. Under no circumstances may LED DII light dunng playback. This is essential to prevent cross-talk between the recording and playback sections. Probably the best solution is to short-circuit the input by using a 3.5 mm socket with a built-in switch. As soon as the plug is removed from the socket the input is then grounded.

When the modifications given here are carried out there will be joy in the hearts of Sinclair ZX81 users as this computer can finally be used with the Elektor digital cassette recorder.

makes the storing of programs more reliable for Sinclair's smallest The flash units used by most photographers today are carefully dasigned to provida the correct amount of light. This is only true, however, in standard circumstances: with tha flash mounted on the camara and the apertura set to tha value recommended for the flash unit. This is not usually very creative and it does not take into account the practice of using a number of flash units and/or ordinery lights. In thesa casas either a lot of arithmetic or this flash meter is required. The most obvious advantage of this meter over similar ready-made units is its lower cost but its versatility is what really makes this design stend out from the rest.

flash meter

measures light ... in a flash

Modern cameras, and flash units for that matter, now contain quite a lot of electronics to ensure that the photographs are correctly lit. It might seem somewhat unnecessary, therefore to have a separate (flash) light meter, but this is, in fact, not so. It is not only for special effects that a photographer (whether amateur or professional) might want to do more than simply fix the flash unit onto the camera and press the button. Photographs taken in this way often have a very 'hard' quality about them. The flash can be pointed at a reflective surface, of course, but only if there is a suitable surface available. A better idea is to use a number of (inexpensive) flash units to give the light a much more natural appearance. The camera must of course be set correctly for this. A computer-controlled flash can become confused in this sort of situation as it is not the only light source and it must also be set up for the camera's position. A camera with TTL flash measurement (which measures the flash through the lens while the actual photograph is being taken) is better in this case but is only usable with (expensive) flash units that are matched to the camera. The best idea of all is to use a flash meter. First make a test flash to find out what aperture should be selected and then take the actual photograph.

When we decided to design a flash meter it is only natural that it would have to be much less expensive than ready-made units but we decided that it must also be more versatile. Most available meters have a fixed measuring time and operate on the principle that if the flash occurs within this time most of the light will be measured. If the camera's flash synchronization time is longer than the meter's the effect of the extra (ambient) light will be neglected. The design proposed here has a measuring time that can be set to the same value as the camera. Normal light measurements can also be taken, with the result given in the aperture value (fl.4 . . . 22 in half stops) that must be selected on the camera. Thus instrument is also fitted with an automatic

switch-off facility and provides the possibility of summing a number of measurements (for multiple exposures, for example).

The circuit

The power supply for the circuit, which is illustrated in figure 1, is provided by a 9 V battery. The automatic power switch-off curcuit ensures that the battery lasts as long as possible. Pressing 34 causes CI2 to be charged with 35 and at the same time darington T8 is driven open thus connecting the negative pole of the battery to the class of the control of the circuit and the control of the circuit and the circuit an

Pressing S4 also fulfils another function, namely that T4 is caused to conduct momentarily via D4 and R14. This discharges C7 and any of the other four capacitors (C8 . . . C11) that are in parallel with it. These capacitors play an essential role in this circuit. In the first place they convert the photo-current of D3 to an analog voltage, which, like the photocurrent, is directly proportional to the amount of light present. The capacitors must also store the measured value with as little leakage as possible. We will return to this point later in the article. The light is measured by a BPW 21 photodiode (D3) which has the right sensitivity for our purposes. Its photo-current is conducted to ground by T3 when D3 is in its quiescent state. During the measuring time transistor T3 will be switched off so C7 and, depending on the states of switches S5... S8, some of the other four capacitors will be charged. In this circuit MOSFET T2 is used as a diode to provide a threshold for the photocurrent when no measurement is being taken. Its very low reverse current also tends to prevent the capacitors from leaking.

The measuring sequence can be started in three ways. The first makes use of switch SI and is the simplest way. The flash unit(s) can be connected to the SYNC input. Pressing the switch causes flap-flop N3/N4 to toggle. This switches off transistor T3 so the photo-current flows into the capacitor network. At the same time the flash is triggered via the SYNC connection and counter IC2 is started by releasing its reset input. A clock signal is provided by oscillator N2/R6/C2 to enable the counter to run for a certain time, as decided by the position of switch S2. After a certain length of time the appropriate O output of the 4040 will go high. The flipflop then resets, the photo-current is again conducted to ground by T3 and the counter stops. The meter can now be reset by pressing \$4 but if this is not done a second measurement can be taken and the total result is the sum of the two measmements.

The second method of taggering the

1

meter is by pressing the button on the flash unit to make a test flash. This is detected by DI and the flip-flop is set via NI. The circuit consisting of RI, R2, C1 and Tl ensures that only sudden changes in the light intensity will affect the flipflop. As a result this method is suitable only for flash measurements, not for normal light measurements.

The third way of triggering the circuit is by pressing the shutter release button on the camera. Either the flash itself (as in method two) or the shutter release switch in the camera (if this is connected to SYNC) will start the meter. The film in the camera will, of course, be exposed at the same time and it may then be cold comfort to know that the picture will be under or over-exposed due to the camera being incorrectly set. Whichever of these methods is used the

Figure 1. The circuit of the flash meter, as the dlegrem here shows, is ightforward. All tha Other types of high-spe photo detector (for D3) and infra red photo detec tor (for D1) will probably work in the circuit but me of the other com ant values may t to be changed.

111 ΞŻ N3B 9 F 256A DO IC3 IC4 IC2

Figure 2. Depending on the type of cese used for the circuit, it may be necessary to cut off the four corners of the printed circuit board to provide clearence for certs of the cese.

Perte iiet

Resistors*
R1 = 10 M
R2 = 2M2
R3, R17, R18 = 1 M
R4, R5, R15 = 100 k
R6 = 560 k
R7 = 22 k
R8, R14, R20 = 10 k
R8, R14, R20 = 10 k
R1 = 2k7
R12 = 1482, 1%
R13 = 56k2, 1%
R13 = 150 k
R1 = 100 k
R1 = 1

 $\begin{array}{lll} \text{C1} = 56 \text{ n} \\ \text{C2} = 82 \text{ p} \\ \text{C3} = 47 \text{ n} \\ \text{C4} = 3 \text{ n3} \\ \text{C5}, \text{C8} = 100 \text{ p} \\ \text{C7}, \text{C8} = 100 \text{ n} \text{ (MKT)} \\ \text{C9} = 22 \text{ n} \text{ (MKT)} \\ \text{C10} = 27 \text{ n} \text{ (MKT)} \\ \text{C11} = 18 \text{ n} \text{ (MKT)} \\ \text{C12} = 22 \text{ } \mu\text{/18} \text{ V} \text{ Ta} \\ \text{C13}, \text{C14} = 560 \text{ p} \\ \text{C15} = 10 \text{ } \mu\text{/18} \text{ V} \text{ Te} \\ \text{C16} = 1 \text{ } \mu\text{/16} \text{ V} \text{ Te} \\ \text{C16} = 16 \text{ } \text{C16} \text{ V} \end{array}$

Semiconductors:
D1 = BPW 34
D2_D4_D5 = 11N4148
D3 = BPW 21
D6 = D23 = LED,
restinguisided on MV zener
T1 T1 = BF 256A
T3_T4 = BC 5608
IC1 = 4093
IC2 = LM 3915
IC3_IC4 = LM 3915
IC3_IC4 = LM 3915

S1,S4 = push button S2 = single-pole 12-way wafer switch S3 = single-pole springloaded changeover S5. S8 = 4-pole DIL

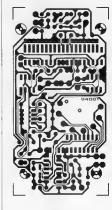




photo-current is now stored as a certain voltage by the network of capacitors. Some way must be found of displaying this, ideally on a logarithmic scale such as that used for the aperture scale on a camera

The capacitor voltage is buffered by means of a voltage follower (CG). This is essential because in order to store the measured value the charge on the capacitors must be as constant as possible. The acrual read-out is given with the aid of a pair of IM 3915 ICs. These are LED drivers with a difference as they have a logarithmic scale that ness in stops of 3 dB and can be used for either a burgaph or dot display mode as we only want to indicate a single aperture value at a time. A side-effect of having only one LED at a time list that the current consumption is kept

Dow. The buffered capacitor voltage is taken from pin 6 of ICB and fed to the signal raput of ICB (year). This IC compares the input to a reference set with P2 to determine which LED should light. A total of 10cd so the range spans 30 dR. This is not sufficient for our purposes so it is extended by amplifying the ICB (signal voltage (with ICB) and feeding it into the signal light of ICB. The ratio of output to

input voltage that gives a range of 30 dB is 31.6 so this is the factor by which the signal must be amplified before being fad to IC3. In this way IC3 will drive its LEDa to indicate a value in the lower part of the total measuring range while IC4 takes over for the upper 30 dB of the range. The gain of IC5 is fixed by means of two 1% resistors, RI2 and RI3.

Two of the outputs of 1C3 are not connected to any LEDs. The total number of LEDs used is 18, giving a range of 54 dB. Each 3 dB step corresponds to a half stop so the meter can be calibrated in apertures from fl.4 (D6) to f22 (D22). If the meter senses too much light LED D23 will be lit - this acts as a sort of overflow indicator. When the light intensity measured is too low no LED will light. Changing the reference voltage enables the scale to be set to whatever range the user selects, so it could run from 12 to f32 or f2.8 to f46, for example. The values of C7...Cll, which are used to set the meter to the right sensitivity for the film used (the film speed), can also be changed to suit personal needs or preferences.

A battery checking facility is provided by \$3. When the battery is good an initial check should be made to see which LED lights. This will be dependent on the reference voltage set with P2. When the

Construction

As we have already hinted the leakage losses of capacitors C7. CII must be reduced as much as possible. This explains the use of MRT (Somers polyester layer type) capacitors here. The design of the printed circuit board also incorporates some precautions in this respect. The junctions of C8... CII with S5... S8 are surrounded by tracks that

\$5 . . . \$8 are surrounded by tracks that carry a similar potential (connected to the output of the voltage follower). There is none the less still a danger with homemade boards that the material itself could permit some leakage. The same problem could arise with the lacquer sprayed onto the board. It is, however, advisable to use a suitable (highly-insulative) lacquer as a protection against dampness. The printed circuit boards supplied by Elektor through the EPS meet all the above demands There are two points to note when constructing this circuit. The rotary wafer switch, \$2, must be fixed to the board by means of the lock-nut on its spindle. Points a...h are wired to points 1...8 respectively on the switch. If you use a singlepole 12-way switch the four remaining contacts are simply left open. The common pole of the switch is wired to the point marked M near T2 (refer to the section on calibration). The setting for film speed is carried out

with DIL switches S5...S8. These were chosen to reduce the amount of wiring around capacitors C7...C11. These switches must protrude significantly above the printed circuit board in order to come up to the level of the exterior of the case used. This is done by using an eight-pin wire wrap socket or a suitable number of ordinary eight pin IC sockets. A suitable case must be found for the circuit and everything must then be fitted into it. For our prototype we used a Verobox with dimensions of 120 x 65 x 40 mm but if you have another suitable box then by all means use it. The board must be mounted on spacers to leave room underneath for the battery and part of \$2. The push buttons, \$1, \$3 and \$4, also fit below the board, but are mounted on an aluminium bracket at the side of the case. This bracket can be fixed to the box by means of a pop rivet. If the SYNC socket is to be fitted it can be placed close to, and in parallel with, Sl. The photodiodes fit in one of the small sides of the case; DI simply fits behind a suitable hole but D3 must be provided with a tube or pipe about 15 mm long and 8 mm in diameter. This tube, which can be seen in the photo of figure 3, should ideally be black and we will come back to its purpose in the next section. The cover of the box must now be prepared by cutting three holes in it. The

photo in figure 5 shows the end product and here we see that a bit of care and



cuit board must be mounted on spacers to provide room for the bettery, push buttone and the body of \$2 underneath it.

Figure 3. The printed cir-

flash meter

attention is needed when cutting the holes for rotary switch S2, DIL switches S8...S8, and LEDs D6...D23 in order to produce a good result.

Calibration

To operate correctly the meter must be calibrated, beginning with compensating the offset of ICS. Pressing S4 switches on the unit for 40 seconds after which it will automatically switch off. This push button will therefore have to be pressed repeatedly. Temporarily short the noninverting input of IC6 (pin 3) to ground and measure the voltage at pin 6 of ICS. Rotate PI until a value of a few dozen millivolts is measured and then trim it until the value is reduced to zero volts but no further. Reconnect pin 3 of IC6 The oscillator must now be set so that the measuring time is the same as the shutter time in the camera. Although the oscillator

Figure 4. This view of the top of the printed circuit beard shows how the DIL switches should be mounted in a wire wrap sockel so Inter they finish flush with the top of the case. An elternative is to use a number of ordinary soht on it backets.





Figure 5. The finished unit is very easy to use because of the way everything is laid out. Rectangular LEOs should be used instead of round ones if possible as they improve the appearance of the unit.

Figure 8. The oscillator is act up with the aid of this circuit. When connacted correctly to the leah metar this 4040 ceases the multimater to datlect sight seconds sfair the Hash meter is started (if the oscillator frequency is right).

is built of fixed components the differences in switching thresholds for the schmitt trigger given by various manufacturers can mean that the frequency is not correct. An oscilloscope is very handy in this case as the frequency measured at pin 10 of 162 should be 32 kHz (giving a period of 31 a,b). Playing around with the resistance of R6 will enable thus value to be achieved.

There is another method of checking this frequency, for those intrepid souls who do not have access to an oscilloscope. The longest measuring time is I/8 of a second, which is a bit too fast to check with a wnstwatch. The alternative is to use the circuit shown in figure 6, with the 4040's clock input connected to pin I (Q12) of IC2. A multimeter can then be connected to pin 4 of the test IC. Depending on the IC manufacturer this output may be called Q6 (if the numbering runs from Q0...Q11) or Q7 (when the outputs are numbered Ol...Ol2). The common pole of S2 must be temporarily disconnected so that IC2 will keep counting rather than being reset. Counter IC2 is started by pressing SI and after 8 seconds the multimeter should deflect. If this takes longer the value of R6 must be reduced. If the time is too short the resistance must be increased. After the frequency is correct remember to reconnect the common pole of \$2. The fast calibration involves the sensitivity of the meter. Before this can be properly

set it is essential that the light is measured

from the right angle. A haked' BPW 21 of measures light incidence within a red of about 180° so the measured value would be higher than the actual amount of light that affects the exposure of a photograph. This problem is solved by mounting this photodiode away from the side of the case at the end of a short length of (preferably matt black) tubing.

at the end of a short length of (preferably and black) the mant black) tuber. The light meet in the camera is used to provide a reference value. Point both the camera and the Sash meter at the same object from the same position then press SI and turn F2 until the reading given by the meter is the same as that given by the meter is the same as that given by the camera. If F2 does not have a sufficient same for this to be done the values of

range for this to be done the values of C?...CII must be changed. To increase the read-out (making the instrument more sensitive) these capacitor values must be reduced. Whatever values they have be sure that the various ratios of the capacitor values always remain the same. The setting for film speed must, of course,

be the same on both flash meter and camera. This adjustment is made with DIL switches S5 . . . S8, where the sensitivity is reduced by closing more switches. At 27 DIN all four switches are open, for 24 DIN SS is closed, 21 DIN requires SS and S8 to be closed, at 19 DIN S5, S6 and S7 are closed and finally for IB DIN all four switches are closed. The corresponding ASA and ISO values are given in table I. During calibration it may occur that, especially at the highest sensitivity, the meter read-out may drift. This is due to leakage in C7 ... Cll so some sort of solution must be found. Make sure the relative section of the board is clean and dry. If the board is home-made spray some plastic lacquer on it and let it dry before trying again. It may be necessary to experiment with various different things to reduce the leakage as much as possible but this is well worth the effort. Finally, as regards using the meter: always take measurements from the camera's position. Thus is the only way to be sure of

knowing how much light will fall on the

film - and that's what it is all about.

Teble 1

DIN	ASA	ISO	switches closed				
18	50	500	S5 S8				
19	64	64°					
21	100		S5, S6				
24	200	200°	S5				
27	400	400°	none				

Tabla 1. These are the ditterent tilm speeds that can be selected by means of the four DIL switches. Opinions as to what is the most important part of a car are many and varied. For some people it is the seat they sit in for hours at a time, for others it is the engine under the bonnet, and for others again it is the built in safety feature designed to save a life. There are differences of opinion, too, on smaller details, such as what is the instrument in the dashboard that could least be missed. The speedometer is generally the largest instrument making it easy to read at a glance. The most important instrument, however, is the tachometer rather than the speedometer, although most car makers consider it as an 'extra' or leave it out altoeuther.

digital tachometer

The importance of a rev counter in a car is greatly underestimated, largely because it is considered as 'something for sports cars' and run-of the-mill car manufacturers are rejuctant to fit anything that is neither a legal requirement nor guaranteed to increase sales. Of late a number of cars have become available with an indicator to advise the driver to change gear when the engine revs rise above the most economical level. This is one use of a tachometer; namely the pursuit of fuel economy. Another purpose of a rev counter is to enable a driver to make the best use of his engine's power - by which we do not mean the presponsible carry-on of many 'boy rscers'. The true professionals (rally drivers, race drivers) use the tachometer both to keep the engine within it's power-band and to avoid damage due to over exuberant use of the loud pedal. Finally, there is one other application where a tachometer is absolutely essential: when turung a car.

Converting engine revolutions into digital pulses

The principle of our design for a digital tachometer can be gleaned from the block diagram of figure 1. Ignition pulses (at half the engine speed - for a fourcylinder four-stroke engine) are taken from the car's contact-breaker points (c.b.) and are formed into a more suitable signal by a pulse shaper. This section is carefully designed to ensure correct operation at all times. The pulses are used to trigger a monoflop which, in turn, provides the clock signal for three BCD counters. The data lines from the counters provide the information for the LCD drivers to tell them which segments must be enabled. An RC oscillator produces a signal which, when divided by 16, is used to provide the a.c. needed for the LCD display and drivers. Two more dividers are included to reduce the signal frequency even more and to provide two different values that can be selected by means of a switch.



The signal chosen in this way passes to a pair of monostable multivibrators (MMV) which provide latch pulses for the display and reset pulses for the BCD counters. The effect of this selection is to enable the measuring time (the time during which c.b. pulses are counted) to be either long (3 s), which gives an accuracy to 10 r.p.m., or short (0.3 s), in which case the display is accurate to within 100 r.p.m. To summarise, then, what actually happens is this. The pulses from the c.b. points are counted by three BCD counters. Every 3 or 0.3 seconds the count is transferred to the display and the counters are then reset

The circuit diagram of figure 2 and tunung chart shown in figure 3 provide more detailed information about the operation of the circuit. The timing diagram is divided into two sections, the first of which shows the progression of the upition pulses through the pulse shaper and monoflop to become clock pulses for the BCD counters. The second part deals with

reads up to 9990 with an accuracy of 10 r.p.m. on an LCD display

the signal generated by RC oscillator R4/R5/PI/C4 and passed through the dividers in IC2 and one half of IC3 until it eventually triggers the latch pulse that appears at pin 3 of N2 and the reset pulse at pin Il of N3.

Points to note

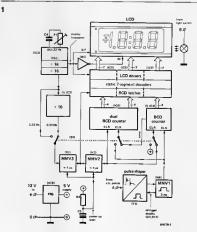
There is little reason to deal with the curcuit in great detail but there are some points about it that are important. The RC oscillator, as we have said, is made up of resistors R4 and R5, preset P1 and capacitor C4. In order to ensure satisfactory stability it is essential to use a polystyrene capacitor for C4. The update-frequency of the display is changed by switching the position of Sl. Doing this affects three parts of the circuit. First, Sla selects the actual frequency (either 0.33 Hz or 3.33 Hz) which determines the measuring time. In the 'fast' position Slb feeds the BCD counter for the second digit (pin 2 of IC4) directly from monoflop N4. In the 'slow' position this signal is taken from the O4 output of the lowest BCD counter. The final function of the switch, Slc, is to connect the clear line of the lowest counter (pin 15 of IC3) either to +5 V or to the output of N3. By doing this the least significant digit of the display is always zero when the 'fast' position is selected. Otherwise it is simply

by the pulse from N3. The significance of this switching is clear: one position gives optimum resolution and the other gives good readability. In the latter position one of the major disadvantages of many digital tachometers, namely that the display tends to flicker a lot, is

reset, along with the other two counters.

avoided. The actual measuring time in this position is a compromise between resolution and readability and was determined by means of trial-and-error experiments A liquid crystal display is used instead of the more common LED or fluorescent displays as it provides much more contrast in high-brightness environments, has lower power consumption and is more reliable. Only the lower three digits of the LCD display are used. The data about which of the segments should be visible is provided by the BCD counters in IC3 and IC4 via display drivers IC5 . . . IC7. The display frequency inputs (pin 6) of the three drivers and the back plane of the display (BP, pins 1 and 40) are fed a 53,33 Hz signal from the Q4 output of IC2. All unused segments are also tied to this line. The appropriate decimal point (DP2) is kept on permanently by connecting it to the inverse of this latter signal. As shown in the diagram here the low frequency is selected and thereby the tachometer is at its more accurate setting.

Figure 1. All the verious perts of the circuit can be recognised in this block diagram but so all embellishments ere done away with the principle of operation can be clearly seen.



Using the techometer for engines other then 4-cylinder 4 strake

The frequency of the RC oscillator, R4/R5/P1/C4, must then be recalibrated. In most cases this requires no component changes. The frequency is calculated from

 $f = \frac{2560 \times k \times c}{s}$ where

2560 is the division factor · 16 × 16 × 10 - (IC2/IC3a)

k is a constant = 0.333 c = number of cylinders e = number of strokes The corresponding frequencies are given in the

table below, for the most common configure bons

c s f IHz

6 4 1280

6 4 1280 5 4 1066 4 4 853 33 3 4 640

With the values shown the oscillator frequency range (given by $f=2.2\times R5\times P1\times C41$ is 838 to 1454 Hz so only when a 3-cylinder angine is involved need any component be changed in this latter case R5 becomes 470 k

Construction

As crucius on this is not particularly large but we chose to put it on two primed cricuit boards in order to keep the size fairly small. Both boards are clearly wishle from the photograph at the end of the armole. As the photograph at the end of the armole and the photograph at the end of the armole of the components, mainly resistors, are mounted vertically, the component layout indicates which are the ones in question. The four connection points to the busided world are located on this board and each world are located on this board and each authornotive tyree connection.

A total of ten connections must be made between the two printed circuit boards. This can easily be done with a short length of ribbon cable as all the points,

numbered 0 . . . 9 in the circuit diagram. have been kept together at one side of each board. The second board is doublesided and as supplied by Elektor has through-plated holes. If you make your own (non through-plated) board bear in mind that the two sides will have to be linked by soldering both sides of the component leads as appropriate. Sockets should be used for the ICs and also for the display. The 31/2-digit LCD display requires special attention as it is mounted above the ICs, and the necessary clearance is made by using two 40-pin DIL sockets with the cross-pieces cut out. The leads for the three-pole toggle switch (SI) should be kept as short as is feasible. The bulb to illuminate the display must be mounted level with the LCD and conFigure 2. The values of the frequencies stated in the circuit diagram shown here apply for a four-cylinder pour-stroke sergine. These will be completely different for soother type of engine. As illustrated the switch, is in the "circuit" measuring time "follow" position.

Current consumption is shour? 5 m.A.

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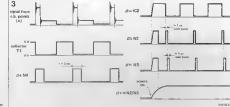


Figure 3. This timing diagram, as already staled in the text, is divided into two parts These must be seen as essentially separate as their time-bases are com pletely different.

Figure 4 The printed cir-

ischometer, as indicated here slong with the com-

ponen) overteys, have

been mede circular in

sort of instrument

order to retain the most

common shape for this

cult boards for the

nected to the board with two off-cuts from thick component leads, for example. We recommend that a 24 V bulb be used as a 12 V one may be too bright. The 'fimshing touch' for our prototype was provided by fitting it into a short length of plastic pipe and adding a perspex 'front glass'.

Calibration and installation

The only part of the circuit that requires calibration is the RC oscillator. The auxiliary 'calibration' circuit shown in figure 5 is used to generate a 50 Hz signal that is then fed into input A of the tachometer. As this signal corresponds to 1500 r.p.m. for a four-cylinder four-stroke engine the display should read 1.50. If this is not the case calibration is carried out by trimming preset Pl

Installing the tachometer is simply a matter of finding a suitable position for it in the car dashboard. It must then, of course, be connected to the appropriate points in the cars electrical circuit: A to the coil side of the c.b. points, B to an unused contact on the lights or dash-lights switch. 0 to the car's earth, and + to a fused +12 V line.

Perte fint

Reserves R1.R2.R3.R7.R9.R10.R13 100 k B4 = 4M7 R5 - 680 k* R6 100 Ω R8 - 47 k R11,R12 22 k 500 k linear present

Capacitois: C1, C2 = 22 p C3 = 1 µ/16 V C4 = 560 p polystyrene

C5 - 100 µ/25 V C8,C7,C10,C11 = 100 n C8 = 10 n C9 = 33 n

Semiconductors D1,D3,D4 - 1N4148 D2 = 12 V/400 mW zener T1 - BC 547B IC1 = 4093 102 = 4060

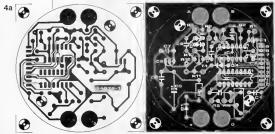
IC3,IC4 - 4518 IC7 = 4056 IC8 - 78L05

Miscellaneous Lat = 12 V I24 V)*/50 mA S1 switch, three-pole

LCD - 3% dsgrt LCD display 12.7 mm cheracter two 40-pin sockets for mounting the LCD display six 16 pin IC sockets one 14-pm IC socket n see text

Using the tachometer

The various uses of a tachometer have been outlined at the beginning of this article so we will not repeat them here. One point must be made, however, concerning the switch, Sl. The short measuring time should be selected when the car is accelerating as the least significant digit then reads zero and is thus less distracting. The 'slow' position, on the other hand is more suitable for motorway driving and particularly for tuning the car. In this latter context, indeed, it is quite feasible to use



净】 elektor 译" GQAGAC? FG_THE_\$} ⁷_BASIC_A} .PRINCIPLE_

BASIC (PART1)

BASIC was introduced as a powerful and yet easily learned programming language for those who intend to use a computer as an aid in solving problems — without the need for extensive knowledge about how the computer actually works. This 'language' was developed at the Dartmouth College in the USA in the early sixties, and since then has become one of the most important programming languages. Increasing interest in computers as a hobby has helped to make BASIC highly popular, The name BASIC (Beginners All-purpose Symbolic Instruction Code) may give the impression that BASIC is an unsophisticated and over-simplified programming aid. However, quite to the contrary: BASIC may be a simple language, but it is very expressive and truly 'All-purposa', At the same time, it is a good language for beginners, as the name suggests: programs can be written in BASIC without any prior knowledge of or

expariance with computers.

All users of microprocessors will agree that programming in "machine language" is not a particularly easy way to develop a program. Machine language is the binary code that tells the computer exactly what to do, step by-step and in the minutest detail. Programming in machine language requires detailed knowledge of the operating principles of the (micro-) computer in question, and it is frustratingly time consuming.

For this reason, 'translation programs' are becoming more and more popular since they are used to translate higher 'programming languages', like BASIC and Fortran, into machine language These higher programming languages are closer to normal English, and contain instructions like.

IF A = 0 GO TO (step) 21, rather than 'read register 03 (= A), compare with 0000 (= 0), etc. By now, being able to understand BASIC has become a fairly standard feature of 'personal computers' and microprocessor development systems. For this type of application, a simplified derivative of the original (and extensive) BASIC language is often used: Tiny BASIC. The main difference between BASIC and Tiny-BASIC is that the mathematical programming capabilities of the latter are much more limited. Nevertheless, Tiny-BASIC is an extremely powerful programming language especially when writing relatively simple programs. As microcomputers have become ever more popular and diversified, new variations of both the original Dartmouth BASIC and Tiny-BASIC have appeared. Virtually every supplier of microprocessors or microcomputers has developed his own version of BASIC, differing marginally from the original BASIC and Tiny BASIC languages, and so a few dozen different BASIC 'dialects' are now in existence. One of these new variants is 'NIBL', derived from Tiny-BASIC and intended specific cally for the National Semiconductor SC/MP microprocessor system.

Compilers and interpreters

BASIC, as such, is not teed to any particular computer. It is a programming language However, to be able to run the program on any particular machine, the program will have to be translated from BASIC into the "machine language" that the computer in question understands. This translation procedure can be performed by the computer itself, provided it has access to a program (in machine language) that tells it how to convert the BASIC instructions into its own language.

Storing a suitable translation program uses up valuable memory space, and so every attempt must be made to keep this program as short as possible. A short program, however, will have limited cape bitties, it may not be able to translate all possible BASIC instructions, and it will be tailored to suit the capabilities that particular (micro-)computer. The result is that most translation programs are not only tied to a particular machine, they are also designed to translate one particular BASIC disactor. This is where the "few dozen different dialects" come in. Fortunately, all these dialects have been



Introduction to a simple computer language

made as similar as possible to 'basic BASIC' so that 'if you know one, you know them all' — up to a point.

Franslating programs from BASIC into a machine language can be done in two different ways.

Compiler

The fatter system is to first translate the whole program from BASIC into mechine language, and only then start to run the actual program. Effectively, therefore, two distinct steps are performed consecutively: the program is first translated and then carried out (see figure 1). A translation program that works in this way is called a 'compiler'. Once the program has been translated it can be run as often as required, without any further need of the translating capabilities of the compiler.

Interpreter

The second possible system is to translate and perform the program line by line The translation por gram, known as an 'interpreter' in this case, reads one stop in the original BASIC program, insterprets' it and causes it to be carried out immediately (see figure 2). Only then does it solds at the next step. Effectively, a BASIC interpreter consists of a large number of very short programs ("subtroutines") in machine language, each of which can be started by one particular command in BASIC.

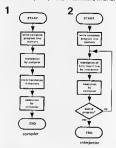


Figure 1. When a program is entered into a compiler, the first step is to translate the complete program into machine lenguage. Only then is the program actually carried out by the computer.

Figure 2. An interpreter 'transletes' the program line by line. After each line has been transleted, the corresponding instruction is carried out immediately; only then is the next line translated.



Advantages of an interpreter

This second system has advantages when (short) regrams are to be run direct from the keyboard. Any programs are to be run direct from the top and responsible of the responsible of the responsible of the responsible of the running short programs under a simple calculations, operation of the grown such as simple calculations, operation of the point of the result of each instruction is available immediately after it has been keyed in.

Disadvantage of an interpreter

When running larger programs, however, an interpreter has a might offusion and when compared to a compiler. Subroutines that are used several times when running the program must be translated times when running the program must be translated times and time again by the interpreter. A compiler, on the other hand, translates each subroutine only once. When the compilers program, including all subroutines, is available in machine language the program itself is run. In this case, no matter how often a particular subroutine is called up the computer can get on with the job without having to call in the compiler again.

To sum it up briefly: a BASIC interpreter is useful when running short programs, a compiler has the edge when larger programs are considered However, the choice between the two is not only a question of weighing the pro's and cons. The choice is limited to what the various manufactures have to offer — unless, of course, one is prepared to write the necessary program. . . . in machine language BASIC translation programs for micro-programs and programs for micro-programs are almost invalidably interpreters are almost invalidably interpreters.

Flow charts

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Before attempting to write a program in BASIC, one must have a fairly good idea of the overall program 'picture'. The program can be seen as a road,

BASIC (PART1)

leading from the intended input data to an ultimate goal offers, several rouses will be possible and it becomes a question of personal skill (and personal taste) to choose the best. Having decided on the 'route', it can be sketched in 'shorthand' in a so-called flow ohert. Writing a program in BASIC then becomes a questron of 'translating' this flow chart into BASIC. For this reson, a good understanding of flow charts is an essential aid when learning the principles of BASIC.

A flow chart is a quick way of sketching a complete program, in the same way that a block diagram can be used to give a general impression of a complex electronic circuit. When drawing a flow chart, a number of standard symbols are used; these help to make the program legible — but to interested third parties and to the programmer himself, at a later date.

The value of a flow chart

The value of a good flow chart can hardly be overstressed: at the best of times a program, even when written in BASIC, is not particularly easy to follow. The flow chart is an important aid when writing the program; it is a clear way of explaining the program to others; and, last but not less; it is an important part of the "instructions for use" when re-running the program at a later date.

Symbols used in flow charts

For the moment, only the most important symbols used in flow charts will be discussed. Their precise meaning and use will become clear in the examples which will follow. The most common

A TYANT GENERAL

G C A GENERAL

C C C G GENERAL

C C C G GENERAL

C C C G GENERAL

TETRINAL

E SED TETRINAL

Figure 3. The main symbols used in a flow diagram.

symbols are shown in figure 3. The first step in any flow chart is 'start'; this is contained in a socalled 'terminal' symbol, as shown in figure 3a The same symbol is used, with the word 'end', to indicate completion of the program (figure 3e). The 'operations' in a program are contained in a rectangle, as shown in figure 3b; the actual operation is indicated by the text within the rectangle. This text should be as brief as possible: only those steps are listed that are important for an understanding of the complete program. In the example given, 'C = A+B' is sufficient; steps like 'fetch A'. 'fetch B', etc. are omitted. A further limitation is that not all operations need be listed; a consecutive sequence of operations can often be summarised in a single rectangle, e.g. 'D = sinA + cosB + (C!)2'. Only in this way can a clear overview of extensive programs be maintained.

Most programs consist of more than a succession of operations. Somewhere along the line, the computer will be required to make decisions: Have all variables been checked? "Have all required points been plotted?", etc. This type of "decision" is entered in the flow other as a diamond (fligure 3c), entered in the flow other as a diamond (fligure 3c), compared. The computer must decide whether they are equal or not, and the route to be followed from here depends on the result of this decision: "no" or 'yes'. A three-way decision is also possible, using the same symbol. The result of a comparison may lead to the result of a Comparison may lead to the different routes; if C > D, then (S. Sweal other Comparisons an election are possible, as will be discussed later.

When running a program, the operator will often require the results of intermediate calculations or tests conducted by the computer. In this way, he can 'keep an eve on the program' and override it if necessary To this end, the computer can print the relevant data on a terminal or video display unit (VDU). This printing of intermediate results is listed in a flow chart as shown in figure 3d, went is listed in a flow chart as shown in figure 3d, went symbol is also used when further input data are required at this point in the program.

The four symbols discussed so far are the mainstays of any flow chart. Long and complicated programs, however, lead to long and complicated flow charts that may well spread over several pages. A further symbol, shown in figure 3f, caters for this: the 'connector' is used to join the various parts of a flow chart.

Using only these symbols, it is possible to 'rough out' almost any program. As mentioned earlier, the examples given further on will help to clarify the use of flow charts and the various symbols used

Calculator or computer?

Cheap pocket calculators have become commonplace. All of these units have one similar capa-

bility: they can perform the four basic mathematical operations (add, subtract, multiply and divide).

Calculations using these four basic functions can also be performed by a computer. As we will also be performed by a computer is in fact capable of performing much more complex calculations. Which is just as well for manufacturers of computers — it is unlikely that they will be forced of the market by main facturers of pocket calculators. Computers and calculators are definitely not the same thing.

A significant difference between computers and calculators is the keyboard. In acclulators, the hexpoard in acclulators the keyboard is an integral part of the unit — in fact, the cheapest calculators consist only of the keyboard of the keyboard of a computer, on the other hand, the display and a single integrated circuit. The "keyboard" of a computer, on the other hand, the display has a completely separate unit. Commads and other information are entered into a computer by means of a so-called "terminal", which is wen in some cases linked to the computer by



The terminal is the link between man and machine: it makes 'conversation' between the two possible. The keyboard of a terminal is very similar to a normal type-writer keyboard, and up to a point its function is also similar. In some cases the terminal will actually type on normal paper, in others the text appears on the screen of a "Video Display Unit" (VIDU, Keyboard and VDU together and the "Elekterninal" reconstruction of the "Video Display Unit" (VIDU, Keyboard and VDU together and the "Elekterninal" reconstructions of the "Elekterninal" reconstructions

The second major difference between calculator and computer is the way in which the results of calculations are presented. A pocket calculation are presented. A pocket calculation only has a numeric display; is a limited number of digits light up to indicate the result of a calculation. A computer, on the other hand, and also the calculation. A computer, on the other hand, or no paper programs the letters and numerated to risplayer and contractions of the other hands or to give an object to the calculation of the computer of the computer of the computer of the calculation of t

'Hello' in computerese . . .

Let us assume that we have a simple computer, a BASC interprete and a terminal. The interpreter will 'say Hello' as soon as the translation program is started. The initial procedure varies from computer to computer: in some cases it is sufficient to switch on and operate a reset button (as will be the case in the Elektor NIBL computer); some times a more extensive procedure must be carried out. The complete procedure is always described in the relevant instruction manual.

The way in which the computer announces its readiness is determined by the interpreter program. Normally this first announcement will be as short as possible, in order to save memory space. A few examples:

ROM BASIC 1.0

READY

DOE TINY BASIC V1.0

OK

These two initial announcements are provided by a BASIC interpreter for the Motoroia M8800 micro-processor, and a DCE Tiny BASIC interpreter for the B80e, respectively These announcements are relatively wordy. By comparison, the AMI S6800. The Tiny BASIC interpreter merely prints a colon (1) at the beginning of a line. Similarly, NIBL prints only only ymbols [1, 2) or #, at the start of a line) are called 'irromptis'. A prompti indicates that the interpreter is ready to receive information (from the keyboard).

As mentioned earlier, the complete initial procedure — including the type of prompt that is to be expected — will be described in the instruction manual supplied with the computer or interpreter program.

Program lines

No matter what programming language is used (BASIC or any other), the program must always be listed in numbered lines. One line may contain more than one instruction, provided they are clearly separated by means of a colon (;).

Since a computer, once started, will keep going until it is told to stop, all programs must be terminated by the instruction END. A very brief program might therefore be listed as follows:

10 INSTRUCTION 1

20 INSTRUCTION 2

35 INSTRUCTION 4: INSTRUCTION 5 40 INSTRUCTION 6

40 INSTRUCTION
50 END

It is common practice to locate the first instruction on line 10, and the following instructions on lines 20, 30, etc. Why the gaps? In practice, the first attempt at writing a program is rarely complete

BASIC (PART1)

- one almost invariably overlooks one or more necessary instructions. By initially placing the instructions at ten line intervals, it becomes possible to add further instructions at a later date without having to rewrite the rest of the program - by using the intermediate lines. An example is line 35 in the brief program listed above. If the worst comes to the worst, up to 9 full lines of instructions can be added without having to rewrite any other part of the program. The interpreter will see to it that instructions added at a later date will be carried out at the correct point in the program. When the program is run, the computer will simply carry out all the instructions in the order in which they are numbered, without worrying about any intermediate gaps

It is also possible, at a later date, to modify or delete instructions if required. The procedure is simplicity itself: to modify an instruction the line number is typed in, followed by the new instruction. Deleting an instruction is accomplished by typing in the line number, immediately followed

by 'CR' (Carriage Return).

Carriage Return is as important when writing a program as it so nay normal typewriter. At the end of a line — or, for that matter, whenever a new line is required—the CR key must be used. The interpreter will respond with a 'prompt' symbol, after which the new line number and the new first of the control of the state of the control of the page."

It is also possible to key in program lines without assigning them a line number. In this case, the instruction(s) will be carried out by the interpreter as soon as the CR key is operated, in other words, the computer will then operate like a pocket calculator, carrying out instructions immediately and then forgetting that they ever existed.

Statements

Like any other language, BASIC uses words, and they are derived from English, which is a help. The vocabulary is limited and simple, so that even beginners can pick if up quickly! A word in a problem to the computer to perform a mission of the computer to perform a mission of the computer to perform a mission of the statements described in the first two parts of this stress, are common to all BASIC and Tiny BASIC dialects. Since they have the same meaning in all dialects of the statements of the statements of the statement of t

when it comes to Jiny BASIC dialects, NIBL will be of particular interest — since it is suitable for the Elektor SC/MP system. However, the differences between NIBL and the other BASIC dialects are so marginal that a general understanding of BASIC can be gained.

PRINT and RUN

A simple calculation, performed on a pocket calcu-

lator, leads to a visible result on the numeric display. Not so for computers. The computer is quite happy to keep the final result to itself, storing it somewhere in memory, unless it is specifically ordered to print out this result. If the human operator would also like to know the final result, he must make use of the PRINT statement.



If a short calculation is to be performed immediately, without actually storing the program, line numbers can be omitted and the total print-out could be as follows:



After the first 'prompt' symbol, the instruction PRINT has been keyed in, immediately followed by the calculation required. When the CR key is then operated (this does not appear on the display) the interpreter immediately ensures that the statement is carried out and that the result is

CPARTI

printed on the next line. It then returns to the beginning of the next line and prints a further prompt symbol, indicating that it is ready for the next statement

If a program for the same calculation is keyed in. this will be stored in the computer memory, ready for re-run at any time. Writing such a program is simplicity itself



After the program has been typed in (the first two lines), the computer must be told to cerry out these instructions. This is eccomplished by typing 'RUN', followed by CR. The computer then performs the calculation end prints the result (11) on the next line. The main difference between this program and the short calculation performed abova is that if, at a later date, 'RUN' is again keyad in the computer will repeat the celculation and again print the result. If only the short calculetion had been performed, the computer would either 'look blenk' (printing a ?, for instence) or alse run some previous program.

After printing the result of the calculation, the interpreter has produced the print-out 'BRK AT 20' This is an abbreviation for 'Break at 20', end signifies that the program hes been terminated at line 20: the END statement. This 'final call' varies from one interpreter to another: 'BRK AT . . .' is the termination announcement as provided by NIBL. Other interpreters often print out 'READY This termination announcement is always followed automatically (as part of the interpreter program) by a CR and LF - carriage raturn and line feed, respectively - end a prompt symbol.

The possibilities of a PRINT statement are more extensive than this simple example may suggest. It is also possible to print a full line of text, or a combination of text and results of calculations. A simple extension of the previous example may help to clarify this:

10 PRINT "5 + 6 =" 20 PRINT 5 + 6 30 END

RUN 5+6= 11

BRK AT 30

Adding quotation marks around 5 + 6 = on program line 10 alters the meaning of this instruction from 'calculation to be performed' to 'text to be printed'. The text 5+6= is printed without modification, and followed automatically by CR and L.F. Then line 20 is read and the instruction is carried out; this line is identical to line 10 in the previous example and the result is the same, the calculation is performed and the result is printed on the next line.

This print out is less than ideal, since text and result are printed on different lines. It would be naater to have text and result on the same line. This result can be achiavad by adding a semi-colon after the required text, as follows:

> 10 PRINT "5 + 6 =";

20 PRINT 5 + 6 > 30 PRINT

> 40 PRINT "5 + 6 ="; 5 + 6

50 END RUN

5+6=11

5+6=11 READY

The effect of the semi-colon is to suppress CR and LF, so that the next PRINT instruction is carried out on the same line. The semi-colon can also be used to separata PRINT statements, es illustrated in line 40, producing the same finel results as lines 10 and 20.

Ona final point remains to be clarified in the progrem example given above. The PRINT statement that is listed on line 30 may appear superfluous. However, since no text or other instruction is included after this PRINT statement, the computer will print the corresponding result on that line: nothing! In other words, this is one way of making the computer leave e one-line gap in the total print-out.

The most important possibilities of the PRINT statement have now been discussed. A few further possibilities will be dealt with later, after some other ralated stetements have been explained.

Questions

- 1. What is the difference between 'standard' BASIC and Tiny BASIC?
- 2. Why is a Tiny BASIC dialect often used for micro-computers?
- 3. What is the main difference between a compiler and an interpreter?
- 4. What are the advantages, and what are the dis-
- advantages, of an interpreter? 5. How did the various 'dialects' of BASIC come into existence?
- 6. Why is a flow chart so important?
- What is a 'prompt'? B. Why should a program line (in a BASIC pro
 - gram) always be numbered? 9. What does the CR key do?
- 10. What will the computer print out as a result of the following instruction in BASIC:

PRINT 3 + 4 + 5

GLOSSARY

binary code

Binary numbers consist only of ones and zeroes For example, the count '1, 2, 3, 4, 5' becomes, in binary: '001, 010, 011, 100, 101' See also: machine language.

character

Any symbol in a print out: letters, numerals, punctuation marks, etc.

compiler

Translation program from a programming language to a machine language. A compiler translates the

whole **Drogram** before any part of it is carried out.

Carriage Return (return to beginning of line in display).

development system

Computer system that is designed specifically as an aid when developing programs (i.e. when writing a new program).

flow chart

A graphic means of illustrating the basic 'lay-out' of a program.

instruction

Describes the next step that the computer must carry out. A program consists of several instructions.

See also; statement.

instruction code

The equivalent, in machine lenguage, of an instruction. An instruction code is a binary number.

interpreter

Translation program from a programming lenguage to a machine language. An interpreter causes each line in the program to be carried out immediately after it has been translated.

LF

Line feed (move to next line in display). This is normally carried out in conjunction with CR (carriage return).

machine language

Binary code in which all instructions must ultimately be expressed, if they are to be understood by the computer.

memory space

Part of the memory that is (or can be) used.

NIBI

National Industrial BASIC Language, a dialect of Tiny BASIC suitable for the National Semiconductor SC/MP systems. Pronounced 'nibble'.

program

A sequence of instructions which, when carried out in the specified order, will cause the computer to perform some specific task.

programming Janguage

A 'language' in which programs can be formulated. Programming languages are closer to 'plain English', and therefore easier for the operator to master.

promot

Symbol that indicates that the computer is ready for the next instruction. Examples: > : #

ubroutine

A small, complete program that will normally be carried out several times in the course of the main program.

statement

Instruction, in BASIC, telling the computer to perform a specific task. Examples: PRINT, END.

termina

Unit that is intended specifically for communication between man and machine, It consists of an input unit (e.g. a keyboard) and an output unit (e.g. printer or VDU).

VDU

Video Display Unit, which allows the computer to 'print' its output on a {TV} screen. See also: terminal.

Summary of symbols and statements used in part 1.

These so-called 'prompt' symbols can be printed at the beginning of a line.

Colon, used as separation between statements, if more than one statement is to be printed on the same line.

Symbol for the addition operation.

PRINT 'ABCD'
The symbols entered in quotation marks will be printed fin this case:

PRINT 3+4 The expression efter the PRINT statement will be performed and the result

will be printed (in this case: 7).
PRINT . . . ; . ; The semi-colon can be used to separete groups of symbols and/or exprassions

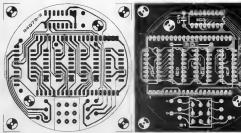
to be printed. A semi-colon at the end of a PRINT statement will result in the following PRINT statement being carried out on the seme line to STATEMENT: A number at the beginning of a pro-

gram line indicates that the following statement is part of the program.

END This statement indicates the end of a program.

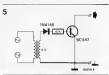
RUN This command causes the computer to start carrying out the program





this tachometer purely as an aid to tuning a car as there is then plenty of time to read the display and take advantage of its accuracy.

The majority of cars today have fourcylinder four-stroke engines so this circuit was designed mainly for this type of engine. The tachometer can, however, be used with most other engine configurations. Full details of this facility are given by the notes in the margin beside figure 2.



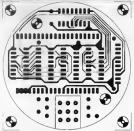


Figure 5. This circuit can be used for calibrating the techometer. Its purpose is to simulate the c.b. pulses (at 50 Hz) normally fad into the circuit at point A by an engine turning at 1500 r.p.m.



Photo 1 Here we see the two boards that make up the tachometer To schieve the compact form shown in the photo at the beginning of this erticle the two are mounted one above the other in the form of a 'sandwich'.

DIRPUT

E. Schmidt

The Ohio Scientific disk operating system, as applied to the Junior Computer, has shown its worth and its versatility. Any user familiar with the DOS software can quite easily change it to add certain options such as those dealt with here. One of these is an extension of the DIR instruction (which lists the files in the directory without using BEXEC) and the second is an extension of PUT (stores files without the need to first add their names to the catalogue). To make it all the more interesting we also introduce a "turbo byte".

DIRPUT

two new commands for the Junior Computer with DOS It is surely a sign of progress (?) when in order to make our lives easier, using computers, we have to complicate them even further, with these same computers. The end result decides whether it has been worth the trouble and certainly in the case of the few changes described here the benefits are clear.

Two extra commands

The new 'DIRECTORY' instruction of the DOS (its shortened form is 'DI'), when it is not followed by a track number, provides a listing of the contants of a diskette from the DOS command interpreter without having to call some BASIC program. It is still possible to use the onginal form

DITT, where TT is the number of a track, The result in that case gives the number of sectors in the track. It is important to remember that only the first half of the directory (32 of the 64 possible file names) can be accessed with the new instruction. This is not usually such a drawback as it is very rare that a diskette contains more than about thirty different files. The existing PUT filename instruction only allows a file to be stored if the name 'filename' already exists in the directory, and that is not very user-friendly. From now on, however, it will be possible to give the PUT command with the name of a file that does not yet exist in the directory. When the DOS does not find this filename it tests to see if there are enough

Table 1.

8 1 2 3 4 5 6 7 B 9 A B C D E F E488: AC E5 2C B1 E1 C9 20 30 8A C9 23 F8 86 20 2E 2D E418: 4C F3 79 28 73 20 8A 28 28 28 44 49 52 45 43 54 E429: 4F 52 59 8D 8A 8A 88 28 1F E5 A9 79 85 18 A9 2E E438: 85 11 20 34 E5 F0 20 A8 80 B1 16 28 43 23 C8 C8 E448: 86 D8 F6 28 73 20 28 28 88 A8 86 B1 18 28 92 20 E458: 29 73 20 28 20 28 88 A8 87 81 18 28 92 20 29 73 FAGE: 20 80 84 88 28 41 E5 D8 C9 68 28 54 27 28 84 26 E478: 28 66 26 4C 61 27 68 AA 68 A8 48 9A 48 C9 DF D6 E488: 79 C8 28 D8 25 A5 E8 80 E5 20 28 IF E5 28 78 E5 FARRY OD 70 30 R5 18 OR A2 FF FR FR 28 FR 7E 88 38 RE E4A8: BO AE E5 FO F3 A4 10 4C 98 E4 A9 8C D8 6A 38 8A E480: E5 10 AA A9 00 18 FB 69 01 CA D0 FB D8 48 20 58 E4CM: E5 D0 52 68 A0 06 91 10 C8 18 F8 6D 7D 3A E9 00 E400: D8 91 10 38 A5 10 ED E5 2C 85 10 B8 02 C6 11 AC E4E8: E5 2E A2 86 B1 E1 C9 23 F8 84 C9 19 18 86 C8 91 E4F8: E1 88 A9 20 91 10 C8 CA D6 EA A9 01 80 5E 26 8D ES00: SF 26 A9 79 85 FE A9 2E 85 FF 20 54 27 20 E1 27 E518: 68 68 4C DD 28 68 A9 8F 4C 48 2A A9 8E D6 F9 A9 E520: 79 85 FE A9 2E 85 FF A9 12 28 BC 26 A9 81 RO 5E E538: 26 4C 1A 28 AN 85 B1 18 C9 73 D0 84 B8 18 F7 C8 E548: 68 18 A5 18 69 88 85 18 A5 11 69 88 85 11 C9 2F F558: DR 88 A5 18 C9 79 DR 82 A9 88 68 A9 79 85 18 A9 F5&4: 2E 85 11 20 34 E5 F8 07 20 41 E5 D8 F6 A9 FF 68 E579: A2 27 A9 88 90 AE E5 CA 18 FA 8E AE E5 A9 79 85 E589: 10 A9 2E 85 11 20 34 E5 F8 1E A0 07 81 18 88 48 F598: A2 FF 38 F8 E9 81 E8 88 FB 68 38 F1 18 D8 A8 A9 F5A9: FF 9D AE F5 CA 88 10 F9 20 41 E5 D0 D8 48

Table 1. Two new functions, DIR and PUT, can be incorporated into the Junior Computer's disk operating system by loading this program in the appropriate place Table 2. DIRPUT

free tracks to store the file. Assuming there is enough space the new filename is included in the directory and the PUT instruction is executed normally. As with the new DIR command, this new PUT instruction only considers the first half of the directory. If the available tracks contain non-documented data (with no file name) they will be destroyed by the new file None the less the new PUT command can only be used with appropriately formatted diskettes. If the DOS does not find enough free tracks for the file it gives the message 'ERR#E' and if there is no space in the first half of the directory for the new file name the message 'ERR#F' appears.

How it is done

As we are making some fairly significant changes we may a well alteration to the HO through control of the HO through cont

In order to earry out the modifications to your system simply follow the instructions given below. In the interests of simplicity the new programs is located in RAM at £469HgX. There are other, shorter but more complex, possibilities but we prefer to avoid them here. Start by making a copy of the Oth Scientific tuthoral disk in the version used for the junior Composition of the program of the other control of the program o

- Start the extended monitor (EM) and load the hexdump from table 1 at the address indicated.
- Save this program with the instruction ISA 12.5 = E409/2 (it just so happens that sector 5 of
- track 12 is free)

 Load tracks 1 and then 8 as follows:
- |CA 4A00=01,1 |:EX 4IFD=00 The extended monitor allows the changes given in table 2 to be made

changes given in table 2 to be made quite easily after giving the command: :D428442AØ A number of addresses must then be

A number of addresses must the changed: 4E42:FF 4E43:E3

4663:4C 4664:6A 4665:E4 4E0D:76 4E0E:E4 and finally 46A5:D2

- to speed up the head movement.

 Reload the contents of track 1 on the disk with the instruction ISA 01.1=4.800/8
- Load the track 9 control routine as follows: !CA9290=96,4 and then start it with the command.

0 1 2 3 4 5 6 7 8 9 A B C D E F 4280: 8C 00 23 A2 01 8E C6 2A A9 00 85 FE A9 E4 85 FF 4290: A9 12 20 8C 26 A9 05 8D 5E 26 20 67 29 4C B3 22

Table 3.

88 PRINT: PRINT: PRINT: PRINT

98 PRINT *CHOOSE ONE OF THE FOLLOWING OPTIONS:"

188 PRINT "CHOUSE ONE OF THE FULLDATING

110 PRINT . - DWARLE DOS-EXTENSIONS (1)

128 PRINT * - DISABLE DOS-EXTENSIONS (2)*

140 PRINT SPC(7):: INPUT "YOUR CHOICE ":CHOICE

150 IF CHOICE=1 OR CHOICE=2 GOTO 200

160 END 208 DIH ADDR(6) BYTE(6)

218 REM ADDRESSES

220 DATA 11842.11843: REM POINTER TO 01-1

238 DATA 9827,9828,9829; REN JMP TO HOME 248 DATA 11789,11799; REN POINTER TO PUT

258 REM DATA

268 DATA 255,227,76,186,228,118,228

278 DATA 48.43,32.138.38,75,42

288 REM LOAD MACHINE LANGUAGE ROUTINE FROM TR 12, SEC 5

298 IF CHOICE=1 THEN DISK! CA E488=12,5"

386 REM CHANGE ADDRESSES IN OOS

310 FOR 1=0 TO 6: READ ADDR(1): NEXT

320 IF CHOICE=1 GOTO 340 330 FDR |=6 TO 6: READ DUMMY: NEXT

348 FOR I=8 TO A: READ BYTE(I): NEXT

358 FOR I=D TO 6: POKE ADDR(I) .BYTE(I): NEXT

348 ON CHOICE GOTO 408.508
488 PRINT: PRINT: --- DOS-EXTENSIONS ENABLEO ----

418 PRINT ***! MEHORY FROM \$E488 ON IN USE !!!"

428 NEW
566 PRINT: PRINT " --- DOS-EXTENSIONS DISABLED ---"

510 NEW

IGO 0200 Save the contents of track 0 by means of

W4200 22008 and the modifications are finished.

The versatile solution

Rather than making any permanent changes to the existing DOS it may be better to be a bit cautious, which is possible with the BASIC program shown in table 3. This is, in effect, a DOS extensions enable/disable switch. The RAM between E400HEX and ESADHEX remains usable if you decide to include the new PUT and DIR instructions, but there is no longer any need to manually change the contents of tracks @ and I as the BASIC program takes care of this. If you decide to place the machine code program somewhere other than sector 5 of track 12 do not forget to change the load instruction in the program of table 3 (DISK!"CA E496=12.5").

Table 2. These are the changes that must be made to track 9 of diek 5. The modifications to track 1, and the procedure for saving tracks 9 and 1, can be found in the main text.

Table 3. If you are reluctant to permanently modify the contents of tracks 9 and 1 of your main diskette or if you do not want to monopolise part of the RAM this BASIC program can be used according to the instructions given in the leat peragraph of this article SCART is the name given to the new plug-and-socket connection between a television receiver and associated equipment such as a video recorder or stereo amplifier. The name is an acronym of Syndicat des Constructeurs d'Appareils Radiorécepteurs et Téléviseurs, the French association of radio and television receiver manufacturers. This association decided some time ago to terminate various inputs to, and outputs from, TV receivers into a 21-way socket, which is becoming a European standard.

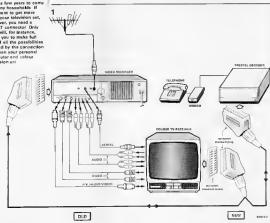
SCART adapter

European standard for video and audio connections

Figure 1. The 'old' arrangement will undoubtedly pertain for quite a Isw years to come In many households If you went to get more from your television set. however, you need a SCART connector Only that will, for instance, ellow you to make full use of all the possibilities offered by the connection between your personal computer and colour televation est

The reasons for the adoption of e multiway connector are not hard to find. Not so long ago, all thet needed to be connected to the TV receiver were the mains supply and the aerial. Nowadays there is the video recorder, video disk player, games computer, Prestel, and the facility to feed the audio to your hi-fi installation. The cables required for all these connections would make the back of your television set look like a hi-fi rack. Another aspect of this is that even if sockets for all these connections were provided on the TV set. they would more often than not be of a different type than the plugs provided

with the recorder or games computer. And special cables or adapters are not exactly cheap! Connections old and new are shown schematically in figure 1, It is clear that those between a video recorder and the TV set are quite simple: a disadvantage is, however, that the r.f. signal is taken to the TV receiver via en r.f. modulator in the video recorder. This round-about way of signal transfer leads to reduced picture quality, and normally it also means that only one ancillary unit, in this cese the video recorder, can be connected to the television receiver at eny one time.







A/V connector

The quality of the transferred signal is improved by applying the audio and video signals direct to the relevant amplifier in the TV receiver or video recorder. This may be done via BNC (video) and phono (audio) connectors as shown in figure 1: four conductors are then required for recording and playback. Many modern TV sets are therefore fitted with a six way A/N (audio/video) socket, Signals applied to the A/V socket should really be amplified by 6 dB (2x) and it is therefore advisable to use the A/V socket for playback only; recording can be accomplished direct from the aenal. To do this, pins I and 5 of the A/V socket must be interconnected. Signals are transferred between the TV receiver and the video recorder via a sixway cable, but even so, and in spite of the improved transfer quality, only one ancillary unit can be connected to the TV set at any one time.

SCART connector

The use of a number of ancillary units becomes possible only when SCART connectors are provided. An example of this is shown in figure I from which you can see that a colour TV receiver fitted with a SCART socket can be connected to the Prestel service and to a video recorder at the same time.

The pin connections of the A/V and SCART connectors are given in table I, from which it becomes clear why the

Pin	Recording	Playback
1	Input switching voltage 0 V	input switching vollage +12 V
2	Video ontput Output impedance. 75 Q Nominal output voltage 1 Vpp into 75 Q Colour sync signal Invet: 0.3 V	Video input Input impedance: 75 Q Nominal input voltage: 1 Vpp Colour sync signal level: 0.3 V
3	Earth	Earth
4	Audio output (left-hand) Output impedance: <1 kΩ (above 20 Hz) Output signal level 0.12 0 V into 10 kΩ	Audio input (left-hand) Input impedance: >10 kQ (above 20 Hzl Input signal level 0.1 2.0 V
5	Supply voltage input: +12 V	Supply voltage inpnt +12 V
6	Audio output (right-hand) Output impedance: <1 kΩ (above 20 Hz(Output signal level 0.1 2 0 V into 10 kΩ	Audio inpu1 (nght-hand) Inpu1 impedance: >10 kQ (above 20 Hz) Input signal level; 0.1 2.0 V
SCAF	RT connector	
Pin	Function	Level
1	Audio output (right-hand) or channel 2	0.5 V for output impedances ≤ 1 kΩ
2	Audio input (right-hand) or channel 2	9.5 V for inpu1 impedances ≥ 10 kQ
3	Audio output (left-hand) or channel 1 or mono	0.5 V for output impedances ≤ 1 kQ
4	Audio earth	
5	Blue earth	
6	Audio input (left-hand) or channel 1 or mono	0 5 V for Inpu1 impedances ≥ 10 kQ
7	Blue component	Difference between peak value and blanking signal level = 0,7 V; load impedance = 75 S supanimposed direct voltaga = 0 2 V
8	Switching vollage: 0 = TV reception 1 = operation of associated units	0 = 0 2 V 1 = 9.5 12 V at input resistance of ≥ 10 kQ and input capacitance of ≤ 2 nF
9	Green earth	_
10	Not used	
11	Green component	Identical to 7
12	Not used	
13	Red earth	
14	Not used	
15	Red component	Identical to 7
16	Blanking signal 1 = blanking	0 = 0 0.4 V 1 = 1 . 3 V Load resistance = 75 Q
17	Video earth	coor resistance - 10 M
18	Blanking signal earth	
19	Video outpul	Difference between

- 19 Video output
- 20 Video inpul Housing screen and/or earth
- Ofference between peak white level and sync signal = 1 V; Output resistance = 75 Q; Superimposed direct voltage = 0 . . . 2 V Synchronization signal only = 03 Vpp Identical to 19 Connected to chassis

Table 1. Pin connections of the A/V and SCART COMPRETORS

SCART connectors have twenty-one pins. In contrast to the AVP facility, the recording and playback signals are not switched but are available simultaneously. Apart from the audio and video inputs and outputs, there are connectors for the red, green, and blue signals, and the blanking signal. Together with the individual earth connectors for all these lines, this accounts for the use of a botal of strateen

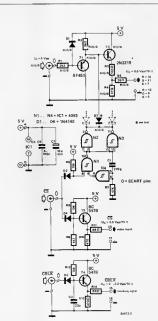
Of the five remaining pins, one is used for switching from TV reception to operation of one of the ancillary units. With AVV connectors (and in some cases even with the SCART arrangement) this switching is carried our manually. Pin 21 is connected

to the housing of the SCART plug or socket and therefore also to the chassis (earth) of the TV receiver. Standard connections to pins 10, 12, and 14 have as yet not been agreed, although it appears that in due course 10 and 12 will be normalized as data connections.

SCART adapter

Proper normalization is, of course, not only a matter of co-ordinating the pin connections, but also one of matching the input and output levels of the TV receiver and the various associated units. For that reason we have included these levels in Table 1. The question remains, however:

Figure 2. The circuit of the SCART adapts consists of the buffer stages for the video and blanking signels, and the metching stages for the red, green, and blue signals. The output levels are in accordance with those given in table 1.







Perts list

Resistors* R1,R2 = 2k2* R3 = 100 Q* R4 = 47 Q*

R5 = 39 Ω* R6, R8, R12 = 10 k R7 = 47 k R9 = 4k7 R10, R13 = 470 Ω R11, R14 = 88 Ω

Capacitors C1 = 270 p

C2 = 33 p C3 . . . C6 = 100 n C7 = 10 u/10 V

Semiconductors: D1, D2 = 1N4148* T1 = BF 451* T2 = 2N2219*

T2 = 2N2219*
D3 . D5 = 1N4148
T3, T4 = BC 547B
IC1 = 4093

Miscelleneous: 10 BNC sockets Screened case Printed circuit 84072

one of each required for red, green, and blue respectively

how can you be sure that the output level of your personal computer matches the sensitivity of the RGB inputs of the TV set? Well, we have designed a means of answering that!

answering that!
The sensitivity of the SCART inputs is too high for signals at TTL or CMOS levels, such as, for instance, the outputs of home computers. Moreover, the input impedance of TS 0 is too low. We have designed matching circuits to counter these differences and accommodated them all on one printed circuits.

With reference to figure 2, only simple buffer stages (73 and 74) are needed to keep the level of the video and blanking signsis within acceptable limits. These stages should preferably be fed from open-collector outputs (collector resistance about 330 Q.).

The red, green, and just signals should be signaled to the red green and pure contributed to the red green and the red g

Red, green, and blue signals at a frequency lower than the line frequency must be switched off periodically to allow the input capacition in the TV receiver to ducharge Thus switching is arranged by ICI which is connected to the colour processing stage via clodes D2 (one for each colour), if the colour signals are obtained via the 'analytical video display' (see Elektor, May 1964, page 5-31), ICI is delimitely necessary. If, however, there are no coupling capacitors in the TV set, ICI

may be omitted. All stages can be accommodated without any problems on the printed-creuit board shown in faque 3. All signals are fed to be a signal of the stage of the stage

It may well be that by using the circuit and relevant control signals you will discover numerous new aspects in your television receiver. Good luck with the experiments!

socket arrangement.

Figure 3 Copper track and component layout of the printed circuit for the SCART adapter. Each (acreened signal line has a experste earth connection for which corresponding terminals have been proyided.

Ready-made equipment cases are often quite expensive and sometimes impossible to get in the dimensions required. In such instances many electronics hobbyists design and produce their own cases. As they often have neither the skill nor the tools to work with perspex or sheet iron, they invariably turn to aluminium, which is light, easy to work, and looks good. The only troubles with aluminium are that it does not look so good when it oxidizes and that it is easily scratched. The remedy for these troubles need not always be paint-spraying; anodizing is a worthwhile and attractive alternative.

anodizing

in your home workshop

J. Laakmann

Anodizing is a process whereby a hard, non-corroding oxide film is deposited onto the aluminium. This film is harder and far more scratch-resistant than the aluminium itself; it also protects against fingerprints which can be a real nuisance.

Ingredients and aquipment required:

caustic soda lye (1:10) nitric acid

sulphune acid solution (1:7) dustilled water

a piece of lead a suitable tank

a variable mains power supply or heavyduty battery

Because of the sulphuric acid, the tank must be of glass or plastic, and il must, of course, be large enough for your purposes. A photographic developing tray

1

may, for instance, be suitable; alternatives are a large plastic bottle or container with the top cut off, domestic glass basins, plastic washing-up bowls, and so on. An electric direct current of 1.5 . . . 2.5 A for every 100 square centimetres (16 in2) of aluminium should be available. This is most easily obtained from a variable power supply, but a suitable heavy duty battery and appropriate variable resistor to keep the current within the limits stated. may also be used.

For the electrolysis the aluminium is made the anode, while the cathode is formed by the piece of lead. The surface areas of the aluminium and lead should be roughly equal.

Obtaining the chemicals should present no problems, although you will not be able to buy them in the concentrations required. The caustic soda lye is prepared by sturring 10 grammes of sodium hydroxide in 100 ml of distilled water: this solution cannot be stored in glass vessels, only in plastic ones. The concentration of the nitric acid is not critical; add one part to about nine parts of distilled water. Preparation of the sulphuric acid solution is a little more complicated although the following formula should be a great help. m1 = m2 (x% - y%)/y%, whereml = distilled water by weight

m2 = sulphuric acid by weight

acid solution

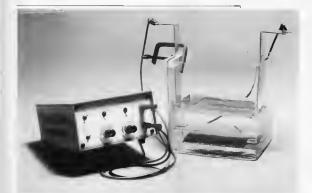
x% = concentration of sulphuric acid v% = concentration of required sulphunc

If, for instance, a 1:7 (sav. 15%) sulphune acid solution is required, and 250 grammes of sulphuric acid in a 50% concentration is available, the relevant

amount of distilled water by weight is 583 orammes. Warning! Always add acids to water,

never water to acids. Always be careful when working with these chemicals. Ensure good ventilation of the working space, do not smoke (because of the production of highly combustible oxy-hydrogen gas), do not wear

10,46 elektor india october 1984



your best clothes, and do use rubber or plastic gloves and some form of effective eye protection.

Processing

First smooth the aluminium with grade 400 wet-and-dry emery paper; take care not to overheat the aluminium as this may cause blemishes during the anodizing. Next, immerse the aluminium for about ten minutes in the caustic soda lve (at room temperature) to remove all grease. Decologration often occurs, but this disappears when the aluminium is etched in a 1:10 nitric acid solution. Only now can the actual electrolysis take place. Suspend the sheet of lead, connected to the negative terminal of the power supply or battery, in the sulphuric acid solution. The aluminium should be connected to the positive terminal via a strip or piece of aluminium; other materials may dissolve during the processing. A suitable means would be an aluminium C-clamp as used in model building in which a screwthread is cut as shown in figure 1. The supply cable is terminated into a soldering tag which is fastened to the clamp by a screw driven into the freshly cut thread. The aluminium work piece or sheet must be slightly larger than required because no anodizing can take place under the clamp

At a solution temperature of 16...20°C (inspect frequently), the processing will take about one hour. The solution may have to be cooled now and then, while an occasional stir is also advisable. When the

current drops, the electrolysis may be terminated. The aluminium work piece should be thoroughly rinsed in distilled water after each of the operations described.

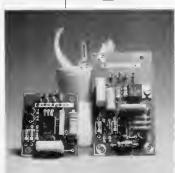
Finally, the aluminium must be condensated in boiling water for about lifteen minutes. In this, the pores of the oxide film close to some extent and the whole work piece hardens.

Protection of the environment

When the chemicals are no longer required, they should be neutralized before they are flushed away. Nitric and sulphunc acids may be neutralized with the caustic soda lve. You may not have enough left of this and it is then necessary to make up some more. The pH value may be checked with a pH meter or litmus paper (which turns red in acids and blue in alkalis). It is also possible to use indicators such as phenolphtalein (C20H14O4) which is colourless in acids but turns red in alkalis, or methyl orange (C14H14N3NaO3S) which is red in acids and gradually changes through orange to a full yellow colour in alkalis.

Most incandescent bulbs have an estimated lifespan of about a thousand hours of brightness. The actual time when the bulb blows is determined by the weakest link — the thinnest part of the filament. The most obvious way of increasing the filaments lifespan is to concentrate on the weak part, and in particular by limiting the peak current at switch-on as this is what causes the filament to burn out. Two versions of this zero-crossing switch have been designed and both are easily built into existing installations.

lamp saver



increases the longevity of incandescent bulbs by switching them on at the zero-crossing point of the mains

About this time of the year home owners are forced to start thinking about making certain preparations for the winter, such as ordering fuel for the central heating. At the same time most people are likely to consider how to economise, and not only on the central heating bill, Lower wattage tight bulbs are then used and insulating products are bought. More significant savings can be made by changing your habits, by drawing curtains sooner, turning the central heating down one or two degrees, by not leaving doors open unnecessarily and so on. These economy measures do not even involve any additional expenditure.

Under the heading of 'energy conscious habits' is the idea of switching off lights in the room you are just about to leave. This certainly saves some electricity but it is not without fault. Continuously switching an incandescent lamp on and off will shorten its lifespan quite considerably.

The filament in an incandescent bulb has a lower cold resistance than when it is warm, so it acts as a resistor with a positive temperature coefficient (PTC). The peak current at switch-on will therefore be much higher than the maximum continuous current, especially if the light is switched on when the voltage is near its maximum value (see figure 1). In order to understand what this high current does to the lamp we must realise that the filament is not smooth and even but rather it is very rough. The switch-on peak will therefore cause hot spots to appear at the. points where the filament is thinnest. These points suffer from wear and tear which eventually leads to one of them burning through, usually immediately upon switch-on. The lifespan of the incandescent lamp is

The lifespan of the incandescent lamp is determined by the weakest point in the by determined by the weakest point in the part which man the lamp at the most favourable moment, namely at the zero-crossing point of the mains. During the first quarter cycle of the mains the uring the first quarter cycle of the mains the order through the fillament will heat it onough so that when the voltage is first at its maximum the resistance will be high enough to keep the current, and therefore the temperature of the hot spoi, (artly low (see Equire 2)). In this way the lifespan of the lamp is improved.

The requirements

What we want is a circuit that will detect the zero-crossing of the mains and switch on the appropriate lamp(s) at this time. It should also be easy to fit into existing installations without having to run extra wires or knock holes in the masonry. Finally the cost should be low to enable the investment to be earned back quickly. The circuit described here meets these demands and is a particularly attractive proposition if you use expensive bulbs. Another interesting application of the lamp saver is in cases where the lamp is difficult to reach for replacement. Lest there be any misunderstanding, let us state clearly that this circuit is only useful for incandescent lamps. It cannot extend the lifespan of fluorescent lights at all.

The two versions

Building this lamp saver circuit into existing installations is simplified by the fact that we have developed two different versions. Version I is a circuit that must be fitted to an existing lamp but no changes to the wiring are required. The wires that were connected to the lamp fitting are now connected to the circuit instead and the fitting is linked to the appropriate points on the printed circuit board. Version 2 is somewhat smaller as it is primarily intended for mounting behind the light switch in the wall. If there is not enough space for this the original switch can be replaced by a miniature 240 V one as the current passing through it is very small. Version 2 is not suitable for use with two-way switches; version I must be used

in this case. The circuits

Moving on to the circuits we will now begin with version I. Technically speaking this is the more interesting of the pair. Various sections can be seen in the circuit diagram of figure 3: a d.c. supply for the gate pulses (Rl, Cl, C2, Dl, D2), a zerocrossing detector (R2, R3, T1, T2) and, of course, a triac with R7 and C3 to suppress excessive voltage peaks. When the switch is closed the mains voltage is applied across the voltage divider consisting of R2 and R3. As long as the voltage at the junction of R2 and R3 does not exceed 0.7 V transistors Tl and T2 are switched off. In practice this means that neither T1 nor T2 will conduct when the voltage is within the range of about -8 V to +8 V. A 'window' is thus formed around the zerocrossing point of the mains. If the instantaneous mains voltage is greater than +8 V T2 will conduct, whereas when the mains value is more negative than -8 V Tl will conduct.

As soon as the mains voltage is applied to the circuit capacitor C2 is slowly charged via Cl, Rl and D2 up to a maximum of 10 V (defined by DI). After a few periods C2 will be charged enough to provide a trigger current for the triac. This is supplied via transistor T3, but only around the zero crossing point. At all other times T3 is kept off through the action of T1 or T2 (depending on the phase).

Summing this up: T1 . . . T3 ensure that the triac can only conduct at around the zerocrossing point. The gate pulses are delayed a few periods after switch-on (by C2 and everything to the night of it) to allow transistors T1 and T2 to get into their rhythm.

Version 2 of the circuit is slightly simpler than version I, at the price of a few concessions. The switching window is set up in the same way (with R3, R4, T1 and T2) but the supply for the gate of the triac is changed. In this case the gate current is supplied via C2 and R2. When on/off switch SI is closed the triac will never be inggered so the lamp remains off. When SI is open, on the other hand the triac can 1

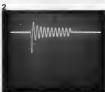


Figure 2. In contrast to the situation in hours 1. the lamp switched on vie the lemp eaver at about the maine zero-croselng point only has to handle a current five times as high so the maximum nominal value This hatving of the peak current corresponds to a reduction of the meximum power by a factor of four. be tnaggred but only within the window

Figure 1 When an incandescent lamp is switched on at the peak voltage level the peak current is about ten times as high as the maximum constant value in normal use The lamp in question here is an expensive spotlight with built-in reflector

lamp saver

Figure 3. Version 1 of the temp sever should be built into the light fitting The light ewitch, S1, then ewitches the whole cir-

cult on and off

defined by R3, R4, T1 and T2. In order for this circuit to work it is essential that the mains is always present and that the lamp. Lal, is connected in senes with it. If the circuit were connected straight across the mains and this just happened to be at its maximum value the triac would immediately be triggered via C2 and R2 even before T1 or T2 starts conducting. In that way the lamp would switch on when the mains voltage is at its maximum value, which is exactly what we wanted to prevent.

3

TIC 206D T1C 2250 1001 T3 = 8C 549C: BC 550C = TIC 2060 (4 A) T1C 225D (8 A)

A subtle difference between this version and the first one is that in this case T2 is a PNP transistor. This is necessary because the gate of the triac is fed an a.c. rather than d.c. signal. The gate current therefore alternates between negative and positive so a PNP transistor is needed to conduct the negative gate current.

The disadvantage of this version of the circuit is that is must always have a Ynains supply connected to it. Naturally this means that there will always be a certain amount of current consumed but in terms of the normal values of current we talk about for mains-powered equipment this is negligible.

Construction and installation

Building either version of the lamp saver is child's play using one of the printed circuit boards shown here. Which version is used depends on your own individual requirements. If the light in question is controlled by more than one switch then version I must be used. As we have already

said, this board is mounted into the actual light fitting. All the necessary constructional details can be gleaned from figure 5 and figure 6.

Space is saved by not mounting the triacs on a heatsink but this means that the maximum power that can be handled is a bit limited. This is dependent upon the way in which the board is mounted, particularly as regards the amount of cooling air that flows around the triac. The circuit can handle 300 W in any case, and this is quite sufficient for the vast majority of domestic applications. If, however, this is found to be insufficient the triac can be cooled by mounting it on an aluminium bracket. This will have to be made especially to suit the space available. Another possibility is to use a more powerful triac, 8 A instead of 4 A, in which case it may be necessary to reduce the value of resistor R5 to 330 Ω (in version 1). The triac is then always triggered by a positive gate current, irrespective of the phase. The TIC 225D's greater gate current

Figure 4. Version 2 of the circuit is slightly eimpler then version 1 Correct operation is ensured by connecting the circuit continuously to the maine. If space is at a premium e smell flow currenti 240 V switch may be used.

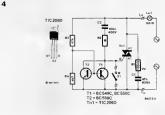


Figure 5. The usual care is required when fitting the components to the printed circuit board (version 1 is shown here! but no special precautions are needed

Parts list

- Version 1

R1. R3 = 4k7 R2 = 47 kR4, R6 = 10 k R5° = 470 Ω

R7 = 220 ♀ C1 = 100 n/400 V C2 = 22 µ/16 V C3 = 47 n/630 V

Semiconductors D1 - 10 V/1 W zener D2 = 1N4001

BC 550C Tri1 = triac, such as TIC 2060 (4.4) or TIC 225D (8 A)

T3 = BC 549C.

- see text







Parts list

- Version 2

esistors

R7 = 100 Ω/1 W R2 = 1 k R3 = 47 k

R4 = 4k7

Cepecitors C1 = 47 n/630 V

C2 = 100 n/400 V

T1, T2 = BC 559C, BC 560C Tn1 = inac, such as TiC206D i4 A) or TiC 225D (8 A)

Miscellaneous \$1° = switch

* = see text

Figure 6 As in version 1 of the lamp saves, the trise in version 2 is not mounted on a hastsink if the power handling capabilities are found to be too limited Tri1 may be mounted on an aluminum bracket

ь





requirement has the result that it is only tropered during positive half-yelles so the lamp will be seen to flicker. It is difficult to drive any spectfic advices in the side of the seen to flicker it is difficult to drive any spectfic advices depends on the individual application. An alternative to mounting version in the light fitting is to fit it into a small case. In this way the circuit can also be used with lamps that are plugged into a mane wall lamps that are plugged into a mane wall in the socket. For the crecuit to operate correctly in this application it is essential that the socket and the lamp saver.

Version 2 of the circuit is small enough to the behind the wall switch in most cases. If this is not so a smaller switch may be used as it only has to be able to hardle the fismall) gate current. Whatever switch to sued mist, however, be raided at 240 V. One last piece of advice: when installing this circuit be sure to remove the mains fuse beforehand and put it in your pocket. If you don't do this some 'helpful' person is bound to notice the fuse and helpfully put it back where it belongs. This same person is kinely to be quite shocked at your commends:

double-sided printed circuit boards Few electronic hobbyists today have any qualms about making printed circuit boards. Building a project on a printed circuit board is much easier than using Veroboard or something similar, wiring is kept to a minimum and fault finding is greatly simplified. The project also takes on a far more professional appearance. These plus points make it worth while to etch printed circuit boards. In general this is quite straightforward and, as the process is well known, we will not go into it here. Double-sided boards, on the other hand, are quite a different matter.

double-sided printed circuit boards

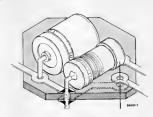
how to make them yourself Double-sided printed circuit boards are quite commonly used in electronics, particularly in HF, where one side acts as an earth plane, or in circuits where a large number of connections between various components have to be made in a reletively small area (such as computers). Making these boards is something hobbvists are happy to leave to professionals and, indeed, without the right facilities it can be very tricky. One of the difficulties with making double-sided printed circuit boards is that the copper tracks on both sides must be correctly aligned. This is dependent on the process used but in general terms what it involves is preparing one side of the board first, by developing it if photoetching is used, or applying transfers or etch resist. Then a few holes are drilled, the most common being the corner ones. The second side of the board cen then be prepared in the same way as the first and using the holes to facilitate alignment. Another possibility is to etch two printed circuit boards corresponding to the two sides of a double-sided design. These are

then drilled and strong glue is applied to the reverse side of each board. The boards are then carefully eligned with each other, by inserting plins in some of the holes, for instance, and stuck together. When the glue sets the board is ready for

So far this is nothing any hobbyst could not handle but after the board is etched Murphy strikes with a vengeance. Procressional doubles saded boards (like those supplied by Elektor through plated holes so that they connect the copper on both sides of the board. The equipment used to plate the holes is generally only found in businesses or schools so unless you have the right connections an alternative will have to be found.

The most simple alternative is shown in figure 1. This simply involves soldering the component leads on both sides of the board if there is a copper pad on both sides. Great care is required when doing this if the components are very hear sensitive as the soldering iron is closer than normal to the components and for

Figure 1. The essist way of connecting the two sides of the printed ofcuit board is by soldering the component leads at both sides



twice as long as usual. Use a heat shunt if possible.

Sometimes it may not be feasible to solder components on both sides or there may not be a component where the two sides of the board are to be linked. This brings us to figure 2a and 2b. Pins for insertion into printed circuit boards are available in two sizes, as figure 2a shows. These can be inserted into the board and fif

necessary) soldered at both sides to provide a more professional appearance than component leads (or off-cuts of same). An altogether better solution is indicated in figure 2b, in the form of through-PCB pins. As the drawing shows, these are simply inserted into the hole and soldered at both sides to provide a good connection. It is quite feasible to make double-sided printed circuit boards using the methods outlined, or, as is more likely, a combination of them, but in the case of complicated circuits they would almost certainly demand a redesign of the printed circuit board. There is, however, an alternative to all these methods that does not require any redesign. This is shown in figure 3. A hollow through PCB pin is inserted into the hole drilled for it and is soldered at both sides. In order to prevent the hole in the insert from filling with solder it is necessary to thread a length of enamelled copper wire (about 0.8 mm in diameter - SWG 20) through it during soldering. When this is removed the result has the same effect as a through plated hole. The insert is prevented from continually falling out of the hole during soldering by first widening its narrow end above the diameter of the hole with a nail or something similar. Unfortunately this wonder insert' has one disadvantage - it is not yet freely available in the U.K., as it is on the Continent, but we hope this

situation will soon improve.



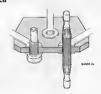


Figure 2a Pine ere svallable in two sizes to: inserting Into PCBs. A special insertion tool is nseded to do this.

double-sided

printed circuit boards



Figure 2b. Another way of connecting the two eldes of a board is to insert this sort of through-PCB pin and solder it at both

aidee



Figurs 3. The next best thing to through pletting the holes is this insert Using this it is possible to make valy good double sided printed cirquit boards without needing any special



applicator

2716 + 6116 = 48202

Memory IC type MK48Z02 from Mostek is compatible both as regards pinout and functions with the bytewide CMOS RAM 6116 as well as the type 2716 EPROM. This may appear to be a contradiction, but a look at the photograph (where you can see two fithium batteries) will make matters a little clearer; the 48Z02 is, in principle, nothing but a batterybuffered RAM. In contrast to earlier attempts at such a device, as for inetance the IPROM, the batteries as well as the voltage control are now contained in the same housing as the chip. Except for its height, which is slightly greater than normal because of the batteries, the housing is of the standard 24-way dual-in-line type. The 48Z02 can therefore be used as a direct replacement of a 2716 or e 6116 to offer the following advantages:

- high storage reliability due to the integrated, secure voltage
- switching;
 data retention in the absence of
- data security provided by eutomatic write protection during power failure:
- power failure;
 long data retention period
 (>10 years) due to HCMOS

(≥10 years) due to HCMOS technology. At the moment, because of its price

At the moment, because of its price of around £35, the 48202 is too expensive to be used as a simple programmable EPROM substitute, but there are applications where the price is justified. These will be discussed later in this article.

Technical characteristics

The pinout of the 48202 is shown in figure 1: except for pins 18, 20, and 21, it is in excordance with that of the 2716 or 6116. Pin 18 of the 2716 is designated $\overline{\rm CE}$ (Chip Enable); that of the 6116 is $\overline{\rm CS}$ (Chip Select), and in the 48202 it is $\overline{\rm E}$ [Enable). In prac-



Figure 1 Pinout of the MK48Z02



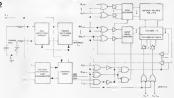


Figure 2. The functions of the RAM/EPROM can be seeily recognized here

lice, these differences er meaningless. In the same way, "forget" the
dissipnation G at pin 20 of the
48/202: this pin is the OE (Dutput Enable) terminal exactly es in the
2716 and 6116, Pin 21 of the 2716
carries the programming voltageafter programming that terminal
should be made logic 1. In the 6116
white Enable terminal fabbreviat of
Write Enable terminal fabbreviat of
World Fabreviat on the
Write Enable terminal fabbreviat of
before any data can be written into
the state of the control of the control of
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In the block diagram of figure 2, the typical structure of the actual memory with its memory matrix, row and column decoders, and so on, is easily recognized at the right. At the left are the voltage switching circuits and the lithium batteries. The comparator compares the voltage at pin 24 (Vcc) with the internally generated reference voltage. The normal supply voltage may lie between 4.75 V and 5.5 V (maximum). Below 4.75 V there are two further important levels: 4.5 V and 3.0 V. If the supply voltage drops below 4.5 V, the data bus will go into the high-ohmic state (three state) independent of the levels on terminals E (pin 18) or W (pin 21). This prevents the data in memory being affected by the on and off switching of the supply voltage. When Vcc drops below 3.0 V, it is

switched off and the lithium batteries provide the required power. When the external supply voltage lies between 4.5 v and 4.75 v, the lithium batteries are tissted. If the voltage of one of them is between 2.0 V, a flag is set. This flag inhibits voltage of one of them is between 2.0 V, a flag is set. This flag inhibits voltage has been eventhed on. It is therefore easy to ascertial by means of a software loop whether the lithium batteries are in good working order:

■ reed the contents 'N' of an arbitrary memory location 'X' and write these in a different position in the system;

- load a value different from 'N' into location 'X';
- check that the new value is stored correctly;
- reload 'N' into 'X'.

 This routine must, of course, be carried out as a first write operation when the 48/02 is used after a

power-on reset.

The timing diagrams of figures 3 and 4, as well as table 1, contain the detailed operating conditions of the 48202. The write cycle time, t_{WC}, in the 48202 is equal to the access time (read-cycle time), t_{RC}. The device is available with a read/write cycle time of 150 ns, or 200 ns, or 250 ns, which is shown in a suffix to the type number: 15, 20, or 25. For instance, an Mk4202-20 is the version



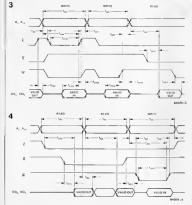


Figure 3, 4. These timing diagrams show the detailed operation of the MK48202.

Table 1 Electrical characteristics of the three versions of the MK48Z02 (0 °C \leqslant $T_A \leqslant$ 79°C) (VCC = 5.0 V + 10% -5%)

	MK 48202-15		SZ02-15	MK48Z02-20		MK48202 25		
Sym	Perende	Min	Max	Men	Max	Min	Max	Units
IRC	Read Cycle Time	150	-	200		250		ns
IAA	Address Access Time		150		200		250	ns
ICEA	Chip Enable Access Time		150		200		250	ns
_!CEZ	Chip Enable Data Off Time		35		40		50	ns
IGEA	Output Enable Access Time		55		65		75	ns
IOEZ	Gutput Enable Data Off Time		35		40		50	ns
юн	Output Hold from Address Change	15		15		16		ns
IWC	Write Cycle Time	150		200	_	250		ns
IAS	Address Setup Time	0		0	-	0	_	ns
ICEW	Chip Enable to End of Write	90	_	120		160		ns
LAW	Address Valid to End of Write	120		140		180	_	ns
IWD	Write Pulse Width	90		120		160		ns
IWB	Write Recovery Time	10		10		10		PIS
IV/EZ	Write Enable Data Off Time		50		60		80	ns
IDSW	Data Satup Time	40	T î	60		100		ns
IDHW	Deta Hold Time	0		0		0		ns

with a 200 ns access time. Power consumption amounts to about 250 mW when the chip is being accessed $i\bar{E}=0$), and around 5.5 mW on standby $i\bar{E}=1$). The standby power consumption is not the same as the drain on the batteries!

All in all, the 48202 is a useful, practical building block: it may be described as a RAM and used as an EPROM or ROM. It may be inserted into the available EPROM socket, as pin 21 of this is always logic 1. And in contrast to EAROMs, the 48202 may be loaded as often as you wish

and at the normal speed of the system.

Some applications

As we said before, the 48Z02 is too expensive to be used as an EPROM replacement. It makes good economic sense, however, to use it in applications where the contents of a ROM are required to be amended frequently and/or rapidly, and more particularly so where such amendments result in circuit changes. Typical applications are as the digital store of large numbers of tuner frequencies, and as memory for a control computer the working program of which is changed regularly by a central processing system (if the program is not too long, the 48Z02 may at the same time take over the duties of the RAM). The 48Z02 should also be of interest

to computer families, for instance, to alter a monitor program. You then copy the EPROM content into the 48202, amend and test the program as often as required, and once you are satisfied with the modified program, copy the content of the 48202 in one go into an EPROM. If you are the same that t

Another possibility is the modification vis the software of the content of a code reversal memory or character generator during operation of the system (the hardware must, of course, be sustable for this). It with the properties of the sustable of the stanisher graphic character accords from e dislate to a character generator, or if required, to provide a generator, or if required, to provide a designations for intendant lays (for instance, BASIC shorthand commands, followed by Pascel or Forth shorthand instructions, or capables

call-in, and so on).

Literature
2K ×8 Zeropower™
RAM MK48202(B)—15/20/25

United Kingdom: Mostek UK Ltd Masons House 1-3 Valley Drive Kingsbury Road LONDON NW9 Phone: 01 204 9322

International:

United Technologies Mostek 1215 W. Crosby Road Carrollton Texas 75006

Phone: 214/466-6000 elektor index october 1984 1 0 .55

telephone amplifier

Modern technology has produced fast trensport end centralised industry. A somewhat less desirable side effect is that close relations have tended to become distant reletions. Instead of gathering around the fire as in the 'good old deys', we tend to gather eround the telephone.

This means of communication suffers, however, from one major flaw: Me Bell never intended it es e vital link between whole femilies. The system itself and all the legal restrictions involved with it are geared to private

conversations between two individuels. The solution to the problem? A loudspeaking telephone.

The circuit described here will pick up the telephone conversation and reproduce it via a loudspeaker, so that several people can list en in

This is only possible, of course, if the electrical signals from the telephone are first picked up in some way. Since the Post Office, understandably, does not like people tampering with their winns. some kind of indirect coupling is required. The most common method is to use a so-called telephone pick-up coil This operates on a very simple principle: in every telephone there is a transformer which is wound and wired in a cunning way in order to route the incoming signal from the telephone line to the earpiece, and at the same time feed the microphone signal onto the line. In effect, it forms a kind of splitter for audio signels, with good coupling from line to earpsece and from microphone to line, but with poor coupling between the microphone and earpiece to avoid acoustic feedback.

All transformers have a stray field, and this one is no exception. If a suitable coil is placed in this field, it will 'pick up' the audio signals. Logically enough. a device of this kind is called a pick-up coil. The electrical signal delivered by the coil is extremely small, so that a lot of gain is required in the following amplifier stages. As shown in the block (figure 1), the amplifier described here consists of two sections. The first section has a gain of 180 (45 dB). It can be connected via almost any length of single-core screened cable

to the second section, which has a gain of up to 50 (34 dB). This second stage drives the loudspeaker.

The advantage of cutting the circuit in two is that the first stage can be mounted quite near to the pick-up coil, minimising the amount of hum and interference picked up by the connecting

wires. The bulk of the circuit, including loudspeaker and power supply, can be mounted at any suitable remote pos-Up to 50 m (160 ft) of screened cable

can be used between the two stations more than enough for any practical application we can imagine. The first section has no power supply of its own: it is powered from the main section via the connecting cable.

The circuit

The complete circuit is shown in figure 2: figure 2a is the first stage, which is mounted near the pick-up coil; figures 2b and 2c are the second stage and the power supply, respectively. The pick-up coil, L1, is a normal minia-

ture choke and the value is not particularly critical. It is sometimes possible to obtain coils designed specifically for this purpose, mounted in a plastic capsule with a suction cup at one end. I.1 and C1 together form a resonant circuit, but this is so heavily damped by R1 and the input impedance of T1 that the resonant peak is hardly noticeable - the main effect is to limit the bandwidth to a useful value

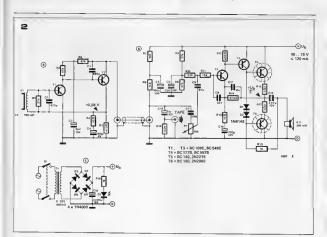
The first stage would be a two-transistor

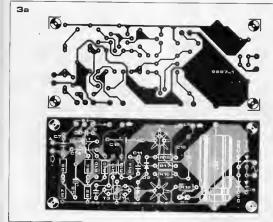
lines is not permitted. The booster itself consists of two sections, one mounted as close as possible to the phone end the other - much lerger - section placed in any con-

since a direct connection into the telephone venient spot.

Figure 1. Block diagram of the telephone

booster. The signal is picked up by a coil.









Parts live

Resistors:

R1 = 22 k R2.R3.R9 = 47 k R4 R13 = 10 Ω R5, R12, R14 = 470 Ω R6.R7 = 1k8 R8 = 27 k R10 = 33 k

R11 = 15 k $B15 = 1 k\Omega$ R16 R17 = 202 R18 = 1 k

R19 = 100 k

P1 = 10 k log.

C2,C6 = 2x2/10 V C3 = 680 pC4 = 100 µ/4 V C5 = 100 µ/10 V C7 = 270 n C8 = 82 n C9 = 22 nC10,C12 = 220 µ/10 V C11 = 10 n C13 = 2200 u/16 V

C14 = 1 µ/10 V

Capacitors:

C1 = 470 p

T1,T2,T3 = 8C 109C, 8C 549C or sou T4 = BC 1778, 8C 5778 or equ. T5 = 8C 140, 2N2219

T6 = BC 160, 2N2905 D1,D2 = 1N4148 D3 . . . D6 = 1N4001

D7 = LED

Semiconductors

Miscellaneous: L1 = ministure choke. 47 100 mH see 18x1 LS = 8 \,\Omega/200 mW loudspeaker Tr = 9 . . . 12 V/150 mA mains trensformer S1 = DPDT mains switch

amplifier with a gain of 180, if T2 had a 1k8 collector resistor. Following the connecting cable, this resistor can indeed be located: R6 in figure 2b. This little trick, which was also used in the Preco, saves one wire: the same cable is used to feed the audio signal from the first section to the second and to supply power from the second section to the first. The output of the first section is basically a current source and can be loaded by a relatively low impedance, permitting the use of a fairly long cable. The second section is a 'bare-bones' design: only four transistors and a handful of other components are used in this little power amphfier. There is no quiescent current adjustment - that would be an unneccessary luxury for this application. On the other hand, no quiescent current at all would be the other extreme - the maximum gain would be lower. Pl is the volume control. A tape output is also provided, although it should be noted that strictly speaking - the other party should be notified if the conversation is to be

The power supply (figure 2c) is straightforward. The only 'luxury' there is the LED, D7.

sections are shown in figure 3. The main

Construction and use Printed circuit board designs for the two

(figure 3h) contains both the second

section and the power supply. It is perhaps interesting to note that this board can also be used on its own as a low-cost, low-fi 'power' amplifier, pro-vided R6, R7 and C5 are omitted. For that matter, the complete unit can also be used as a 'low-fi' public address installation.

Note that T5 and T6 should be provided with cooling fins or clips. There's no harm in them running 'warm', but they're not supposed to get 'hot'.

The two sections can each be mounted in their own case (even a tobacco tin will do for the first stage!) and connected by means of the desired length of cable. The pick-up coil should be connected to the first stage by the shortest possible length of twin-core screened cable: the two ends of the coil are connected to the two cores and the screening is connected to supply common

The best position for the pick-up coil can be found by trial and error. When the handset is lifted off the hook, a dialling tone is obtained (if no dialling tone is heard, complain to the Post Office, not us) and the pick-up coil can now be moved, twisted and turned all over the telephone (not the handset) until this tone is reproduced at maximum strength by the loudspeaker. Note that both the position of the coil and the direction in which it is pointing will influence the 'reception'. Once the best position and location have been found, the coil can be fixed in position.

Figure 2. The complete circuit, Figure 2s is the first section, which is connected by means of single-core screened cable to the second section, shown in figure 2b. The pow supply, figure 2c, can also be mounted in the main station.

Figure 3. The two p.c. boards required, The terger of the two (figure 3e), EPS 9987 - 1, is fer the main station including power supply; the second is for the first section of the circuit (EPS 9987 - 2).

BATTERY CHARGER

A solid state, thyristorised charger with saveral in-built satety measures has been put on the market by Advance Industries under the trade name "Globe" battery charger The manulacturers claim the charger to be toolproof against undervollage and overvoltage, short circuit and reverse battery connection The automatic battery chargers are available in different models from 6V to 36V



More datails can be obtained from Advance Industries, Tinwela Building, Tribhuvan Road, Bombsy-400 004

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Switchas in international design, called "Intel" series, especially for electronic applications, here been introduced by Indian Engineering Company The series ranga consists of miniature toggle switches rated for 3 amps. 250V A.C. or 28V D.C. Available in four types with push buttons



For lumber details contact. Indian Engineering Company, 132A, Dr. Annie Besant Road. Post Box no. 16551, Worll Naka, Bombay-400 018



write for Jost's Engineering Company. 60, Sr P M Road, Bombay-400 001.

DIGITAL NUMERAL PRINTER IRA have introduced Digiprint-18,

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THUMBWHEEL SWITCHES

"Comtach T-21" series for miniature Ihumbwhaal switches have been dayaloped by M/S Component Tachnique tor a variety of epplications such as preset devices in digital systems, as divider natwork switches or selector switches. A number of basic switch modules can be stecked together Tha thumbwheel is marked prominently with while legend on bleck background



For further details, confact: Component Technique, 8, Orion, L.P. Road, Andhen west, Bombay-400 058

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tions are available: DC volts, AC volts, DC amps, AC amps, RC amps, relistance, and diode choice — a total of 21 messus ement sangss. Protection against accidental overlead is built in. The instrument can work up to 2000 hous from a standard PSI battery. It is available from stock at £ 24 95 + VAT.

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tection is obtained by the incorporation of a high performance mains filter with a varietor transient clippei Dimensions are 425 × 78 × 45 mm, and the unit weights 1.5 kg. Unit price is ES4.34 excluding VAT, cer-

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menual mode. Decibel measurements are also provided. Readings are presented on an LCD deplay, along with an analogue mater for convenance when making peak measurements. The voltmetts can be hired from Microleage for pariods of a week upwards; the weekly here rate is from C38 50.

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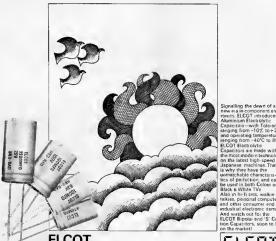
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advertisers index

miszing

capacitence meter

read

(Merch 1964, page 3-42) Some unfortunate arrore crept into the above article. The first two paragraphs unday "construction" should

First of all, mount and solder all components except C10 end Rf2, where pins should be fitted Next, fit all com ponents (but were links instand of insistors Rf and R7) to the display board shown in figure 7. The display and LEDs must be located on the track side; the LEDs should be soldered as close to the board as possible to ansure that they are flush with the display. Lastly, fit were bridge B

Additionally ■ Note that neither wire

- bridge A nor input Z shown in figure 5 is used It is advisable to lit Cf5
- and IC8 on the track side of the metating board to ensure that the switch shaft of the range selector projects from the front panel on lenal assembly
- fit is recommended that a type 1N4148 diode be connected in series with each of the LEDs D4 O7 to prevent possible melfunctioning of ES1...ES3 through leakage currents of the LEDs.
- ft happens from time to time that when a canacitor is measured with \$1 set to too high a range the display indicates odd values. This may be prevented by solder ing a 100 ohm resistor be tween pin 6 of fC2 and the +Cx terminal
- Rf. R7, D1, and D3 are not used and should therefore be delered from the relevant circuit diagram and parts list.

deisywheel typewriter printer interfece

July 1984, page 7 - 32-There is an error in Table 2 of this article. The "Y219AHax" should read "Y519AHax".

universal active filter

(February 1984, paga 2-36) In the circuit diagram of figure 1 we have somehow menaged to confuse a number of pins in ICS. Pins 4, 5 and 6 should be connected to the +10 V line whereas pins 8, 9 and 12 should be connected to ground.

frequency meter (August/September 1984. page # - 38

The circuit diagram on page 7-44 indicates IC4 as 4515 This should, in fact, be 4518 as the parts list conectly states

meximum and minimum memory

ZODIAC10.08

(July 1984, paga 7 - 37 Offset tolerances in the opamps used can result in the memory renge of 0 to 1 volt not operating correctly. This is noticed when the input is 0 V as the output voltage in the minimum position dignal from 1 V (after a reset, for example) to almost 0 V and than jumps to 1 V only to fall towards 0 V again, and so it goes on Just the opposit heppens for the 'maximum' voltage.

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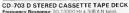
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Frequency Response: 30-12000 Hz (LN tape), 30-16000 tape) Signel to Noise Retio: Better than 45dB Wow & Flutter: Less than 0.2% WRMS Heads (2): One High Oensity Super Permalloy REC/PB One Erase



Frequency Response: 30-13000 Hz ± 3dB (LN tape), 30-16000 Hz ± 3dB (CrO₂) Signel to Noise Retio: Better han -54dB (LN tape), Better than -56dB (cr0₂ tape) NB Dolby Switch ON Improves upto 10 dB above 5 KHz Wow & Flutter: Less than 0.08% WBMS Heade (2): One Glass Surface Ferrite REC/PB One Erase





CD-360 STERED CASSETTE TAPE RECORDER WITH BUILT (N AMPLIFIER

Frequency Response: 30-12000 Hz (LN tape), 30-16000 Hz (CrO₂ tape) Signal to Noise Retio: Better than -40dB Wow & Flutter Less than 0.2% WRMS Heede (2): One High Density Super Hard Permalloy REC/PB One Erase

CD-5100 D STERED CASSETTE TAPE DECK

Frequency Response: 30-13000 Hz ± 3dB (LN tape) 30-16000 Hz ± 3dB (CrO₂ tape) Signal to Noise Ratio: Better than 60dB (JIS A) (LN tape), Better than 63dB (Cr KHz Wow & Flutter: Less than 0.06% WRMS Heade (2): One High Density Super Hard Permalloy REC/PB One





HS 410 DD DIRECT DRIVE STEREO TURNTABLE

Pletter 30cms diameter with Built-in Stroboscope for 50 Hz and 60Hz Motor: Servo controlled – Direct Drive Wow & Flutter: Below 0.04% RMS JIS C5521 Certridge: Magnetic with diamond stylus

STUDIO STANDARD SERIES ... **K8 STERED CASSETTE TAPE DECK** Frequency Response. Metal tape - 20Hz to 20KHz (30Hz

Frequency Response. Metal tape: 20Hz to 20KHz (30Hz to 1 X Hz 3 3B) Cr02 tape: 20Hz to 18KHz (30Hz to 16KHz, 3 3B) LH tape: 20Hz to 15 Khz (30Hz to 16 Khz, 3 3B). Signat to Noise Ratio: Metal Tape: 5 better than 5-56B (NR 0FE), better than 5-56B (NR 0FE), better than 6-56B (NR 0FE), better than 6-56B (NR 0FE). On) Wow & Flutter: Less than 0.055% WRMS Heads (2): One Sendust REC/PB. One double gap erase





CD-GRAM 4000 DD MK II DIRECT DRIVE STERED TURNTABLE

Platter: 30cms diameter with Built-in Stroboscope Motor: Servo Controlled - Direct Onve Wow & Flutter: Below 0 07% RMS JIS C5521 Certridge: Magnetic, with diamond

CD-GRAM 2000 BD BELT DRIVE STERED TURNTABLE

Pletter: 2Bcms diameter, Aluminium alloy die-cast Motor: 4 pole synchronous motor - Belt Onve Wow & Flutter: 0 15% WRMS Cartridge: Magnetic with diamond stylus

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